



NANDHA ENGINEERING COLLEGE

(An Autonomous Institution affiliated to Anna University Chennai and
Approved by AICTE, New Delhi)

Pitchandampalayam, Erode To Perundurai Road, Erode-638 052

BoS Meeting Minutes

The meeting of the Board of studies is held as given below:

Board	Electrical and Electronics Engineering	Meeting No.	2	R2013
Venue	Board Room, NEC	Date & Time	1 st Feb 2014, 10.00 AM	
List of members attended		The details with signatures are given in Annexure - I		

The Agenda discussed is as follows:

S. No.	ITEMS
01.	Preparation of Syllabi for UG Course (III & IV Semester).
02.	Approval of List of examiners for question paper setting and valuation.
03.	Other related matters.

The minutes of the meeting are as follows:

Dr.A.Satheesh, the Chairman welcomed the gathering and introduced the external members. The board considered various items in the agenda and the deliberations are detailed below.

Item-01.	Preparation of Syllabi for UG course based on curriculum as approved by First BoS Meeting.
Details	The syllabi for UG courses (III & IV Semester) are framed after enough deliberations.
Resolution	It is resolved to submit the curriculum and syllabi (III & IV Semester) to the Academic council for approval.

Item-02.	Approval of panel of examiners for question paper setters and valuation.
Details	The chairman presented the list of examiners for question paper setting, practical examinations and valuation.
Resolution	It is resolved to submit the panel of examiners to the academic council for approval.



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Item-03.	Other related matters.
Details	<ol style="list-style-type: none"><li data-bbox="363 432 1214 510">1. The members suggested to add IGCT & its applications in Semiconductor Devices and Circuits.<li data-bbox="363 544 1374 689">2. The members suggested to include Switching Functions and Simplification Using K-Maps & Quine Mccluskey method and Specify PLA, PAL-Version in Digital Logic System Design.<li data-bbox="363 701 1358 824">3. The members suggested to add Study of Opto Coupler and Study of Class B (Or) Push Pull Amplifier in Semiconductor Devices and Circuits Laboratory.<li data-bbox="363 846 1353 936">4. The members suggested to add Regulation of Three Phase Salient Pole Alternator by Slip Test in Machines Laboratory.

Prof.M.Balachandran proposed vote of thanks to the Chairman and the members of the board and cordially invited them for the next meeting.

[Handwritten signature]

Date: 01.02.2014