

**NANDHA ENGINEERING COLLEGE, ERODE – 638 052**  
**(An Autonomous Institution, Affiliated to Anna University Chennai and**  
**Approved by AICTE New Delhi)**

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**MINUTES OF THE 6<sup>th</sup> BOARD OF STUDIES MEETING**

<b>Name of the Body</b>	Board of Studies
<b>Name of the Board</b>	Electrical and Electronics Engineering
<b>Meeting No.</b>	06
<b>Date &amp; Time</b>	03.05.2018, 10.00 am
<b>Venue</b>	Board Room-NEC Nandha Engineering College (Autonomous) Erode - 638 052

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**Minutes of 6<sup>th</sup> Board of Studies Meeting (BoS) held on 03.05.2018**

The 6<sup>th</sup> Board of Studies (BoS) meeting was held on 03.05.2018 by 10.00 am at Board Room Nandha Engineering College, Erode - 52. The members attended the meeting are given in Annexure I.

The Chairman of the BoS, Dr. A.Satheesh, Professor & Dean, Electrical and Electronics Engineering welcomed the members for the meeting. Then, the items listed below were taken for discussion.

**AGENDA**

- |           |  |
|-----------|--|
| Item 6.01 | Approval of Curriculum & Syllabi for 7 <sup>th</sup> and 8 <sup>th</sup> semesters under Regulation R15 for the batch of students admitted in B.E., Electrical and Electronics Engineering programme from the year 2015 - 16 onwards |
| Item 6.02 | Approval of Curriculum & Syllabi of 3rd to 8th semesters under Regulation R17 for the batch of students admitted in B.E., Electrical and Electronics Engineering programme from the year 2017 - 18 onwards                           |
| Item 6.03 | Any other matters  |

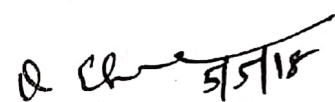
The proceedings of BoS started and the minutes of the meeting are recorded as follows:

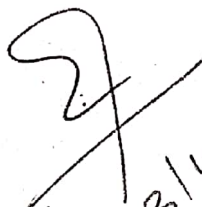
Item 6.01	Approval of Curriculum & Syllabi for 7 <sup>th</sup> and 8 <sup>th</sup> semesters (R15) semesters under Regulation R15 for the batch of students admitted in B.E., Electrical and Electronics Engineering programme from the year 2015 - 16 onwards	
Discussion		✓ Defined
Resolution	Resolved to approve the curriculum and Syllabi under Regulation R15 for 7th and 8th Semesters of B.E., Electrical and Electronics Engineering for the batch of students admitted from the academic year 2015-16 onwards.	
Item 6.02	Approval of Curriculum & Syllabi of 3rd to 8th semesters under Regulation R17 for the batch of students admitted in B.E., Electrical and Electronics Engineering programme from the year 2017 - 18 onwards	
Discussion	<b>3<sup>rd</sup> Semester:</b>	
	<b><u>Electronic Devices and Circuits</u></b>	
	• Dr.R.Latha suggested include schottky diode and special diodes in Unit-I.	✓ Included
	• Dr.N.Rajasekar suggested to add UJT characteristics in Unit -II	✓ Added
	<b><u>Electrical Machines I</u></b>	
	• Dr.N.Rajasekar suggested to add applications related to generators, Motors and transformers.	✓ Added
	<b><u>Electrical Machines I Laboratory</u></b>	
	• Dr.V.Natarajan suggested to add auto transformer as study experiment.	✓ Included
	<b>4<sup>th</sup> Semester:</b>	
	<b><u>Electrical Machines II</u></b>	
• Dr.N.Rajasekar suggested to add applications related to synchronous and induction machines.	✓ Added	
• Dr.N.Rajasekar suggested to include the book Timothy J E Miller as reference.	✓ Included	
<b><u>Linear and Digital Integrated Circuits:</u></b>		
• Dr.Moorthi and Dr.R.Latha are suggested to separate the above subject in to Linear Integrated Circuits and Digital logic system design.	✓ Modified	
<b><u>Transmission and Distribution:</u></b>		
• Dr.V.Natarajan suggested to change the AC and DC distributions topics to Unit 1 and also adding the grid concept.	✓ Changed	
<b>5<sup>th</sup> Semester:</b>		
<b><u>Measurements and Instrumentations:</u></b>		
• Dr.Moorthi suggested removing the X-Y recorders and adding the DSO topic.	✓ Modified	

	<ul style="list-style-type: none"> <li>• Dr.R.Latha suggested to add digital electronics measurements.</li> </ul>	✓ Added
	<b><u>Control Systems:</u></b>	
	<ul style="list-style-type: none"> <li>• Dr.R.Latha suggested to remove the state space model topic from Unit IV.</li> </ul>	✓ Removed
	<b><u>Power Electronics:</u></b>	
	<ul style="list-style-type: none"> <li>• Dr.N.Rajasekar suggested to add digital drivers, snubbers and isolation circuits in Unit I, add SMPS in Unit II.</li> </ul>	✓ Added
	<b><u>Power Electronics Laboratory:</u></b>	
	<ul style="list-style-type: none"> <li>• Dr.N.Rajasekar suggested to add pulse generation using bread board connections.</li> </ul>	✓ Considered
	<b><u>6<sup>th</sup> Semester:</u></b>	
	<b><u>Micro processors and Micro controllers:</u></b>	
	<ul style="list-style-type: none"> <li>• Dr.Moorthi and Dr.R.Latha are suggested to starts with 8086 and add advanced like PIC micro controller.</li> </ul>	✓ Considered
	<b><u>7<sup>th</sup> Semester:</u></b>	
	<b><u>Protection and Switch Gear:</u></b>	
	<ul style="list-style-type: none"> <li>• Dr.N.Rajasekar suggested to add DC protection and digital protection..</li> </ul>	✓ Added
	<ul style="list-style-type: none"> <li>• Dr.Moorthi suggested to add digital relays in Unit III.</li> </ul>	✓ Added
	<b><u>Electric Drives and Controls:</u></b>	
	<ul style="list-style-type: none"> <li>• Dr.N.Rajasekar suggested to add power quality issues.</li> </ul>	✓ Added
Item 6.03	Any other matters Nil	

Dr.A.Satheesh, Dean/EEE thanked all the members for their active participation.

Date: 05.05.2018

  
 Dr. A.SATHEESH  
 (Chairman, BoS & Dean /EEE)

  
 23/5/18