

NANDHA ENGINEERING COLLEGE, ERODE – 638 052
(An Autonomous Institution, Affiliated to Anna University Chennai and
Approved by AICTE New Delhi)

DEPARTMENT OF BIOMEDICAL ENGINEERING

NEC/BME/CIR/BoS-5/2022

Date: 27.07.2022

CIRCULAR

Originator: BoS Chairman (HoD – Biomedical Engineering)	Circulated to: All faculty members
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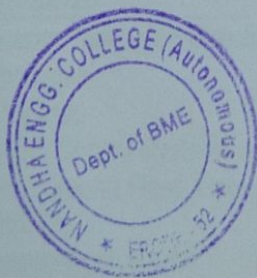
Sub: BoS Meeting

The 5th BoS meeting has been scheduled on 04.08.2022 (Thursday).

In this connection, BoS members of the Biomedical Engineering Program are requested to attend the meeting and provide their valuable suggestions.

Date & Time of Meeting: 04.08.2022 (2.00 PM) - Thursday

VENUE: Board Room, Block 3.



P. Sukumar

Dr. P. Sukumar
CHAIRMAN, BoS/BME



NANDHA ENGINEERING COLLEGE
(Autonomous Institution)
Pitchandampalayam, Erode To Perundurai Road, Erode-638 052

BOARD OF STUDIES

Academic Year: 2022-2023

Board	Biomedical Engineering	Meeting No.	5	R2022
Venue	Board Room, Block 3 – NEC	Date & Time	04.08.2022 & 2.00 p.m	

LIST OF MEMBERS

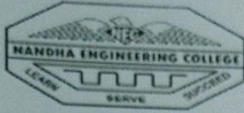
Sl.No	Members	Representation
1	Dr. K. Hariharan, Professor, Department of Electronics and Communication Engineering, Thiyagarajar College of Engineering, Madurai – 625015. Ph: 9942584251 Email: khh@tce.edu	University Nominee
2	Dr.P.Palanisamy, Professor, Dept. of Electronics and Communication Engineering, National Institute of technology, Trichirappalli-15 Email: palan@nitt.edu Mobile No: 9486001111	Expert Nominee (Nominated by Academic Council)
3	Dr. S. Thomas George, Professor & Head, Department of Biomedical Engineering, Karunya Institute of Technology & Science (Deemed University), Coimbatore Email: thomasgeorge@karunya.edu Mobile No: 9443292426	Expert Nominee (Nominated by Academic Council)
4	Mr. R. Kodeeswaran, Managing Director, Mechsoft Technologies, Kaar Infotech Pvt. Ltd., True Engineering & Control Systems, Amman Complex, Ground Floor, Opp. New Bus stand, Perundurai - 638052. Email: kodeeswaran@gmail.com Ph: 7010047887	Member (Expert from Industry)



NANDHA ENGINEERING COLLEGE
(Autonomous Institution)
Pitchandampalayam, Erode To Perundurai Road, Erode-638 052

BOARD OF STUDIES

5	Dr. A. Karthika, Assistant Professor, Department of Electronics and Communication Engineering, SNS College of Technology, Coimbatore Email: akarthikakdm@gmail.com Ph:8344336576	Alumni
6	Dr. P.Sukumar, Professor & Head/ BME	Chairman
7	Dr. M. Dhipa, ASP/ BME	Member
8	Ms. E. Madura, AP/ BME	Member
9	Ms. B. Sowparnika, AP/ BME	Member



HOD BME <hodbme@nandhaengg.org>

NEC - BME - BOS Invitation - Reg

1 message

HOD BME <hodbme@nandhaengg.org>
To: khh@tce.edu

Wed, Aug 3, 2022 at 9:46 AM


Sir,

Warm Greetings from Nandha Engineering College (Department of BME). We are happy to inform that you have been appointed as University Nominee for Board of Studies (BoS) to discuss new regulation and curriculum (R22). The meeting will be held on Thursday (04th August 2022 at 2.00 P.M.) in Board Room, Block 3.

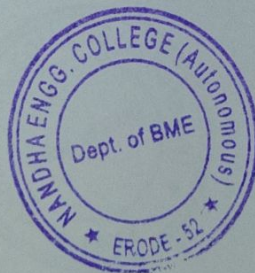
We request you to kindly make it convenient to attend the meeting and offer your valuable guidance and suggestions to improve the academic standards of Department of Biomedical Engineering. A confirmation of your participation through e-mail (hodbme@nandhaengg.org or dhipa@nandhaengg.org) as soon as possible would be appreciated.

Thanking you

With Best Regards,
Dr P Sukumar

 University Nominee BoS Invitation.pdf
724K

P. Sukumar





HOD BME <hodbme@nandhaengg.org>

NEC - BME - BOS Invitation - Reg

1 message

HOD BME <hodbme@nandhaengg.org>
To: palan@nitt.edu

Wed, Aug 3, 2022 at 9:43 AM


Sir,

Warm Greetings from Nandha Engineering College (Department of BME). We are happy to inform that you have been appointed as Academic Expert for Board of Studies (BoS) to discuss new regulation and curriculum (R22). The meeting will be held on Thursday (04th August 2022 at 2.00 P.M.) in Board Room, Block 3.

We request you to kindly make it convenient to attend the meeting and offer your valuable guidance and suggestions to improve the academic standards of Department of Biomedical Engineering. A confirmation of your participation through e-mail (hodbme@nandhaengg.org or dhipa@nandhaengg.org) as soon as possible would be appreciated.

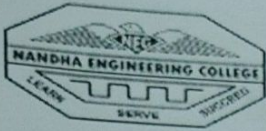
Thanking you

With Best Regards,
Dr P Sukumar

 Academic Expert 1 BoS invitation.pdf
724K

P. Sukumar





HOD BME <hodbme@nandhaengg.org>

NEC-BME-BOS Invitation - regarding

1 message

HOD BME <hodbme@nandhaengg.org>
To: thomasgeorge@karunya.edu

Fri, Jul 29, 2022 at 2:01 PM

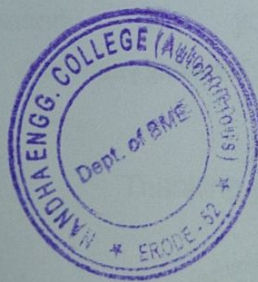
Sir,

Warm Greetings from Nandha Engineering College (Department of BME). We are happy to inform you that you have been appointed as an academic expert member for Board of Studies (BOS) to discuss new regulation and curriculum (R22). The meeting will be held on Thursday (04th August 2022 at 2.00 p.m) in the Board Room, Block 3.

We request you to kindly make it convenient to attend the meeting and offer your valuable guidance and suggestions to improve the academic standards of the Department of Biomedical Engineering. A confirmation of your participation through e-mail (hodbme@nandhaengg.org or dhipa@nandhaengg.org) as soon as possible would be appreciated.

Thanking you

With regards,
Dr P Sukumar
Head of the Department
Department of Bio Medical Engineering
Nandha Engineering College (Autonomous)
Erode, Tamilnadu - 638052

Academic Expert BoS Invitation pdf.pdf
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NANDHA ENGINEERING COLLEGE

(AUTONOMOUS)

(Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai)

DEPARTMENT OF BIOMEDICAL ENGINEERING

ERODE – 638 052 TAMILNADU

Email : hodbme@nandhaengg.org

Mobile : 9842119035

Dr.P.Sukumar B.E, M.E, Ph.D
Professor & Head

NEC/BME/BoS/Invitation/01
Date & Time : 28.07.2022, 04.00 PM

To,

Dr. S. Thomas George,
Professor & Head,
Department of Biomedical Instrumentation Engineering,
Karunya Institute of Technology and Sciences,
Coimbatore.

Sir,

Warm Greetings from Nandha Engineering College (Department of BME). We are happy to inform that you have been appointed as academic expert member for Board of Studies (BoS) to discuss new regulation and curriculum (R22). The meeting will be held on Thursday (04th August 2022 at 2.00 P.M.) in Board Room, Block 3.

We request you to kindly make it convenient to attend the meeting and offer your valuable guidance and suggestions to improve the academic standards of Department of Biomedical Engineering. A confirmation of your participation through e-mail (hodbme@nandhaengg.org or dhipa@nandhaengg.org) as soon as possible would be appreciated.

Thanking you

Signature,

(Dr P Sukumar)



Dhipa Chandrasekar <dhipa@nandhaengg.org>

Re: NEC-BME-BOS Invitation - regarding

1 message

Dr. S. THOMAS GEORGE. 137 <thomasgeorge@karunya.edu>
To: HOD BME <hodbme@nandhaengg.org>, dhipa@nandhaengg.org

Tue, Aug 2, 2022 at 12:19 PM

Dear Sir,

I hereby confirm my participation for the BoS. Thank you for the opportunity. Thank you again. Regards.

Dr. S. Thomas George
Professor and Head,
Biomedical Engineering &
Coordinator, Karunya - IPR Cell,
Karunya Institute of Technology and Sciences (Deemed-to-be University)
Coimbatore-641 114.

Official: (+91) 422 261 4394
Personal: (+91) 9443292426

Official: thomasgeorge@karunya.edu
Personal: s_thomasgeorge@rediffmail.com
Scopus ID: 57211650041
Web of Science Researcher ID : F-1931-2018

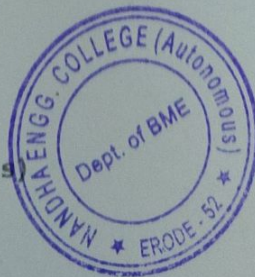
On Fri, Jul 29, 2022 at 2:01 PM HOD BME <hodbme@nandhaengg.org> wrote:
Sir,

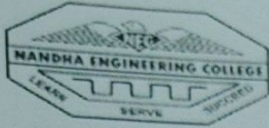
Warm Greetings from Nandha Engineering College (Department of BME). We are happy to inform you that you have been appointed as an academic expert member for Board of Studies (BOS) to discuss new regulation and curriculum(R22). The meeting will be held on Thursday (04th August 2022 at 2.00 p.m) in the Board Room, Block 3.

We request you to kindly make it convenient to attend the meeting and offer your valuable guidance and suggestions to improve the academic standards of the Department of Biomedical Engineering. A confirmation of your participation through e-mail (hodbme@nandhaengg.org or dhipa@nandhaengg.org) as soon as possible would be appreciated.

Thanking you

With regards,
Dr P Sukumar
Head of the Department
Department of Bio Medical Engineering
Nandha Engineering College (Autonomous)
Erode, Tamilnadu - 638052





HOD BME <hodbme@nandhaengg.org>

BOS Invitation - reg.

1 message

HOD BME <hodbme@nandhaengg.org>
To: kodeeswaran@gmail.com

Fri, Jul 29, 2022 at 1:54 PM


Sir,

Warm Greetings from Nandha Engineering College (Department of BME). We are happy to inform you that you have been appointed as industry expert member for Board of Studies (BOS) to discuss new regulation and curriculum (R22). The meeting will be held on Thursday (04th August 2022 at 2.00 p.m) in Board Room, Block 3.

We request you to kindly make it convenient to attend the meeting and offer your valuable guidance and suggestions to improve the academic standards of the Department of Biomedical Engineering. A confirmation of your participation through e-mail (hodbme@nandhaengg.org or dhipa@nandhaengg.org) as soon as possible would be appreciated.

Thanking you

With Regards,
Dr P Sukumar
Head of the Department
Department of Bio Medical Engineering
Nandha Engineering College (Autonomous)
Erode, Tamilnadu - 638052

 **Industry Expert BoS Invitation pdf.pdf**
485K





NANDHA ENGINEERING COLLEGE

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DEPARTMENT OF BIOMEDICAL ENGINEERING

ERODE – 638 052 TAMILNADU

Email : hodbme@nandhaengg.org

Mobile : 9842119035

Dr.P.Sukumar B.E, M.E, Ph.D
Professor & Head

NEC/BME/BoS/Invitation/02
Date & Time : 28.07.2022, 4.00 PM

To,

Mr. R. Kodeeswaran,
Managing Director,
Mechsoft Technologies,
Kaar Infotech Pvt. Ltd.,
True Engineering & Control Systems,
Perundurai, Erode.

Sir,

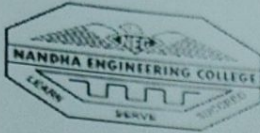
Warm Greetings from Nandha Engineering College (Department of BME). We are happy to inform that you have been appointed as industry expert member for Board of Studies (BoS) to discuss new regulation and curriculum(R22). The meeting will be held on Thursday (04th August 2022 at 2.00 p.m) in Board Room, Block 3.

We request you to kindly make it convenient to attend the meeting and offer your valuable guidance and suggestions to improve the academic standards of Department of Biomedical Engineering. A confirmation of your participation through e-mail (hodbme@nandhaengg.org or dhipa@nandhaengg.org) as soon as possible would be appreciated.

Thanking you

Signature,

(Dr P Sukumar)



HOD BME <hodbme@nandhaengg.org>

BOS Invitation - reg.

2 messages

HOD BME <hodbme@nandhaengg.org>
To: kodeeswaran@gmail.com

Fri, Jul 29, 2022 at 1:54 P


Sir,

Warm Greetings from Nandha Engineering College (Department of BME). We are happy to inform you that you have been appointed as industry expert member for Board of Studies (BOS) to discuss new regulation and curriculum (R22). The meeting will be held on Thursday (04th August 2022 at 2.00 p.m) in Board Room, Block 3.

We request you to kindly make it convenient to attend the meeting and offer your valuable guidance and suggestions to improve the academic standards of the Department of Biomedical Engineering. A confirmation of your participation through e-mail (hodbme@nandhaengg.org or dhipa@nandhaengg.org) as soon as possible would be appreciated.

Thanking you

With Regards,
Dr P Sukumar
Head of the Department
Department of Bio Medical Engineering
Nandha Engineering College (Autonomous)
Erode, Tamilnadu - 638052

 **Industry Expert BoS Invitation pdf.pdf**
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Kodeeswaran R <kodeeswaran@gmail.com>
To: HOD BME <hodbme@nandhaengg.org>

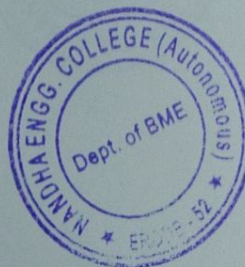
Fri, Jul 29, 2022 at 4:26 PM

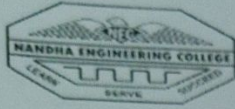
Hi,

Hereby i confirm my presence for the same BOS meeting on the said date and time,

Best Regards
kodeeswaran

[Quoted text hidden]





HOD BME <hodbme@nandhaengg.org>

NEC - BME - BOS Invitation - Reg

2 messages


HOD BME <hodbme@nandhaengg.org>
To: akarthikakdm@gmail.com

Wed, Aug 3, 2022 at 9:40 AM

Madam,

Warm Greetings from Nandha Engineering College (Department of BME). We are happy to inform that you have been appointed as Alumni Member for Board of Studies (BoS) to discuss new regulation and curriculum (R22). The meeting will be held on Thursday (04th August 2022 at 2.00 P.M.) in Board Room, Block 3.

We request you to kindly make it convenient to attend the meeting and offer your valuable guidance and suggestions to improve the academic standards of Department of Biomedical Engineering. A confirmation of your participation through e-mail (hodbme@nandhaengg.org or dhipa@nandhaengg.org) as soon as possible would be appreciated.

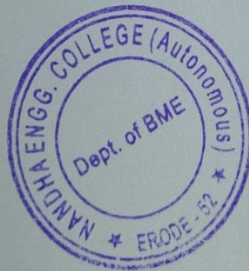
 Alumni BoS Invitation.pdf
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Karthika A <akarthikakdm@gmail.com>
To: HOD BME <hodbme@nandhaengg.org>

Fri, Aug 5, 2022 at 1:15 PM

Thank you sir!
[Quoted text hidden]

Ms. Karthika A, M.E.,(Ph.D)
Assistant Professor / ECE
SNS College of Technology
Coimbatore





**NANDHA ENGINEERING COLLEGE,
ERODE – 638 052**

(An Autonomous Institution, Affiliated to Anna University Chennai and
Approved by AICTE New Delhi)

MINUTES OF 5th BOARD OF STUDIES MEETING

Name of the Body	Board of Studies
Name of the Board	Biomedical Engineering
Meeting No.	05
Date & Time	04.08.2022 & 2.00 PM
Venue	Board Room, Block 3



**NANDHA ENGINEERING COLLEGE,
ERODE – 638 052**

(An Autonomous Institution, Affiliated to Anna University Chennai and
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Minutes of 5th Board of Studies Meeting (BoS) held on 04.08.2022

The 5th Board of Studies (BoS) meeting was held on 04.08.2022 by 2.00 P.M, Board Room, Block 3, Nandha Engineering College, Erode – 52. The members attended the meeting are given in **Annexure I**. The items listed below were taken for discussion.

BoS – 5		Date: 04.08.2022
Board: Biomedical Engineering		
AGENDA		
Item 5.01	Welcome address and Introduction to members	
Item 5.02	Review of the 4 th BOS meeting minutes and ATR	
Item 5.03	Review of the PAC and DAB meeting minutes & ATR	
Item 5.04	Review of Institute Vision & Mission	
Item 5.05	Review of Department Vision, Mission, PEOs and PSOs	
Item 5.06	Review of Correlation between the Vision and Mission statement of Institute and Department, correlation between PEOs and POs.	
Item 5.07	Review of Curriculum (R22) for B.E/B.Tech programme	
Item 5.08	Review of 1 st and 2 nd semester syllabus for B.E/B.Tech programme with CO – PO/PSO Mapping	
Item 5.09	Review on analysis of CO- PO/PSO mapping and attainment of R17 Curriculum.	
Item 5.10	Review on Attainment target fixed for next batch.	
Item 5.11	Review of R17 Ratification, if any.	
Item 5.12	Review of one credit courses	
Item 5.13	Review of Panel of Examiners	
Item 5.14	Any other matter	
Item 5.15	Vote of Thanks	

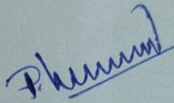
The proceedings of BoS started and the minutes of the meeting are recorded as follows:

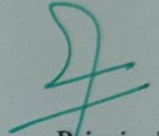
Item 5.01	Welcome Address and Introduction to members
Discussion	The Chairman of the BoS, Dr. P. Sukumar, Professor & Head/ Biomedical Engineering welcomed the members for the meeting.
Item 5.02	Review of the 4th BOS meeting minutes and ATR
Discussion	Reviewed and approved the action taken report of 4th BoS meeting.
Item 5.03	Review of the PAC and DAB meeting minutes & ATR
Discussion	Reviewed and approved the action taken report of PAC and DAB meeting.
Item 5.04	Review of Institute Vision & Mission
Discussion	<ul style="list-style-type: none"> • Dr. S. Hariharan had proposed to include “Entrepreneur” in the institute mission.
Resolution	Resolved to inform the management regarding the discussion.
Item 5.05	Review of Department Vision, Mission, PEOs and PSOs
Discussion	<ul style="list-style-type: none"> • Dr. P. Palanisamy had suggested to include the term ‘biomedical’ in the department vision. • Dr. P. Palanisamy had recommended including ‘Knowledge in Mathematics and Science’ in PSO1. • Dr. K. Hariharan suggested having three PEOs and two PSOs.
Resolution	Resolved to implement the changes in Vision, Mission, PEOs and PSOs.
Item 5.06	Review of Correlation between the Vision and Mission statement of Institute and Department, correlation between PEOs and POs
Discussion	<ul style="list-style-type: none"> • Dr. K. Hariharan suggested to correlate both the mission statements of institute and department.
Resolution	Resolved to review.
Item 5.07	Review of Curriculum (R22) for B.E/ B. Tech Programme
Discussion	<ul style="list-style-type: none"> • Dr. K. Hariharan recommended including Electronics workshop instead of Engineering Practice Lab, so that the students are aware of the basic components easily. • Dr. K. Hariharan suggested to offer the course Medical Electronics as open elective to ECE department. • Dr. K. Hariharan proposed to change the course name as ‘Real Time Embedded System’ instead of ‘Embedded and Real Time Systems’ in elective course.

	<ul style="list-style-type: none"> • Dr. P. Palanisamy: suggested to reduce credits in 7th semester and increase the credits in 6th semester by shifting the courses suitably. • Dr. S. Thomas George recommended to include electives like Artificial Intelligence, Machine Learning and Medical Ethics. • Mr. R. Koodeswaran proposed to include courses like Industry policy and Medical Devices and Safety as electives. • Mr. R. Koodeswaran suggested to offer Hazards and Safety as open elective to other departments. • Dr. A. Karthika suggested to include Artificial Intelligence and Virtual Reality for Biomedical Applications as elective.
Resolution	Resolved to review.
Item 5.08	Review of 1st and 2nd semester syllabus for B.E/ B. Tech programme with CO-PO/ PSO mapping
Discussion	Dr. K. Hariharan suggested to include one unit providing introduction to biomedical in the course Basics of Electrical and Instrumentation Engineering.
Resolution	Resolved to review.
Item 5.09	Review on analysis of CO-PO/PSO mapping and attainment of R17 Curriculum
Discussion	Reviewed and approved.
Item 5.10	Review on Attainment target fixed for next batch
Discussion	Attainment is fixed as 67%
Resolution	Reviewed and approved.
Item 5.11	Review of R17 Ratification
Discussion	<ul style="list-style-type: none"> • 17CSZ07 – Web Services using JAVA – Industrial Course as an Open Elective offered by CSE
Resolution	Resolved to approve Open Elective Course in R17.
Item 5.12	Review of One Credit Courses
Discussion	2021-22 Medical Equipment & its Calibration
Resolution	Resolved to approve
Item 5.13	Review of Panel of Examiners

Discussion	Reviewed and approved
Item 5.14	Any other matter
	<ul style="list-style-type: none"> • Dr. P. Palanisamy suggested to have NPTEL course in VII semester under PSE/OE. • Dr. S. Thomas George suggested to include one credit courses related to Innovation, Entrepreneurship, Start-up to instigate start-up ideas in students.
Item 5.15	Vote of Thanks
Dr. P. Sukumar, Professor & Head/ BME thanked all the members for their active participation.	

Date: 04.08.2022

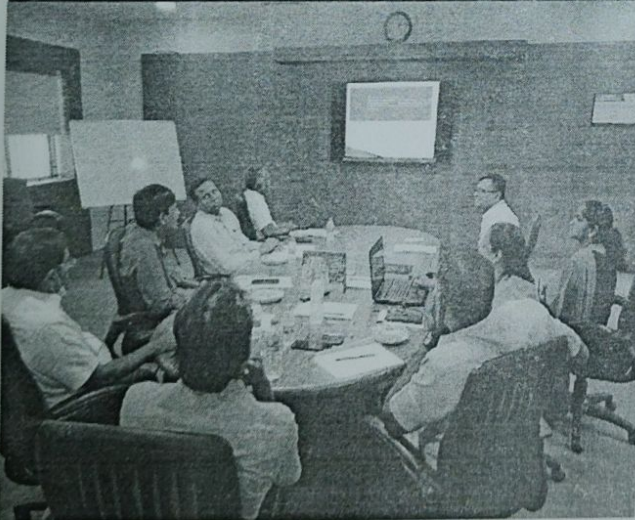
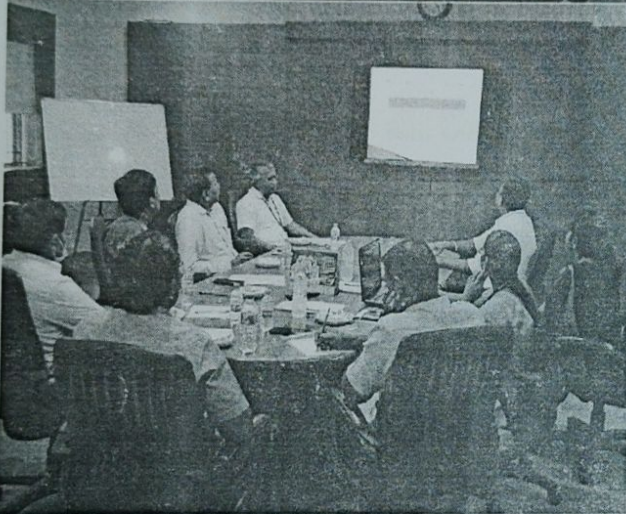
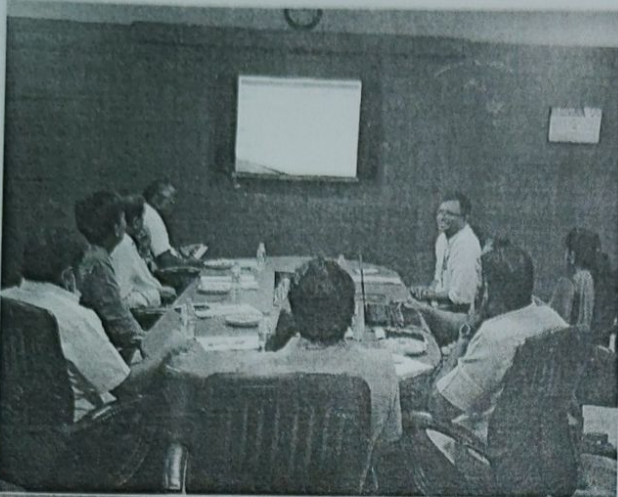

Dr. P.Sukumar


Principal

(Chairman, BoS/ Prof. & Head/ BME)



5TH BOARD OF STUDIES MEETING PHOTOS





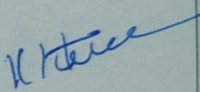
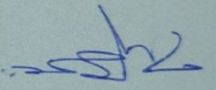
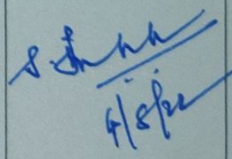
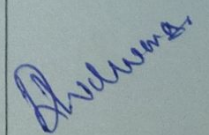
NANDHA ENGINEERING COLLEGE
(Autonomous Institution)
Pitchandampalayam, Erode To Perundurai Road, Erode-638 052

BOARD OF STUDIES

Academic Year: 2022-2023

Board	Biomedical Engineering	Meeting No.	5	R2022
Venue	Board Room, Block 3 – NEC	Date & Time	04.08.2022 & 12.00 p.m	

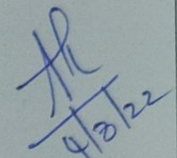
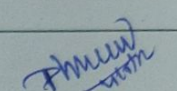
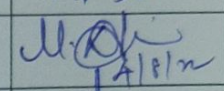
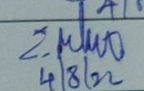
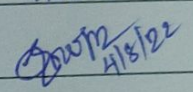
MEMBERS ATTENDED

Sl.No	Members	Representation	Signature
1	Dr. K. Hariharan, Professor, Department of Electronics and Communication Engineering, Thiyagarajar College of Engineering, Madurai – 625015. Ph:9942584251 Email: khh@tce.edu	University Nominee	
2	Dr.P.Palanisamy, Professor, Dept. of Electronics and Communication Engineering, National Institute of technology, Trichirappalli-15 Email:palan@nitt.edu Mobile No:9486001111	Expert Nominee (Nominated by Academic Council)	 4.8.2022
3	Dr. S. Thomas George, Professor & Head, Department of Biomedical Engineering, Karunya Institute of Technology & Science (Deemed University), Coimbatore Email: thomasgeorge@karunya.edu Mobile No: 9443292426	Expert Nominee (Nominated by Academic Council)	
4	Mr. R. Kodeeswaran, Managing Director, Mechsoft Technologies, Kaar Infotech Pvt. Ltd., True Engineering & Control Systems, Amman Complex, Ground Floor, Opp. New Busstand, Perundurai - 638052. Email: kodeeswaran@gmail.com Ph: 7010047887	Member (Expert from Industry)	



NANDHA ENGINEERING COLLEGE
(Autonomous Institution)
Pitchandampalayam, Erode To Perundurai Road, Erode-638 052

BOARD OF STUDIES

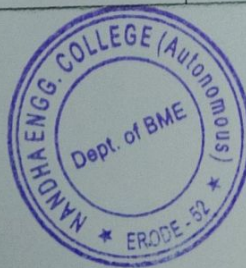
5	Dr. A. Karthika, Assistant Professor, Department of Electronics and Communication Engineering, SNS College of Technology, Coimbatore Email: akarthikakdm@gmail.com Ph:8344336576	Alumni	 4/8/22
6	Dr. P. Sukumar, Professor & Head/ BME	Chairman	 4/8/22
7	Dr. M. Dhipa, ASP/ BME	Member	 4/8/22
8	Ms. E. Madura, AP/ BME	Member	 4/8/22
9	Ms. B. Sowparnika, AP/ BME	Member	 4/8/22




NANDHA ENGINEERING COLLEGE (AUTONOMOUS), ERODE.

DEPARTMENT OF BIOMEDICAL ENGINEERING

Action Taken Report for 5 th Board of Studies meeting held on 04.08.2022		
Biomedical Engineering		
S.No.	Suggestions	Action Taken
1	Dr. P. Palanisamy: Suggested to include the term 'Biomedical' in the department vision.	Incorporated
2	Dr. P. Palanisamy: Suggested to include 'Knowledge in Mathematics and Science' in PSO1.	Incorporated
3	Dr. K. Hariharan: Suggested to have three PEOs and two PSOs	Incorporated
4	Dr. K. Hariharan: Suggested to offer the course Medical Electronics as open elective	Incorporated
5	Dr. K. Hariharan: Suggested to change the course name as 'Real Time Embedded System' instead of 'Embedded and Real Time Systems' in elective course.	Incorporated
6	Dr. P. Palanisamy: Suggested to reduce credits in 7 th semester and increase the credits in 6 th semester by shifting the courses suitably.	Incorporated
7	Dr. S. Thomas George: Recommended to include electives like Artificial Intelligence, Machine Learning and Medical Ethics	Incorporated "Artificial Intelligence"
8	Dr. A. Karthika: Suggested to include Artificial Intelligence and Virtual Reality for Biomedical Applications as elective	Included "Virtual Reality in Medicine"



[Signature]
Chairman, BoS/ BME



Dr.K.Hariharan

✉ khh@tce.edu (mailto:khh@tce.edu)

Educational Qualification

DEGREE	BRANCH	INSTITUTE	YEAR
B.E	Electronics and Communication Engineering	Madras university	1998
M.E	Communication Systems	Thiagarajar college of Engineering, Madurai. Madurai Kamaraj University	2000
Ph. D	VLSI Testing	Anna University, Chennai	2011

Experience

PERIOD	NO OF YEARS	DESIGNATION	INSTITUTION
01-01-2001 to 02-01-2006	5	Lecturer	TCE, Madurai.
02-01-2006 to 01-01-2008	3	Senior Lecturer	TCE, Madurai.
01-01-2008 to 03-01-2011	2	Assistant Professor	TCE, Madurai.
01-07-1998 to 30-08-1999	1	Design Engineer	TOYOTECH SYSTEMS
01-03-2022 to 01-01-0001	0	Professor	TCE, Madurai
04-01-2011 to 28-02-2022	11	Associate Professor	TCE, Madurai

Publications

Journals

1. "Time Series Traffic Flow Prediction with Hyper-Parameter Optimized ARIMA Models for Intelligent Transportation System" Journal of Scientific & Industrial Research, Madurai 625015, Tamil Nadu, India. Vol. 87, Apr, 2022, pp. 408-415
 Hariharan K and Praveen K, Link: <http://top.riiscar.res.in/article/123456789/59432>
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2. A Novel Coverage Improved Deployment Strategy for Wireless Sensor Network Sathees Lingam Paulswamy, A. Andrew Roobert & K. Hariharan Springer Wireless Personal Communications (December – 2021) <https://doi.org/10.1007/s11277-021-09387-y>
3. Scene terrain classification for autonomous vehicle navigation based on semantic segmentation method, K.Hariharan, S.JuliousFusic, R.Sitharthan, S.Karthikeyan Decmber-14,2021. Transactions of the Institute of Measurement and Control, SAGE Journal 0142-3312 (print) 1477-0369 (web) <https://doi.org/10.1177/01423312211062972>
4. UmaMaheswari G, K. Hariharan & M. S. K. Manikandan, "Path optimization of box-covering based routing to minimize average packet delay in software defined network".Peer-to-Peer Networking and Applications, springer, volume 13, pages 932–939 Jan 2020
5. S. Julius Fusic , Anandh N. ,K.Hariharan and I. Leando, "Peer Activity Approach for Engineering Students to Understand Robotics Course", International Journal of Smart Education and Urban Society (IJSEUS), IGI GLOBAL, 11(1),2020,DOI: 10.4018/IJSEUS.2020010101
6. UmaMaheswari G Hariharan K, Manikandan MSK, "Detection and mitigation of UDP flooding attack in a multicontroller software defined network using secure flow management model" Journal of concurrency and computation practice and Experience, Wiley, Vol 31, Issue 20, May 2019
7. Sathees Lingam Paulswamy, Hariharan Kaluvan"Quadrant Based Neighbor to Sink and Neighbor to Source Routing Protocol and Alternate Node Deployment Strategies for WSN"International Journal of Parallel Programming - Springer, 16-june 2008, PP1- 23.
8. Mani Chandran Thirumoolam, Ananda Kumar Manikandan, Balaji Sivaramakrishnan, Hariharan Kaluvan, and Mohan Rao Gowravaram "An Investigation of Facile One-Pot Synthesis of Li₂FeSiO₄/C Composite" for Li Ion Batteries, Journal of ELECTRONIC MATERIALS, Vol. 47, No. 3, 2018
9. P.Sathees Lingam, S.Parthasarathi, K.Hariharan "An effective deployment strategy and geometric routing for wireless sensor network", International Journal of Innovative Research and Advanced Studies (IJIRAS) Volume 4 Issue 6, June 2017.
10. P. Sathees Lingam, S. Parthasarathi, K. Hariharan "ENERGY EFFICIENT SHORTEST PATH ROUTING PROTOCOL FOR WIRELESS SENSOR NETWORKS", International Journal of Innovative Research in Advanced Engineering (IJIRAE) ISSN: 2349-2163 Issue 06, Volume 4 (June 2017) ISSN: 2349-2163, 2017.
11. R.Abinaya, V.Varsha, Kaluvan Hariharan, "An intelligent street light system based on piezoelectric sensor networks", Indian Journal of Science and Technology, 2016.
12. 1.K. Paldurai, K. Hariharan, "Implementation of Signed Vedic Multiplier Targeted at FPGA architectures", ARPN journal of Engineering and Applied Sciences, ISSN: 1819-6608, Volume-10, Number-5, pp: 2193-2197, 2015.
13. 1.K. Paldurai, K. Hariharan, "FPGA implementation of Area Efficient and Reduced Delay 64 x 64 Vedic Multiplier", International Journal of Advanced Engineering Applied Sciences, ISSN: 1819-6608, VOLUME-8, PP: 17-22, 2014.

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15. K. Hariharan, E. Benitta Hubert, K. V. O. Divyalakshmi, K. Shamalla and V. Abhai Kumar, "Coherent Sinusoid Generation using Novel DDFS Architecture", International Journal of Smart Home, Vol. 6, No. 1, January, 2012.
16. K. Hariharan, R. Ganesh skandha, T.J. Rambabu, V. Abhaikumar, "Dynamic range testing of Data converter using Low resolution signal", International journal of advances in science and technology, 2011
17. Hariharan Kaluvan, T. J. Rambabu, R. K. M Rajkumar, V. Abhaikumar, "Design of solar isolation power logging system with ADC error testing and correction", Journal of Scientific and industrial research, 2011.
18. Hariharan Kaluvan, R. K. M. Rajkumar, Dr. V. Abhaikumar, "Time tick based method for measuring static errors of ADC", Instruments and Experimental Techniques, Springer, 2011.
19. K.Hariharan, Practical Digital system design , Journal-VLSI,
20. K.Hariharan, Basics of Microprocessor and controller, Journal-Embedded system,
21. K. Hariharan, E. Benitta Hubert, K.V.O Divya Lakshmi, K. Shamalla, Dr. V. AbhaiKumar, "Test Bench for Dynamic Range testing of ADC", International Journal of Engineering Science and Technology, vol.2(12),2010
22. K. Hariharan, S. Gowthamraj, B. Subramaniam, S.R. Venkateshbabu, V. Abhaikumar "A Novel method for Testing Digital to Analog converter in Static Range", American journal of Applied Sciences, ISSN:1546-9239, 2010.
23. K. Hariharan, S. Gowthamraj, B. Subramaniam, S.R. Venkateshbabu, V. Abhaikumar "A Novel method for Testing Digital to Analog converter in Static Range", American journal of Applied Sciences, ISSN:1546-9239, 2010.
24. 1.K. Hariharan, P. Vasanthakumar, G. Varun, V. Abhaikumar, "A method for ADC error testing and its compensation in ratio metric measurements", Measurements science review, Volume 10, No2, 2009.
25. K.Hariharan, S.A.Angayarkanni, R.Premalatha, Design and FPGA Implementation of testable Floating Point Unit, Journal-, Testing
26. K.Hariharan, V.Abhaikumar, S.A.Angayarkanni, R.Premalatha, S.A.Angayarkanni, R.Premalatha, A Class of test pattern generation for digital circuits using differential evolution algorithm, Journal-ICDF 2008, Testing
27. Mansoor Romi, R. Abinesh, S.B.Anandhi, Hariharan Kaluvan, "Integrating testing Instruments using 8051 microcontrollers", NCVET'04, 2004.
28. S.Julius Fusic, Dr.K.Hariharan, I.Leando, Modeling and Analysis of GPS-GLONASS Navigation for Car Like Mobile Robot. J. Electr. Eng. Technol. 15, 927-938 (2020). Scopus & was indexed with impact factor of 1.069 & SJR is 0.23. <https://doi.org/10.1007/s42635-020-00365-1>.008774 UTC

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1. S. Karpagavalli, K. Hariharan, G.Dheivanai and M. Gurupriya "Design of Direct Digital Frequency Synthesizer with the Technique of Segmenting in Quarter Wave Symmetry" International Conference on Computer Networks and Communication Technologies pp 469-477. September 2018
2. K. Hariharan, S. Julius Fusic ; P. Ramkumar; "Path planning of robot using modified dijkstra Algorithm" IEEE - National conference on Power Engineering Conference (NPEC-2018)
3. K. Hariharan, S. Julius Fusic ; P. Ramkumar; " Path planning for car like mobile robot using robot operating system " IEEE - National conference on Power Engineering Conference (NPEC-2018)
4. Gurusamy, U., Hariharan, K., Manikandan, M.S.K. "Modelling and Performance Analysis of Flow Management in a Multi-Controller Software Defined Network using M/M/c/K model" INDICON 2018 - 15th IEEE India Council International Conference
5. N.Kumutha, K.Hariharan, B.Manimegalai, " Low RCS using Superluminal Propagation", Proceedings of the 2017 IEEE Region 10 Conference (TENCON), Malaysia, November 5-8, 2017.
6. R.Abinaya, V.Varsha, Kaluvan Hariharan, "An intelligent street light system based on piezoelectric sensor networks", International conference on electronics and communication systems, 978-1-5090-3355-3, 2017.
7. S.Nusrath Nasreen Fathima, M.R. Satheesh Kumar, B. Raja, Dr. K. Hariharan," DDFS based on Modified Taylor Series Approximation with Piecewise Linearization", International Journal of Advanced Research in Basic Engineering Sciences and Technology (IJARBEST),2017.
8. B.Raja, M.R.Satheesh Kumar, S.Vikash, K.Hariharan. "Maximum power point tracking in solar panels under partial shading condition using equilibration algorithm" IEEE- International Conference on Communication and Signal Processing (ICCSP), 2016
9. Raja.B, Satheesh Kumar M.R, Vikash.S, Hariharan.KMaximum Power Point Tracking in Solar Panels under Partial Shading Condition using Equilibration Algorithm, International Conference on Communication and Signal Processing, April 6-8, 2016.
10. R.Abinaya,V.Varsha, Kaluvan Hariharan," An intelligent street light system based on piezoelectric sensor networks" IEEE SPONSORED 3rd INTERNATIONAL CONFERENCE ON ELECTRONICS AND COMMUNICATION SYSTEMS (ICECS) -2016
11. A.Rakavi,K.Hariharan and MSK Manikandan,"Grid based mobile sensor node deployment for improving area coverage in wireless sensor network", 3rd IEEE International Conference on Signal processing, Communication and networking (ICSCN-2015) at Anna university, MIT campus, March 2015.
12. A. Rakavi, M. S. K. Manikandan, K. Hariharan, "Exposure Path prevention for wireless sensor networks in Internet of Things using bond percolation theory", International conference on Innovations in Information embedded and communication systems, ISBN:978-1-4799-6816-9, 2015.

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13. N. Kumutha, K. Hariharan, B. Manimegalai, "Reduction of Interference between Two Neighbouring Antennas by a Modulated Metasurface", IEEE International WIE Confernece on Electrical and computer Engineering, ISBN:978-1-4673-8786-6,2015
14. A.Rakavi, K. Hariharan and MSK Manikandan"Exposure path prevention for wireless sensor network in internet of things using bond percolation theory" 2nd IEEE International Conference on Innovations in Information Embedded and Communication Systems (ICIIECS'2015) at Karpagam College of Engineering, March 2015
15. G. C. Karthikeyan, K. Lakshmanan, Dr. K. Hariharan, " A Novel ROM-less Architecture for Direct Digital Frequency Synthesizer based on Linear Neural Networks", IEEE Sponsored 2nd International Conference on Innovations in Information Embedded and Communication Systems ICIIECS'15, 978-1-4799-6818-3,2015.
16. 1. K. Paldurai, K. Hariharan, "FPGA implementation of delay optimized single precision floating point multiplier", International conference on advanced computing and Communication Systems, 2015.
17. A. Rakavi, M. S. K. Manikandan, K. Hariharan, "Grid based mobile sensor node deployment for Improving area coverage in wireless sensor network", International conference on signal processing, communication and networking, ISBN: 978-1-4673-6823-0,2015.
18. 1.K. Paldurai, Dr. K. Hariharan, G. C. Karthikeyan and K. Lakshmanan, "Implementation of MAC using Area Efficient and Reduced Delay vedic multiplier targeted at FPGA architecture", IEEE transaction on 2014 International conference on Communication and Network Technologies, ISBN: 978-1-4799-6265-5,2014.
19. U. Dinesh, M. Hariharan, S. Aravind Ram, "Shaping of non linearity in direct digital frequency synthesizer- An approach by Genetic algorithm", 2014 International Conference on Advances in Electrical Engineering (ICAEE), 2014.
20. Dr.K.Hariharan,S.Aravind Ram,U.Dinesh,M.Hariharan"Shaping of Non-linearity in Direct Digital Frequency Synthesizer-An approach by Gemetic Algorithm",ICAEE'14
21. BA. Sindhu, K.hariharan, "Static Range testing of ADC", International conference on information, communication and embedded systems, 2013.
22. X. Realine Infacia, K. Kavya, Hariharan Kaluvan, "Equilibration algorithm for maximum power point tracking in solar panels", IEEE International conference on Computational Intelligence and Computing Research, 2013.
23. K.Hariharan, K.Gayathri, K.Shamalla, , K..Sugumar, Preethi, Direct digital frequency synthesizer using cubic spline approximations, Conference-IEEE-International Conference on Computing, Electronics and Electrical Technologies (ICCEET), 2012 , Design
24. K. Hariharan, A. J. Prasanth, M. Vinoth Arun, "ADC Error Detection using recursive linear reduction and wiener filtering", Conference-ETET2011 Anna university Madurai, Testing., 2011
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28. K. Hariharan, , K. Arun Raj, V. Anand, " Time Tick BIST for testing Static errors in an ADC using Expo Signal , Conference-ICCCD-2010 IIT Karagpur , 2010
29. K. Hariharan, V. Abhaikumar, A. Sakthi,, B.Mahalakshmi,, A. Sakthi,, B. Mahalakshmi,, "Design of a Readable Programmable PRBS Generator" Conference-ICES 2010 INTERNATIONAL CONFERENCE ON EMBEDDED SYSTEMS, Embedded System, 2010
30. K. Hariharan, B.Subramaniam, S.R.Venkatesh Babu, S.Gothamraj, "Time-Tick BIST for Static Error Testing in DAC" Conference-Testing, ICES 2010.
31. K.Hariharan, B.SUBRAMANIAM, S.R.VENKATESH BABU, S.GOUTHAMRAJ, TIME-TICK BIST FOR STATIC ERROR TESTING IN DAC, Conference-Testing, ICES 2010
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34. K. Hariharan, S. A. Angayarkanni, R. Premalatha and V. Abhaikumar, "A Class of Test Pattern Generation for Digital Circuits using Differential Evolution Algorithm", International Conference on Digital Factory – ICDF, 2008
35. S. A. Angayarkanni, R. Premalatha, K. Hariharan, "Design and FPGA implementation of Testable floating point unit", Proceedings of National conference on Emerging Technologies,2008.
36. Praveen Kumar, B., Hariharan, K "Multivariate Time Series Traffic Forecast with Long Short Term Memory based Deep Learning Model" ., Proceedings of 2020 IEEE International Conference on Power, Instrumentation, Control and Computing, 2021
37. "Displacement Estimation by Savitzky Golay Filtering Technique Through Inertial Sensor". Conference NIT Delhi - ICCIS 2021. SuryaKumar, Joshi rajan and K.Hariharan
<https://drive.google.com/file/d/16l6w8HMZii2l5l6loxQQiz2MPXVYIkzM/view>
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39. K. Hariharan, S. Rajaram, "FPGA implementation of Reed solomon Encode ER and Decoder", Proceedings of national conference on communication and Entertainment (NICE 2001)ISTE chapter, Cochin,2001

40. K.Hariharan., B.Raja, M.R.Satheesh Kumar, S.Vikash, "Maximum power point tracking in solar panels under partial shading condition using equilibration algorithm" IEEE- International Conference on Communication and Signal Processing (ICCSP), 2016

41. K. Hariharan, S. Rajaram, "FPGA implementation of Viterbi Decoder", Proceedings of national conference on communication and Entertainment (NICE 2001) ISTE chapter, Cochin,2001

42. S. Rajaram, K. Hariharan "VLSI architectures for TURBO codes and its implementation in FPGA", Proceedings of National conference on communication and Entertainment (NICE 2001) ISTE chapter, Cochin, Feb 23-24, 2001

43. S.Rajaram, K.Hariharan VLSI architectures for TURBO codes and its implementation in FPGA, Proc. Of national conference on communication and Entertainment(NICE 2001)ISTE chapter, Cochin, Feb 23-24,2001

44. K.Hariharan, S.Rajaram, FPGA implementation of Reedsolomon Encode and Decoder, Proc. of national conference on communication and Entertainment(NICE 2001)ISTE chapter, Cochin,

45. K.Hariharan, S.Rajaram, FPGA implementation of Viterbi Decoder, Proc. of national conference on communication and Entertainment(NICE 2001)ISTE chapter, Cochin,

Books

1. Preparing book entitled as Digital System Design in collaboration with YES DEE publishing on 18th February, 2014

Industry Interactions

COMPANY NAME	NATURE OF WORK	PERIOD	DETAILS
TTSL	Embedded design	01-02-2020 to 31-12-2021	Consultant for Embedded design
Sapience consulting,Bangalore	. Appointed as a member of Advisory faculty and Certified Trainer	08-04-2014 to 20-04-2018	Trainer
Texas Instruments	Analog filter experimental Kit Project	26-05-2008 to 27-06-2008	Consultant

Sponsored Research

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TITLE	SPONSORING AGENCY	AMOUNT	RESPONSIBILITY
Design of Test bed for mixed signal circuit testing	AICTE-RPS	13.5	PI
Intel Embedded and IOT LAB set up	TI	5 Nos kit	Faculty in charge
Intel Embedded LAB set up	INTEL India	10 Nos of ATOM processor Kit is proposed	-
Free Scale Embedded Lab set up	Free scale Bangalore	20 laksh worth of 20 nos of Embedded Kits are sponsored	-
Design of electronic Camera Alignment unit For Auto align projects	RCI Hydrabad	14.5 lakhs	-

Consultancy

NAME OF INDUSTRY	DETAILS	PERIOD
Dr.Su. Thillai Vallal, Venkateswara Hospitals	on encountering problem in solar fencing	16-07-2015 to 20-04-2018
FreeScale India	Free Scale Embedded Lab set up	20 laksh worth of 20 nos of Embedded Kits are sponsored
INTEL India	Intel Embedded LAB set up	10 Nos of ATOM processor Kit is proposed
RCI Hydrabad	Design of electronic Camera Alignment unit For Auto align projects	External Funds received 14.5 lakhs

Seminar, Conference, Workshop Attended

1. Participated in AICTE-QIP sponsored one-week short term course on "Research opportunities and challenges in Robotics" at Thiagarajar College of Engineering, Madurai, 19-02-2018 to 25-02-2018

2. Participated in ISRO sponsored National Conference on "Small Satellite Technology and Applications" at Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology, Chennai, 15-12-2017 to 16-12-2017

3. Attended in ISRO sponsored one day Pre-Conference workshop on "Small Satellite Technology and Applications" at Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology, Chennai., 14-12-2017 to 15-12-2017
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4. Participated in a two week workshop on "CMOS, Mixed Signal and Radio frequency VLSI Design" conducted by IIT, Kharagpur, 30-01-2017 to 04-02-2017

5. Participated in a two week workshop on "CMOS, Mixed Signal and Radio frequency VLSI Design" conducted by IIT, Kharagpur, 30-01-2017 to 04-02-2017

6. Participated in workshop on "Standard operating procedures for TCE" at Thiagarajar college of Engineering, Madurai , 22-11-2016 to 22-11-2016

7. Participated in "Science Academies' refresher course in Experimental Physics at Thiagarajar college of Engineering, Madurai., 05-07-2016 to 20-07-2016

8. Participated in the Indian Antenna Week (IAW) pre-conference workshop on Microstrip Antennas, 05-06-2016 to 05-06-2016

9. Participated in TEQIP sponsored workshop on "Role of Thermal Imagers in Research, operation and maintenance" at Thiagarajar college of Engineering, Madurai , 24-03-2016 to 24-03-2016

10. 1.Participated in the IoT hands on workshop in CXC PROGRAM 2015 at IISC Bangalore , 17-12-2015 to 18-12-2015

11. Participated in the "INTEL India Academic Forum 2015" held at Hotel JW Marriott, New Delhi, 08-10-2015 to 09-10-2015

12. Participated in Faculty Colloquium on "IoT at the Intel India Innovation Conclave 2014", organized by INTEL on 09-12-2014 and 10-12-2014 held at Bangalore, 09-12-2014 to 10-12-2014

13. Participated in the training "Systematic Creative thinking and creative problem solving", organized by The International Center for Studies in Creativity held at Madurai, 08-10-2014 to 09-10-2014

14. Participated in the NBA "Workshop on new outcome based accreditation" on 3rd February, 2014 and from 22 -02-2014 to 24-02-2014 February, 2014 held at Thiagarajar College Engineering, Madurai , 22-02-2014 to 24-02-2014

15. Participated in a two week ISTE Workshop on "Signals and Systems" conducted by IIT, Kharagpur, 02-01-2014 to 12-01-2014

16. Participated in the Intel India Academic forum held organized by INTEL at Goa, 18-09-2013 to 19-09-2013

17. Attended a two day conference on "Architecture, Programming & Applications of Stellaris (ARM Cortex-M3) Microcontroller", at Texas Instruments India, Bagmane Tech Park, Bangalore jointly conducted by Texas Instruments University Program and IEEE CAS Bangalore Chapter, 19-11-2011 to 20-11-2011

18. Workshop on ATOM processor, IISC bangalore,
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19. Workshop on Intel Processor, IISC Bangalore,
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20. Workshop on advanced microprocessor, Infosys Mysore,
21. Attended the short term course on "Electronics System Packaging" organized by TIFAC CORE in wireless technologies and department of ECE, 06-07-2009 to 10-07-2009
22. Attended the course on Microprocessors as part of the Indo-US Engineering Faculty Leadership Institute at the Infosys Mysore Center., 22-06-2009 to 26-06-2009
23. Workshop on MATLAB, NIT Surathkal,
24. Participated in the workshop on "Awareness of Intellectual Property Rights and Related Issues", Organized by the Centre for Awareness of Intellectual Property Rights & Trademarks, Anna University Chennai in Association with the Ministry of Communications and Information Technology, Government of India , 30-01-2009 to 31-01-2009
25. Workshop on Intel Processor, IISC Bangalore,
26. Participated in the Faculty Updation Training on "Linux/Embedded Linux Kernel and Driver Development" organized by C-DAC, Hyderabad in collaboration with INTU, Hyderabad , 17-11-2008 to 28-11-2008
27. Workshop on Embedded system , CDAC Hyderabad,
28. Attended a one week Training Program on "National Instruments Lab view and Elvis" at Thiagarajar College of Engineering, Madurai, 28-04-2008 to 02-05-2008
29. 1. Participated in the workshop on "Embedded System Design on MSP430" held at Texas Instruments, India., 23-04-2008 to 25-04-2008
30. Workshop on ARM processor, IWave Bangalore,
31. Road show on TCE BOT(EU Project), PSGTECH Coimbatore,
32. Road show on TCE BOT(EU Project), IGNOU Delhi,
33. Attended the Pre-conference Tutorial on "Embedded Systems on Chip" held at National Institute of Technology, Trichy, 01-04-2007 to 01-04-2007
34. Work shop on Robotics , TCE Madurai,
35. Course on arm processor, Iwave Bangalore,
36. Workshop on LAB view, National Instruments Bangalore,
37. Workshop on Anupama Processor, Hyderabad Anurag,
38. Conference of emerging technology, CIT Bangalore.

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39. Workshop on embedded system,, IIT, DELHI,
40. Participated in the one day Educators Seminar cum Training Program on "Analog, Digital and RF Measurements", Jointly organized by Thiagarajar college of Engineering, Madurai and M/S Agilent Technologies, 06-03-2002 to 01-01-0001
41. DSP & VLSI Hardware Design, Agilent Chennai,
42. Design with 8 bit microcontroller , CEDTI, CALICAT,
43. Workshop on high speed digital design, Agilent Technology Chennai,
44. short term course on "Digital Signal Processing-Software and Hardware", 27-12-2000 to 31-12-2000
45. short term course on "Digital Signal Processing-Software and Hardware", 27-12-2000 to 31-12-2000
46. . Participated in the one day Educators Seminar cum Training Program on "Analog, Digital and RF Measurements", Jointly organized by Thiagarajar Collge Engineering and M/S Agilent Technologies on 6th March 2002., 01-01-0001 to 01-01-0001

Seminar, Conference, Workshop Organised

1. Organized(off campus) FDP on "Embedded Systme Design with Arduino " at Noorul Islaam University , Nagercoil , 29-03-2018 to 31-03-2018
2. Organized workshop on "Embedded System design with AVR controller" at PSG institute of Technology and Applied Research, Coimbatore., 06-12-2017 to 08-12-2017
3. 1.Organized workshop in Indian Antenna Week (IAW) on "Advanced Antenna Technology" at Thiagarajar college of Engineering, Madurai from 06-06-2016 to 10-06-2016., 06-06-2016 to 10-06-2016
4. Organized a seven days FDTP on "Linear Integrated Circuits" at KLN college of Information Technology, 30-09-2015 to 06-12-2015
5. Organized two days workshop on "Microprocessor and Interfacing" at K.L.N college of Engineering., 23-01-2015 to 24-04-2015
6. College level Texas Instruments Innovation Challenge-INDIA Analog Design Contest 2014 held at Thiagarajar College Engineering , 22-07-2014 to 14-08-2014
7. Contributed as Teaching Assistant in two week ISTE workshop on "Analog Electronics" conducted by IIT, Kharagpur, 04-06-2013 to 14-06-2013
8. 4 days Workshop on Microprocessor and controllers., Thiagarajar College of Engineering, Madurai, Dept ECE and EEE and outside college

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9. one day workshop on "Microprocessor and controllers" held at Thiagarajar college of engineering, Madurai , 16-10-2009 to 16-10-2009
10. Workshop on Hardware hobby circuit design, Thiagarajar College of Engineering, Madurai, Dept ECE and EEE
11. Workshop on Hardware hobby circuit design, Thiagarajar College of Engineering, Madurai, Dept ECE and EEE
12. Workshop on Hardware hobby circuit design, Thiagarajar College of Engineering, Madurai, Dept ECE and EEE
13. Workshop on Hardware hobby circuit design, Thiagarajar College of Engineering, Madurai, Dept ECE and EEE
14. Workshop on Hardware hobby circuit design, Thiagarajar College of Engineering, Madurai, Dept ECE and EEE
15. One day workshop on Electronics instruments, Thiagarajar College Of Engineering, Student and non Teaching staff
16. Workshop on Hardware hobby circuit design, Thiagarajar College of Engineering, Madurai, Dept ECE
17. Workshop on Amature Radio Technology and license, Thiagarajar College of Engineering, Madurai, Dept ECE
18. Workshop on Amature Radio Technology and license , Thiagarajar College of Engineering, Madurai, Dept ECE
19. Workshop on Hardware hobby circuit design, Thiagarajar College of Engineering, Madurai, Dept ECE
20. Workshop on Microcontrollers, Thiagarajar College of Engineering, Madurai, Dept ECE and EEE and outside collge
21. Workshop on Hardware hobby circuit design, Thiagarajar College of Engineering, Madurai, Dept ECE and EEE Mechatronics
22. Organized(off campus) Faculty Development Programme at PSG college of Technology, Coimbatore ,2018, 01-01-0001 to 01-01-0001

Lectures Delivered

TOPIC	DELIVERED AT	PERIOD
Electronics and EEE	Thiagarajar College of Engineering, Madurai, Dept ECE and EEE	14-09-2018 to 14-09-2018

Last Updated: 2022-08-01 18:29:51

TOPIC	DELIVERED AT	PERIOD
FDP on ARM and ADVANCED INTEL MICROPROCESSOR ARCHITECTURE	VIT VELLORE	05-04-2018 to 07-04-2018
Smart Sensors and Systems and Embedded Networking and setting up lot components	Coimbatore institute of Technology	20-12-2017 to 20-12-2017
Next-generation communication networks for IoT	Sona College of Technology	04-11-2017 to 04-11-2017
Linear Integrated Circuits	KLN college of Information Technology	02-12-2015 to 02-12-2015
Linear Integrated Circuits - A System Approach	TI centre of Excellence, RGPV, Bhopal	31-08-2015 to 02-09-2015
Digital System Design	Sudarsan Engg college	Digital systems
Digital electronics	NTTF Toothukudi	Control system
Microprocessor	Kalasalingam University	Embedded system
Microcontrollers	Sowdambike Engg college	Embedded system
Advanced Microprocessor	Govt polytechnic Sivakasi	Embedded system

Awards / Recognition Received

1. Wetness monitoring in AIRPORT RUNWAY, Indian Patent
2. 1.Awarded for the contribution made to the event as a mentor for Internet of Things Camp , Intel
3. Awarded as "Motorola Scholar Award Program for the year 2011-2012" for the project entitled "Intelligent Home Security System" at IISC Bangalore , Motorola Foundation
4. Best project guide Renesas-Japan in 2008, Renesas Japan
5. Awarded Second Prize for the project entitled "PEER-CARE CHALLENGING THE MONITORING APPROACH THROUGH EMBEDDED SENSORS" , Motorola Foundation, USA

Visits Abroad

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PLACE **PURPOSE** Last Updated: 2022-08-01 18:29:51.008774 UTC **DURATION**

PLACE	PURPOSE	DURATION
Germany	Project roadshow at Würzburg university(JMU)	2006
Sweden	Road show at KTH university	2006
France	Road show	2006

Membership

NAME OF SOCIETY	DETAILS	PERIOD
ISHRAE	MEMBER	
BOS member for DOTE	Intel Atom processor curriculum development	-
IEEE member in Instrumentation and measurement	IEEE member in Instrumentation and measurement	

Other Achievements

1. Organied one Week WORKSHOP ON EmBEDDED SYSTEM DESIGN with OPENSOURCE HARDWARE AND SOFTWARE
2. Organied one Week WORKSHOP ON EmBEDDED SYSTEM DESIGN with OPENSOURCE HARDWARE AND SOFTWARE
3. Awarded Indian Patent for the work on "Wetnes measurment on AIRPORT RUNWAY"
4. Awarded Indian Patent for the work on "Wetnes measurment on AIRPORT RUNWAY"
5. Act as member in First Doctoral Committee(DC) meeting in Noorul Islam University, Kanyakumari
6. mentor in "Smart India Hackathon" 2017
7. Act as External Expert Member in Department Review Committee (DRC) meeting at Kalasalingam university, Krishnankovil
8. Board of member in Board of Studies meeting at Kalasalingam University
9. Act as External Expert member in Department Review Committee meeting (DRC) at Kalasalingam University, Krishnankoil.
10. Act as board of member in Board of studies meeting conducted at Kalasalingam University on 19-01-2015.

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National Institute of Technology, Tiruchirappalli:
Performa for CV of Dr.P.PALANISAMY

BRIEF PROFILE

Dr.P.PALANISAMY graduated B.E. in Electronics and Communication Engineering from Bharathiar University, Coimbatore and M.E. in (Communication systems) from National Institute of Technology (then Regional Engineering College), Tiruchirappalli and Ph.D from National Institute of Technology in the area of Signal Processing. He secured University Rank in B.E., First Rank in M.E. and awarded Gold Medal for best thesis in Ph.D.

He worked as a Research Assistant from December 1995 to March 1997 and then he worked as a Scientific Officer in the Department of Electronics, Govt. of India, sponsored research project at Regional Engineering College, Tiruchirappalli from March 1997 to July 1998. Since July, 1998, he has been with National Institute of Technology, Tiruchirappalli as a regular Faculty in the Department of Electronics and Communication Engineering. Currently he is a Professor in the Department of Electronics and Communication Engineering, National Institute of Technology, Tiruchirappalli-620015. In addition, he had served as Associate Dean (Academic) for 3 years, Chairperson PG Admissions Committee for 2 years, Member PG Admission Committee for 5 years, Associate Convener Campus Communication Service for 4 years and currently he is a Convener Campus Communication Service, training officer in Training & Placement department and Member, Industry Institute Interaction Cell.

His area of interest includes Statistical Signal Processing, Adaptive Signal Processing, Adaptive Noise Cancellation, Array Signal Processing, Detection and Bearing Estimation, Frequency Estimation, Image Processing, Medical Image Analysis, Data and Image compression, Digital and Wireless communication etc. He handles Signals & Systems, Digital Systems, Control Systems, Probability Theory & Random processes, Digital Signal Processing, Statistical Theory of Communication, Digital Communication, Error Control Coding, Satellite Communication, Advanced Digital Signal Processing, Advanced Digital Communication, Detection and Estimation, Spectral Analysis of Signals etc.

Under his guidance 8 Ph.D. and 5 MS (By Research) completed. Currently, he is guiding 9 Research Scholars. He has published 51 papers in reputed International Journals and 64 papers in International Conferences.

He has been reviewer for IEEE Transaction on Signal Processing, IEEE Signal Processing Letter, IET (IEE) Signal Processing, IET radar, Sonar and Navigation, International Journal of Signal Processing (Elsevier), Digital Signal Processing (Elsevier Publication), European Journal of Advances in Signal Processing (Hindawi Publication), International Journal of Electronics (Taylor and Francis Publication) and for IEEE and other National and International conferences. He is the member of IEEE, ISTE, IETE, ACEEE and IAENG.



Dr. S. Thomas George, M.E, Ph. D
 Professor and Head, Biomedical Engineering
 Mobile : 9443292426
 Email : thomasgeorge@karunya.edu

RESEARCH INTERESTS

Biosignal Processing, Image Processing, Medical Electronics and AI in Healthcare, Neural Engineering, Optimization

EXPERIENCE

24 Years

RESEARCH PROFILES

1	Google Scholar Profile	https://scholar.google.com/citations?user=kLn--tgAAAAJ&hl=en
2	Scopus Profile	https://www.scopus.com/authid/detail.uri?authorId=57211650041
3	Patents	Published 4 Indian Patents

RESEARCH PROJECTS

No	Title of the Project	Funding Agency	Amount (Rs)	Year
1	Enhanced Source Separation Tool for Interrogating Neurological (Seizures) Disorders (Completed)	DST-TDP	47,00,000	2016-2018
2	Design and development of an adaptive brain computer interface (BCI) based assistive tool for paralytic people (Ongoing)	DST-ICPS	38,84,329	2019-2022

SUMMARY OF INTERNATIONAL JOURNAL & CONFERENCE PUBLICATIONS - [PUBLISHED & ACCEPTED]

Year	International Journal Publications	International Conference Publications	National Conference Publications	Cumulative Impact Factor	Scopus Citations	Google Scholar Citations	h-index
2009 - 2021	36	13	06	3.01	206	300	9

EDUCATION

Degree	Branch/ Specialization	University	Class	Mode	Month & Year of Passing
Ph.D	Biomedical Signal Processing	Karunya University	NA	Part Time	May 2014
M.E	Applied Electronics	Karunya University	First Class with Distinction	Full Time	April 2002
B.E	EEE	Bharathiar University	First Class	Full Time	April 1997

TEACHING INTERESTS

Advanced Medical Signal Processing, Advanced Medical Image Processing, Digital Signal Processing, Control Systems, Medical Electronics

RESEARCH GUIDANCE

Research guidance as supervisor	Completed	Ongoing
Doctor of Philosophy - PhD Thesis advising	2	5
Master of Engineering - Master's Thesis advising	24	1

Name : Kodeeswaran.R
DOB:10-10-1981

Communication Address

1,Perianna Street
Kaikolanthottam
Erode 638001
Tamilnadu
Mobile : 7010047887
Email : kodeeswaran@gmail.com

Designation

Managing Director
1)Mechsoft technologies
2)Kaar Infotech private limited
3)True Engineering and control systems,

Official Address

True Engineering and control systems
65/22, Amman Complex, Ground floor,
Opp, New bus stand,
Perundurai 638052
Erode District, Tamilnadu

Experience Details

Mr.R.Kodeeswaran has more than 15 years of experience in the fields of Design and development of Software Application for Automotive R&D Testing machines, Machine Building, Assembly Solutions, Process automation and Monitoring, Industrial automation, embedded hardware & software development, Fundamentally an Integration Specialist with hands on experience on mechanical design, Electrical and Electronic subsystems, Instrumentation and Control system design, Hydraulics and Pneumatic systems, Software application development, responsible for the complete engineering design and developments, business development of the company, Prior to founding Mechsoft technologies. Mr.R.Kodeeswaran gained knowledge with various process automation and test bench manufacturing facilities across the country.

R.Kodeeswaran by qualification holds an

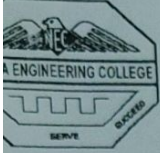
- 1) Diploma in Electrical and Electronics Engineering
- 2) Bachelor degree in Electronics and Instrumentation Engineering
- 3) Member (MIE) Electrical Engineering, the Institution of Engineers (India).
MIE M-1567130 from 30-11-2016
- 4) Chartered Engineer

**NANDHA ENGINEERING COLLEGE
(AUTONOMOUS), ERODE
DEPARTMENT OF BIOMEDICAL ENGINEERING**

Academic Year

2021-22

Feedback Analysis and Action
Taken Report for Students



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E.mail : info@nandhaengg.org

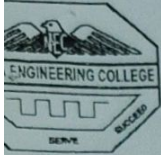
Website : www.nandhaengg.org

FEEDBACK ON SYLLABUS FROM FACULTY ON SYLLABUS

FACULTY FEEDBACK 21-22

								SURVEY COUNT: 13	
S.No	Question's	Excellent	Good	Fair	Poor	Total Weightage	Percentage	3-Scale Weightage	
1	Curriculum and Syllabus on par with recent trends	6	7	0	0	45	86.54%	2.6	
2	Elucidation of Course Outcomes	4	9	0	0	43	82.69%	2.48	
3	Adequacy of Academic tasks in the course plan	4	9	0	0	43	82.69%	2.48	
4	Syllabus in accordance with Competitive Examinations	4	7	2	0	41	78.85%	2.37	
5	Importance and Relevance of the course to Industry and Societal needs	7	5	1	0	45	86.54%	2.6	
6	Accessibility of Relevant Reading Materials and E - sources in the library	6	5	2	0	43	82.69%	2.48	
7	Course equilibrium between theory and application	7	6	0	0	46	88.46%	2.65	
8	Privilege to propose, modify, suggest and incorporate new topics in the syllabus through proper forum	7	6	0	0	46	88.46%	2.65	
9	Flexibility in adopting new techniques / tools/ strategies in teaching	6	7	0	0	45	86.54%	2.6	
10	Scope provided for Research activities	4	9	0	0	43	82.69%	2.48	
AVERAGE SCORE						44	84.62%	2.54	

Phummm
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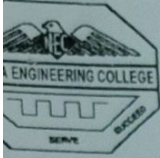
FEEDBACK ON SYLLABUS FROM STUDENTS

STUDENTS FEEDBACK 21-22

SURVEY COUNT: 78								
S.No	Question's	Excellent	Good	Fair	Poor	Total Weightage	Percentage	3-Scale Weightage
1	The curriculum and syllabus are well organized and easy to follow.	15	54	8	1	239	76.60%	2.3
2	The entire syllabus was covered by the faculty?	9	47	22	0	221	70.83%	2.13
3	Laboratory exercise improve my ability to understand concepts and helps to relate and apply theory to practice	27	36	14	1	245	78.53%	2.36
4	The depth of the syllabus is proportional to course outcomes	18	47	11	2	237	75.96%	2.28
5	The correct credit were allocated to the course depending on the difficulty of the course	28	44	6	0	256	82.05%	2.46
6	The Syllabus provide the necessary skill set required by the industry	17	47	10	4	233	74.68%	2.24
7	The books prescribed as reference material are relevant, updated and appropriate	16	42	17	3	227	72.76%	2.18
8	The elective offered are pertinent to the specification streams and to technology advancements	14	52	10	2	234	75.00%	2.25
9	Is Syllabus career oriented?	23	45	8	2	245	78.53%	2.36
10	Is the Pre-requisite course appropriate?	14	53	10	1	236	75.64%	2.27
11	Does the teacher provide examples of concepts/principles, description effectively?	27	42	8	1	251	80.45%	2.41
AVERAGE SCORE						238.55	76.46%	2.29

[Signature]
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STUDENT FEEDBACK REPORT ON CURRICULUM AND SYLLABUS (2021-2022) QUESTIONS

1. The curriculum and syllabus are well organized and easy to follow.
2. The entire syllabus was covered by the faculty?
3. Laboratory exercise improve my ability to understand concepts and helps to relate and apply theory to practice
4. The depth of the syllabus is proportional to course outcomes
5. The correct credit were allocated to the course depending on the difficulty of the course
6. The Syllabus provide the necessary skill set required by the industry
7. The books prescribed as reference material are relevant, updated and appropriate
8. The elective offered are pertinent to the specification streams and to technology advancements
9. Is Syllabus career oriented?
10. Is the Pre-requisite course appropriate?
11. Does the teacher provide examples of concepts/principles, description effectively?

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STUDENT FEEDBACK REPORT ON CURRICULUM AND SYLLABUS ANALYSIS AND ACTION TAKEN REPORT

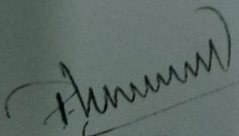
Feedback from the students is collected to strengthen the quality of teaching-learning environment and improve the performance of the students.

Analysis

- Most of the students satisfied with the laboratory exercises which improve the practical knowledge.
- Students agreed that the design of the course and the depth of the syllabus are proportional to course outcomes.
- 82.05% of students accepted that the accurate credit is allocated to the course by considering the complexity of the course.
- Most of the students suggested that the text books and reference books are adequate and map on to the syllabus.
- 75% of the students accepted that the elective courses are appropriate to the specification streams and technology advancements.
- Students are satisfied with the examples of concepts/principles and description provided by the teachers.
- Majority of the students reported that the placement activity of the college is excellent.

Suggestions

1. To include adequate industry oriented courses to fulfill the industry needs.
2. To modify the teaching learning process in mathematical subjects.
3. Suggested to include skill development courses in curriculum.


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Feedback Analysis and Action
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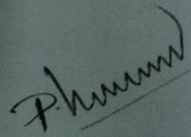
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FEEDBACK ON SYLLABUS FROM FACULTY ON SYLLABUS

FACULTY FEEDBACK 21-22

SURVEY COUNT: 13								
S.No	Question's	Excellent	Good	Fair	Poor	Total Weightage	Percentage	3-Scale Weightage
1	Curriculum and Syllabus on par with recent trends	6	7	0	0	45	86.54%	2.6
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4	Syllabus in accordance with Competitive Examinations	4	7	2	0	41	78.85%	2.37
5	Importance and Relevance of the course to Industry and Societal needs	7	5	1	0	45	86.54%	2.6
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7	Course equilibrium between theory and application	7	6	0	0	46	88.46%	2.65
8	Privilege to propose, modify, suggest and incorporate new topics in the syllabus through proper forum	7	6	0	0	46	88.46%	2.65
9	Flexibility in adopting new techniques / tools/ strategies in teaching	6	7	0	0	45	86.54%	2.6
10	Scope provided for Research activities	4	9	0	0	43	82.69%	2.48
AVERAGE SCORE						44	84.62%	2.54


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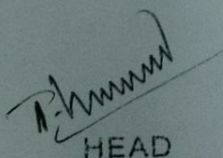
E.mail : info@nandhaengg.org

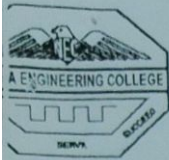
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FACULTY FEEDBACK REPORT ON CURRICULUM AND SYLLABUS

(2021-2022) QUESTIONS

1. Curriculum and Syllabus on par with recent trends
2. Elucidation of Course Outcomes
3. Adequacy of Academic tasks in the course plan
4. Syllabus in accordance with Competitive Examinations
5. Importance and Relevance of the course to Industry and Societal needs
6. Accessibility of Relevant Reading Materials and E -sources in the library
7. Course equilibrium between theory and application
8. Privilege to propose, modify, suggest and incorporate new topics in the syllabus through proper forum
9. Flexibility in adopting new techniques / tools/ strategies in teaching
10. Scope provided for Research activities


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FACULTY FEEDBACK REPORT ON CURRICULUM AND SYLLABUS

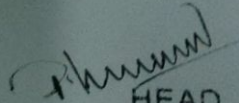
ANALYSIS AND ACTION TAKEN REPORT

Teachers' feedback is a vital factor in improving the quality of the curriculum which in turn improves the quality of the students. Teachers' feedback on curriculum is collected to evaluate every year and the necessary updation of the curriculum design and syllabus for the betterment of the student community.

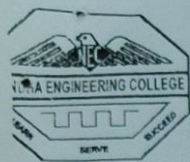
For the academic year 2021-22, feedback from the faculty members is received and analyzed. The following points are extracted from the analysis:

Analysis:

- 86.54% of the faculty members opined that the Curriculum and Syllabus framed is on par with recent trends.
- 82.69% of faculty members suggested that the elucidation of Course Outcomes is well defined.
- Most of the faculty members proposed to revise the Syllabus which make the students to get through the Competitive Examinations easily.
- Majority of the faculty members stated that the accessibility of Relevant Reading Materials and E-sources in the library is good.
- Some of the faculty members recommended to maintain Course equilibrium between theory and application.
- 86.54% of faculty members satisfied on the flexibility in adopting new techniques/tools in teaching – learning process.
- Most of the teachers suggested that, scope for research activities is to be improved.


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Feedback Analysis and Action
Taken Report for Employer



NANDHA ENGINEERING COLLEGE

(Autonomous)

Affiliated to Anna University Chennai ✦ Approved by AICTE ✦ Accredited by NBA-NewDelhi

Pitchandampalayam, (P.O), Vaikkalmedu, Erode - Perundurai Road, Erode - 638 052
 Phone : 04294-225585, 223711, 223722, 226393 Mobile : 73737 23722 Fax : 04294 - 224787
 E.mail : info@nandhaengg.org

ebsite : www.nandhaengg.org

FEEDBACK ON SYLLABUS FROM EMPLOYERS

EMPLOYERS FEEDBACK 21-22

SURVEY COUNT: 6								
S.No	Question's	Excellent	Good	Fair	Poor	Total Weightage	Percentage	3-Scale Weightage
1	Satisfaction with the caliber of the graduates	1	5	0	0	19	79.17%	2.37
2	Satisfaction that graduates are learning the right skills/courses relevant to your organization's requirements	1	4	1	0	18	75.00%	2.25
3	Satisfaction with the speed at which course content is being adapted to meet the changing industrial needs	2	4	0	0	20	83.33%	2.5
4	Institutional Reputation	1	5	0	0	19	79.17%	2.37
5	Relevant subject or Discipline Knowledge	0	5	1	0	17	70.83%	2.13
6	Quality of Employability Skills and Attributes of our graduates	3	3	0	0	21	87.50%	2.63
7	The Institution produces high quality graduates	0	6	0	0	18	75.00%	2.25
8	Successful past experience of recruiting from this institution	1	4	1	0	18	75.00%	2.25
9	Organization and institution collaborate on joint research projects	1	4	1	0	18	75.00%	2.25
10	Ability to apply professional and / or technical knowledge in the workplace	2	4	0	0	20	83.33%	2.5
AVERAGE SCORE						18.8	78.33%	2.35

[Signature]
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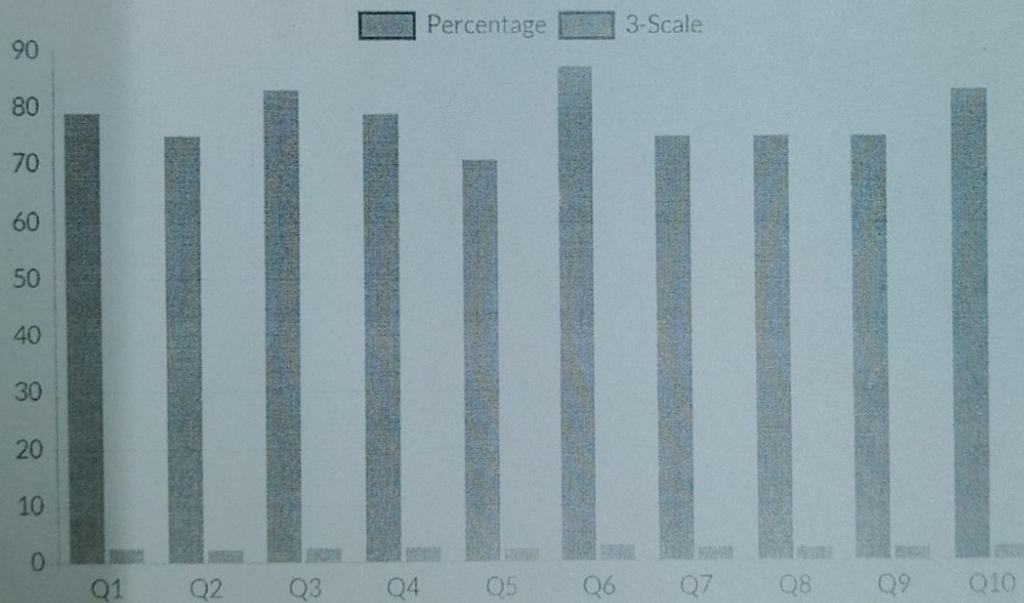
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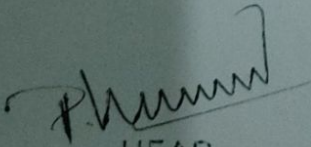
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EMPLOYER FEEDBACK REPORT ON CURRICULUM AND SYLLABUS (2021-22)




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