NANDHA ENGINEERING COLLEGE

(An Autonomous Institution affiliated to Anna University Chennai and approved by AICTE, New Delhi)
Erode-638 052, Tamilnadu, India, Phone: 04294 – 225585

Curriculum and Syllabi

for

B.E – Electronics and Communication Engineering [R22]

[CHOICE BASED CREDIT SYSTEM]

(This Curriculum and Syllabi are applicable to Students admitted of 2022-26 and 2023-27 Batches only)

April 2025

INSTITUTE VISION AND MISSION	
VISION	• To be an Institute of excellence providing quality Engineering, Technology and Management education to meet the ever changing needs of the society.
MISSION	• To provide quality education to produce ethical and competent professionals with social Responsibility • To excel in the thrust areas of Engineering, Technology and Entrepreneurship by solving real- world problems. • To create a learner centric environment and improve continually to meet the changing global needs.

B.E – ELECTRONICS AND COMMUNICATION ENGINEERING	
VISION	• To foster academic excellence imparting knowledge in Electronics, Communication and allied disciplines to meet the ever growing needs of the society.
MISSION	• To impart quality education and develop an aptitude for professional career and continuous learning with ethics and social responsibility. • To provide a framework for research and innovation to meet the emerging challenges through regular interaction with industry. • To create a learner centric environment by upgrading knowledge and skills to cater the needs and challenges of the society.
PROGRAMME EDUCATIONAL OBJECTIVES (PEO)	The graduates of Electronics and Communication Engineering will be **PEO1: Core Competency:** Successful professionals with core competency and inter-disciplinary skills to satisfy the Industrial needs. **PEO2: Research, Innovation and Entrepreneurship:** Capable of identifying technological requirements for the society and providing innovative ideas for real time problems. **PEO3: Ethics, Human values and Life-long learning: Able to demonstrate ethical practices and managerial skills through continuous learning.**
PROGRAMME SPECIFIC OUTCOMES (PSO)	The students of Electronics and Communication Engineering will be able to • Analyze, design and validate processes, products by applying knowledge and skills in Electronics, Antennas and Networks. • Design and analyze the process in VLSI and Embedded systems by selecting appropriate tools and techniques.

PROGRAM OUTCOMES:

At the end of this programme the students will be able to

a-l	GRADUATE ATTRIBUTES	PO No.	PROGRAMME OUTCOMES
a	Engineering Knowledge	PO1	Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
b	Problem Analysis	PO2	Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
c	Design and Development of Solutions	PO3	Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
d	Investigation of Complex Problems	PO4	Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
e	Modern Tool Usage	PO5	Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
f	The Engineer and Society	PO6	Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
g	Environment and Sustainability	PO7	Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
h	Ethics	PO8	Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
i	Individual and Team Work.	PO9	Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
j	Communication	PO10	Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
k	Project Management and Finance	PO11	Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
l	Lifelong Learning	PO12	Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

MAPPING OF PROGRAMME EDUCATIONAL OBJECTIVES WITH PROGRAMME OUTCOMES

A broad relation between the Programme Educational Objectives and the outcomes is given in the following table

PROGRAMME EDUCATIONAL OBJECTIVES	PROGRAMME OUTCOMES											
	A	B	C	D	E	F	G	H	I	J	K	L
1	3	3	2	3	2	1	1	2	1	1	3	1
2	3	3	3	3	3	1	1	1	1	1	1	2
3	3	3	3	3	3	2	2	3	1	2	2	2

MAPPING OF PROGRAM SPECIFIC OUTCOMES WITH PROGRAMME OUTCOMES

A broad relation between the Program Specific Objectives and the outcomes is given in the following table

PROGRAM SPECIFIC OUTCOMES	PROGRAMME OUTCOMES											
	A	B	C	D	E	F	G	H	I	J	K	L
1	3	3	2	3	2	1	1	1	1	1	1	2
2	3	3	3	3	3	2	2	3	1	3	3	3

Contribution 1: Reasonable 2: Significant 3: Strong

NANDHA ENGINEERING COLLEGE (AUTONOMOUS), ERODE – 638 052

REGULATIONS – R22 **CHOICE BASED CREDIT SYSTEM**

B.E. ELECTRONICS AND COMMUNICATION ENGINEERING

SEMESTER: I									
S. NO.	**COURSE CODE**	**COURSE TITLE**	**CATEGORY**	**PRE-REQUISITE**	**CONTACT PERIODS**	**L**	**T**	**P**	**C**
THEORY & EMBEDDED COURSES									
1	22EYA01	Professional Communication - I	HSMC	-	4	2	0	2	3
2	22MYB01	Calculus and Linear Algebra*	BSC	-	4	3	1	0	4
3	22CYB04	Engineering Chemistry	BSC	-	3	3	0	0	3
4	22CSC01	Problem Solving and C Programming	ESC	-	3	3	0	0	3
5	22ECC02	Basics of Electrical and Instrumentation Engineering	ESC	-	3	3	0	0	3
6	22GYA01	தமிழர் மரபு/ Heritage of Tamils	HSMC	-	1	1	0	0	1
PRACTICALS									
7	22CSP01	Problem Solving and C Programming Laboratory*	ESC	-	4	0	0	4	2
8	22CYP01	Chemistry Laboratory*	BSC	-	2	0	0	2	1
9	22GEP01	Engineering Practices Laboratory	ESC	-	4	0	0	4	2
MANDATORY NON CREDIT COURSES									
10	22MAN01	Induction Programme	MC	-	0	0	0	0	0
11	22MAN02	Soft /Analytical Skills - I	MC	-	3	1	0	2	0
12	22MAN03	Yoga – I *	MC	-	1	0	0	1	0
				TOTAL	**32**	**16**	**1**	**15**	**22**

*** Ratified by Eleventh Academic Council**

SEMESTER: II									
S. NO.	**COURSE CODE**	**COURSE TITLE**	**CATEGORY**	**PRE REQUISITE**	**CONTACT PERIODS**	**L**	**T**	**P**	**C**
THEORY & EMBEDDED COURSES									
1	22EYA02	Professional Communication- II	HSMC	22EYA01	4	2	0	2	3
2	22MYB04	Transforms and Partial Differential Equations*	BSC	-	4	3	1	0	4
3	22PYB03	Solid State Physics	BSC	-	3	3	0	0	3
4	22CSC02	Data Structures using C*	ESC	22CSC01	3	3	0	0	3
5	22ECC04	Electronic Devices and Circuits (Theory + Lab)	PCC	-	5	3	0	2	4
6	22GYA02	தமிழரும் தொழில்நுட்பமும் /Tamils and Technology	HSMC	-	1	1	0	0	1
PRACTICALS									
7	22PYP01	Physics Laboratory*	BSC	-	2	0	0	2	1
8	22CSP02	Data Structures Laboratory*	ESC	22CSP01	4	0	0	4	2
9	22MEP01	Engineering Graphics Laboratory	ESC	-	4	0	0	4	2
MANDATORY NON CREDIT COURSES									
10	22MAN04	Soft /Analytical Skills - II	MC	22MAN02	3	1	0	2	0
11	22MAN05	Yoga - II*	MC	-	1	0	0	1	0
				TOTAL	**34**	**16**	**1**	**17**	**23**

* **Ratified by Eleventh Academic Council**

SEMESTER: III									
S. NO.	**COURSE CODE**	**COURSE TITLE**	**CATEGORY**	**PRE-REQUISITE**	**CONTACT PERIODS**	**L**	**T**	**P**	**C**
THEORY & EMBEDDED COURSES									
1	22MYB06	Probability and Random Processes	BSC	-	4	3	1	0	4
2	22ECC05	Digital Logic Design	PCC	-	3	3	0	0	3
3	22ECC06	Signals and Systems	PCC	22MYB01, 22MYB04	3	3	0	0	3
4	22ECC07	Analog Electronics	PCC	22ECC04	3	3	0	0	3
5	22ECC08	Electromagnetic Waves	PCC	22PYB03	3	3	0	0	3
6	22ITC04	Algorithms	ESC	22CSC02	3	3	0	0	3
PRACTICALS									
7	22ECP02	Digital Logic Design Laboratory	PCC	-	4	0	0	4	2
8	22ECP03	Analog Electronics Laboratory	PCC	22ECC04	4	0	0	4	2
MANDATORY NON CREDIT COURSES									
9	22MAN07*	Soft / Analytical Skills - III#	MC	-	3	1	0	2	0
	22MAN07R*	Soft / Analytical Skills - III##							
10	22MAN09	Indian Constitution	MC	-	1	1	0	0	0
				TOTAL	**31**	**20**	**1**	**10**	**23**

Applicable for 2022-26 Batch only

Applicable for 2023-27 Batch only

SEMESTER: IV									
S. NO.	**COURSE CODE**	**COURSE TITLE**	**CATEGORY**	**PRE-REQUISITE**	**CONTACT PERIODS**	**L**	**T**	**P**	**C**
THEORY & EMBEDDED COURSES									
1	22ITC06	Java Programming	ESC	-	3	3	0	0	3
2	22ECC09	Analog Circuit Design	PCC	22ECC04	3	3	0	0	3
3	22ECC10	Transmission Lines and RF Systems	PCC	22ECC08	3	3	0	0	3
4	22ECC11	Digital Signal Processing*	PCC	22ECC06	5	3	0	2	4
5	22ECC12	Analog and Digital Communication*	PCC	22ECC06	3	3	0	0	3
PRACTICALS									

6	22ITP04	Java Programming Laboratory	ESC	-	4	0	0	4	2
7	22ECP04	Analog Circuit Design Laboratory	PCC	22ECC04 22ECC07	4	0	0	4	2
8	22ECP05	Analog and Digital Communication Laboratory*	PCC	22ECC06	4	0	0	4	2
MANDATORY NON CREDIT COURSES									
9	22MAN08*	Soft/Analytical Skills - IV#	MC	-	5	3	0	2	0
	22MAN08R*	Soft/Analytical Skills - IV##							
10	22GED01	Personality and Character Development	MC	-	1	0	0	1	0
				TOTAL	**35**	**18**	**0**	**17**	**22**

*** Ratified by Twelfth Academic Council**

 # Applicable for 2022-26 Batch only

Applicable for 2023-27 Batch only

SEMESTER: V									
S. NO.	**COURSE CODE**	**COURSE TITLE**	**CATEGORY**	**PRE-REQUISITE**	**CONTACT PERIODS**	**L**	**T**	**P**	**C**
THEORY & EMBEDDED COURSES									
1	22ECC13	Microprocessors and Microcontroller Interfacing	PCC	-	3	3	0	0	3
2	22ECC14	Data Communication Networks	PCC	-	3	3	0	0	3
3	22CYB06	Environmental Science and Sustainability	BSC	-	3	3	0	0	3
4	E1	Elective(PEC)	PEC	-	3	3	0	0	3
5	E2	Elective(PEC)	PEC	-	3	3	0	0	3
6	E3	Elective(PEC)	PEC	-	3	3	0	0	3
PRACTICALS									
7	22ECP06	Microprocessors and Microcontrollers Laboratory	PCC	-	4	0	0	4	2
8	22ECP07	Data Communication Networks Laboratory	PCC	-	4	0	0	2	2
MANDATORY NON CREDIT COURSES									
10	22MAN10R	Communication and Quantitative Reasoning	MC	-	3	1	0	2	0
				TOTAL	**29**	**19**	**0**	**11**	**22**

SEMESTER: VI									
S. NO.	**COURSE CODE**	**COURSE TITLE**	**CATEGORY**	**PRE-REQUISITE**	**CONTACT PERIODS**	**L**	**T**	**P**	**C**
THEORY & EMBEDDED COURSES									
1	22ECC15	VLSI and Chip Design	PCC	22ECC05	3	3	0	0	3
2	22ECC16	Embedded Systems and IOT Design	PCC	-	3	3	0	0	3
3	E4	Elective (PEC)	PEC	-	3	3	0	0	3
4	E5	Elective(PEC)	PEC	-	3	3	0	0	3
5	E6	Elective(PEC)	PEC		3	3	0	0	3
6	EM1	Elective (Management)*	HSMC	-	3	3	0	0	3
PRACTICALS									
7	22ECP08	VLSI Design Laboratory	PCC	22ECC05	4	0	0	4	2
8	22ECP09	Embedded Systems and IOT Design Laboratory	PCC	-	4	0	0	4	2
				TOTAL	**26**	**18**	**0**	**8**	**22**

SEMESTER: VII									
S. NO.	**COURSE CODE**	**COURSE TITLE**	**CATEGORY**	**PRE-REQUISITE**	**CONTACT PERIODS**	**L**	**T**	**P**	**C**
THEORY & EMBEDDED COURSES									
1	22GEA01	Universal Human Values	HSMC	-	2	2	0	0	2
2	E7	Elective(OEC/PEC)*	OEC/PEC	-	3	3	0	0	3
3	E8	Elective(OEC)	OEC	-	3	3	0	0	3
4	E9	Elective(OEC)	OEC		3	3	0	0	3
5	E10	Elective(OEC)	OEC	-	3	3	0	0	3
PRACTICALS									
6	22GED02	Internship/ Industrial Training	EEC	-	-	0	0	0	2
7	22ECD01	4	EEC	-	4	0	0	4	2
TOTAL					**18**	**15**	**0**	**4**	**18**

* **Ratified by Thirteenth Academic Council**

SEMESTER: VIII									
S. NO.	**COURSE CODE**	**COURSE TITLE**	**CATEGORY**	**PRE-REQUISITE**	**CONTACT PERIODS**	**L**	**T**	**P**	**C**
PRACTICALS									
1	22ECD02	Project Work - II	EEC	-	20	0	0	20	10
				TOTAL	**20**	**0**	**0**	**20**	**10**

REGULATIONS – 2022	**CHOICE BASED CREDIT SYSTEM**

(A) HSMC, BSC, HSC and MC

(a) Humanities and Social Sciences and Management Courses (HSMC)

S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1	22EYA01	Professional Communication - I	HSMC	-	4	2	0	2	3
2	22GYA01	தமிழர் மரபு /Heritage of Tamils	HSMC	-	1	1	0	0	1
3	22EYA02	Professional Communication- II	HSMC	22EYA01	4	2	0	2	3
4	22GYA02	தமிழரும் தொழில்நுட்பமும் /Tamils and Technology	HSMC	-	1	1	0	0	1
5	22GEA01	Universal Human Values	HSMC	-	2	2	0	0	2
6	EM1	Elective (Management)	HSMC	-	3	3	0	0	3

(b) Basic Science Courses(BSC)

S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1.	22MYB01	Calculus and Linear Algebra	BSC	-	4	3	1	0	4
2.	22CYB04	Engineering Chemistry	BSC	-	3	3	0	0	3
3.	22CYP01	Chemistry Laboratory	BSC	-	2	0	0	2	1
4.	22MYB04	Transforms Techniques and Partial Differential Equations	BSC	-	4	3	1	0	4
5.	22PYB03	Solid State Physics	BSC	-	3	3	0	0	3
6.	22PYP01	Physics Laboratory	BSC	-	2	0	0	2	1
7.	22MYB06	Probability and Random Processes	BSC	-	4	3	1	0	4
8.	22CYB06	Environmental Science and Sustainability	BSC	-	3	3	0	0	3

(c) Engineering Science Courses (ESC)									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1.	22CSC01	Problem Solving and C Programming	ESC	-	3	3	0	0	3
2.	22ECC02	Basics of Electrical and Instrumentation Engineering	ESC	-	3	3	0	0	3
3.	22CSP01	Problem Solving and C Programming Laboratory	ESC	-	4	0	0	4	2
4.	22GEP01	Engineering Practices Laboratory	ESC	-	4	0	0	4	2
5.	22CSC02	Data Structures using C	ESC	22CSC01	3	3	0	0	3
6.	22CSP02	Data Structures Laboratory	ESC	22CSP01	4	0	0	4	2
7.	22MEP01	Engineering Graphics Laboratory	ESC	-	4	0	0	4	2
8.	22ITC04	Algorithms	ESC	22CSC02	3	3	0	0	3
9.	22ITC06	Java Programming	ESC	-	3	3	0	0	3
10.	22ITP04	Java Programming Laboratory	ESC	-	4	0	0	4	2

(d) Mandatory Courses (MC)									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1.	22MAN01	Induction Programme	MC	-	0	0	0	0	0
2.	22MAN02	Soft /Analytical Skills - I	MC	-	3	1	0	2	0
3.	22MAN03	Yoga - I	MC	-	1	0	0	1	0
4.	22MAN04	Soft /Analytical Skills - II	MC	22MAN02	3	1	0	2	0
5.	22MAN05	Yoga - II	MC	-	1	0	0	1	0
6.	22MAN07 22MAN07R	Soft / Analytical Skills - III	MC	-	5	3	0	2	0
7.	22MAN09	Indian Constitution	MC	-	1	1	0	0	0
8.	22MAN08 22MAN08R	Soft/Analytical Skills - IV	MC	-	5	3	0	2	0
9.	22GED01	Personality and Character Development	MC	-	1	0	0	1	0
10.	22MAN10R	Communication and Quantitative Reasoning	MC	-	3	1	0	2	0

(B) Programme Core Courses (PCC)									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1.	22ECC04	Electronic Devices and Circuits (Theory + Lab)	PCC	-	5	3	0	2	4
2.	22ECC05	Digital Logic Design	PCC	-	3	3	0	0	3
3.	22ECC06	Signals and Systems	PCC	22MYB01, 22MYB04	3	3	0	0	3
4.	22ECC07	Analog Electronics	PCC	22ECC04	3	3	0	0	3
5.	22ECC08	Electromagnetic Waves	PCC	22PYB03	3	3	0	0	3
6.	22ECP02	Digital Logic Design Laboratory	PCC	-	4	0	0	4	2
7.	22ECP03	Analog Electronics Laboratory	PCC	22ECC04	4	0	0	4	2
8.	22ECC09	Analog Circuit Design	PCC	22ECC04	3	3	0	0	3
9.	22ECC10	Transmission Lines and RF Systems	PCC	22ECC08	3	3	0	0	3
10.	22ECC11	Digital Signal Processing	PCC	22ECC06	5	3	0	2	4
11.	22ECC12	Analog and Digital Communication	PCC	22ECC06	3	3	0	0	3
12.	22ECP04	Analog Circuit Design Laboratory	PCC	22ECC04 22ECC07	4	0	0	4	2
13.	22ECP05	Analog and Digital Communication Laboratory	PCC	22ECC06	4	0	0	4	2
14.	22ECC13	Microprocessors and Microcontrollers	PCC	-	3	3	0	0	3
15.	22ECC14	Data Communication Networks	PCC	-	3	3	0	0	3
16.	22ECP06	Microprocessors and Microcontrollers Laboratory	PCC	-	4	0	0	4	2
17.	22ECP07	Data Communication Networks Laboratory	PCC	-	4	0	0	2	2
18.	22ECC15	VLSI and Chip Design	PCC	22ECC05	3	3	0	0	3
19.	22ECC16	Embedded Systems and IOT Design	PCC	-	3	3	0	0	3
20.	22ECP08	VLSI Design Laboratory	PCC	22ECC05	4	0	0	4	2
21.	22ECP09	Embedded Systems and IOT Design Laboratory	PCC	-	4	0	0	4	2

(C) Programme Elective Courses (PEC)									
Vertical 1: Semiconductors									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1.	22ECX01	ASIC Design	PEC	-	3	3	0	0	3
2.	22ECX02	System on Chip Design	PEC	-	3	3	0	0	3
3.	22ECX03	System Verilog	PEC	22ECC05	3	3	0	0	3
4.	22ECX04	VLSI Testing and Testability	PEC	22ECC05	3	3	0	0	3
5.	22ECX05	Electronic System Design	PEC	22ECC04	3	3	0	0	3
6.	22ECX06	Electronic Circuit Board Design	PEC	-	3	3	0	0	3
7.	22ECX07	Semiconductor Device Modelling and Simulation	PEC	-	3	3	0	0	3
8.	22ECX08	Electronic System Packaging	PEC	-	3	3	0	0	3
Vertical 2: Communication									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1.	22ECX11	Mobile Communication	PEC	-	3	3	0		3
2.	22ECX12	Satellite Communication	PEC	-	3	3	0		3
3.	22ECX13	Optical Communication	PEC	22ECC10	3	3	0		3
4.	22ECX14	Information Theory and Coding	PEC	22ECC12	3	3	0		3
5.	22ECX15	Radar Communication	PEC	-	3	3	0		3
6.	22ECX16	Digital Communication receivers	PEC	22ECC12	3	3	0		3
7.	22ECX17	Software Defined Radio	PEC	-	3	3	0		3
8.	22ECX18	4G / 5G Communication Networks	PEC	-	3	3	0		3
Vertical 3: Networks									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1.	22ECX21	Computer System and Hardware	PEC	-	3	3	0	0	3
2.	22ECX22	Network Information Security	PEC	-	3	3	0	0	3
3.	22ECX23	Cryptography and Network Security	PEC	-	3	3	0	0	3
4.	22ECX24	High Performance Communication Networks	PEC	-	3	3	0	0	3
5.	22ECX25	Wireless Adhoc and Sensor Networks	PEC	-	3	3	0	0	3
6.	22ECX26	Automotive Electronics and Networking	PEC	-	3	3	0	0	3
7.	22ECX27	Neural Networks	PEC	-	3	3	0	0	3
8.	22ECX28	Artificial Intelligence	PEC	-	3	3	0	0	3

Vertical 4: Signal and Image Processing									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1.	22ECX31	Digital Image Processing	PEC	-	3	3	0	0	3
2.	22ECX32	Speech Signal Processing	PEC	-	3	3	0	0	3
3.	22ECX33	Multimedia Compression Techniques	PEC	-	3	3	0	0	3
4.	22ECX34	Deep Learning	PEC	-	3	3	0	0	3
5.	22ECX35	Computer Vision	PEC	-	3	3	0	0	3
6.	22ECX36	Machine Learning	PEC	-	3	3	0	0	3
7.	22ECX37	Soft Computing	PEC	-	3	3	0	0	3
8.	22ECX38	Pattern Recognition	PEC	-	3	3	0	0	3
Vertical 5: Embedded and IOT									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PREREQUISITE	CONTACT PERIODS	L	T	P	C
1.	22ECX41	Control Systems	PEC	-	3	3	0	0	3
2.	22ECX42	Virtual Instrumentation	PEC	-	3	3	0	0	3
3.	22ECX43	Wearable Devices	PEC	-	3	3	0	0	3
4.	22ECX44	Real Time Embedded Systems	PEC	22ECC13	3	3	0	0	3
5.	22ECX45	Internet Of Things & Its Applications	PEC	-	3	3	0	0	3
6.	22ECX46	IOT With Single Board Computers	PEC	-	3	3	0	0	3
7.	22ECX47	Industrial IOT And Industry 4.0	PEC	-	3	3	0	0	3
8.	22ECX48	Automation for Robotics	PEC	-	3	3	0	0	3

(C) MANAGEMENT ELECTIVES									
1.	22GEA02	Principles of Management	MEC	-	3	3	0	0	3
2.	22GEA03	Total Quality Management	MEC	-	3	3	0	0	3
3.	22GEA04	Professional Ethics and Human Values	MEC	-	3	3	0	0	3
4.	22GEZ01	Entrepreneurship Development #	MEC	-	3	3	0	0	3
(D) OPEN ELECTIVES									
1.	22ECZ01	Fundamentals of IoT	OEC	-	3	3	0	0	3
2.	22ECZ02	Sensors and transducers	OEC	-	3	3	0	0	3
3.	22ECZ03	Principles of Communication	OEC	-	3	3	0	0	3
4.	22ECZ04	VLSI technology	OEC	-	3	3	0	0	3

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(E) Employability Enhancement Courses (EEC)									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PRE REQUISITE	CONTACT PERIODS	L	T	P	C
1.	22GED02	Internship/ Industrial Training	EEC	-	4	0	0	0	2
2.	22ECD01	Project Work - I	EEC	-	4	0	0	4	2
3.	22ECD02	Project Work - II	EEC	-	20	0	0	20	10

Minor Courses									
Semi Conductor Technologies									
S. NO.	COURSE CODE	COURSE TITLE	CATEGORY	PRE REQUISITE	CONTACT PERIODS	L	T	P	C
1.	22ECM01	Semiconductor Physics	OEC	-	3	3	0	0	3
2.	22ECM02	Semiconductor Devices	OEC	-	3	3	0	0	3
3.	22ECM03	Semiconductor Device Modelling and Simulation	OEC	-	3	3	0	0	3
4.	22ECM04	Analog and Digital Electronics	OEC	-	3	3	0	0	3
5.	22ECM05	Semiconductor Optoelectronics	OEC	-	3	3	0	0	3
6.	22ECM06	Micro Electro Mechanical Systems	OEC	-	3	3	0	0	3
7.	22ECM07	Electronic system Packaging	OEC	-	3	3	0	0	3
8.	22ECM08	System on a chip Design	OEC	-	3	3	0	0	3

SUMMARY

S. No.	SUBJECT AREA	CREDITS AS PER SEMESTER								CREDITS TOTAL
		I	II	III	IV	V	VI	VII	VIII	
1.	HSMC	4	4	0	0	0	3#	2#	0	13
2.	BSC	8	8	4	0	3	0	0	0	23
3.	ESC	10	7	3	5	0	0	0	0	25
4.	PCC	0	4	16	17	10	10	0	0	57
5.	PEC	0	0	0	0	9	9#	3/0#	0	21/18
6.	OEC	0	0	0	0	0	0	9/12#	0	9/12
7.	EEC	0	0	0	0	0	0	4	10	14
CREDITS TOTAL		22	23	23	22	22	22	18	10	162
CREDITS %		8%	14.2%	35.2%	15.4%	8.6%	13%	7.4%		
AICTE CREDITS		12	25	48	24	15	24	12		160
AICTE %		8%	16%	30%	15%	9%	15%	8%		

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22EYA01 - PROFESSIONAL COMMUNICATION I *(Common to All Branches)*					
		L	**T**	**P**	**C**
		2	**0**	**2**	**3**
PRE REQUISITE : NIL					
Course Objective:	• To build essential English skills to address the challenges of communication • To enhance communication employing LSRW skills				

Course Outcomes The Student will be able to		**Cognitive Level**	**Weight age of COs in End Semester Examination**
CO1	Communicate effectively in various work environments.	R	20%
CO2	Involve in diverse discourse forms utilizing LSRW Skills.	U	20%
CO3	Participate actively in communication activities that enhance the creative skill.	U	20%
CO4	Associate with the target audience and contexts using varied types of communication.	Ap	20%
CO5	Convey the ideas distinctly both in verbal and non-verbal communication in work culture.	U	20%

UNIT I –INTRODUCTORY SKILLS	**(6+6)**
Grammar – Parts of Speech – Verb (Auxiliaries – Primary & Modal, Main Verb) -**Listening** – Listening to Short Conversations or Monologues - Listening to Experiences – Listening to Descriptions- **Speaking** – Introducing Oneself – Exchanging Personal information - Talking about food and culture - **Reading**– Reading for Interrogation – Reading Newspaper, Advertisements and Interpreting - **Writing** - Seeking Permission for Industrial Visit & In-plant Training	
UNIT II – LANGUAGE ACUMEN	**(6+6)**
Grammar – Word Formation – Tenses (Present Tense) – Synonyms & Antonyms - **Listening** – Listening to Announcements – Listening to Interviews - Listening and Note-taking - **Speaking** – Talking about Holidays & Vacations – Narrating Unforgettable Anecdotes - **Reading** – Skimming – Scanning (Short Texts and Longer Passages) – Critical Reading - **Writing** – Instruction – Process Description	
UNIT III – COMMUNICATION ROOTERS	**(6+6)**
Grammar– Cause and Effect – Tenses (Past Tense) – Discourse Markers - **Listening** – Listening to Telephonic Conversations – Listening to Podcasts - **Speaking** – Talking about neoteric Technologies – Eliciting information to fill a form - **Reading** –Book Reading(Motivational) - Practicing Speed Reading (reading newspaper reports & biographies) - **Writing** – Checklist – Circular, Agenda & Minutes of the Meeting	
UNIT IV - DISCOURSE FORTE	**(6+6)**
Grammar – Tenses (Future Tense) –Yes/No & WH type questions – Negatives - **Listening** – Listening to TED/ Ink talks -**Speaking** – Participating in Short Conversations - **Reading** – Reading Comprehension (Multiple Choice / Short / Open Ended Questions) - **Writing** - E-Mail Writing.	

UNIT V - LINGUISTIC COMPETENCIES	(6+6)
Grammar – Articles – Homophones & Homonyms – Single line Definition – Phrasal Verb - **Listening** – Intensive listening to fill in the gapped text - **Speaking** –Expressing opinions through Situations & Role play - **Reading** – Cloze Texts - **Writing** – Paragraph Writing	
LIST OF SKILLS ASSESSED IN THE LABORATORY 1. Grammar 2. Listening Skills 3. Speaking Skills 4. Reading Skills 5. Writing Skills	
TOTAL (L:30 , P:30) = 60 PERIODS	

TEXT BOOKS:

1. Shoba K N., Deepa Mary Francis. *English for Engineers and Technologists.* Volume 1, 3rd Edition, Orient Black Swan Pvt. Ltd, Telangana, 2022.

REFERENCES:

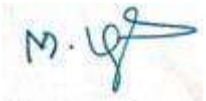
1. Koneru, Aruna. *English Language Skills.* Tata McGraw Hill Education (India) Private Limited, Chennai, 2006.
2. Hewings, M. *Advanced English Grammar.* Cambridge University Press, Chennai, 2000.
3. Jack C Richards, Jonathan Hull and Susan Proctor. *Interchange*. Cambridge University Press, New Delhi, 2015 (Reprint 2021).

WEB REFERENCE:

1. https://youtu.be/f0uqUzEf3A8?si=vyzu5KGIfbu35_IQ

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1									2	3				
2									2	3				
3									2	3				
4									2	3				
5									2	3				
CO (W.A)									2	3				



22MYB01-CALCULUS AND LINEAR ALGEBRA *(Common to All Branches)*				
	L	**T**	**P**	**C**
	3	**1**	**0**	**4**

PRE REQUISITE : NIL

Course Objective:	• To understand the mathematical concepts of matrices and analytical geometry in real time problems. • To formulate differential and integral equations to model physical, biological, and engineering systems

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the concepts of matrix theory for find solutions to complex problems efficiently.	Ap	20%
CO2	Analyze the geometric configurations and relationships by using Analytical geometry.	An	20%
CO3	Interpret the partial derivatives which involve heat conduction problems modeled by the heat equation.	Ap	20%
CO4	Apply the differential and integral techniques to solve the differential equations and multiple integrals in heat conduction, fluid mechanics and potential theory.	Ap	40%
CO5	Demonstrate the importance of matrix theory, analytical geometry and integral methods using programming tools.	Ap	Internal Assessment

UNIT I - MATRICES	**(9+3)**
Characteristic Equation - Eigen values and Eigen vectors of a matrix - Cayley Hamilton Theorem (excluding proof) and its applications - Quadratic form-Reduction of a Quadratic form to canonical form by orthogonal transformation.	
UNIT II – ANALYTICAL GEOMETRY OF THREE DIMENSIONS	**(9+3)**
Equation of plane – Angle between two planes – Equation of straight lines - Coplanar lines –Equation of sphere – Orthogonal spheres.	
UNIT III - GEOMETRICAL APPLICATIONS OF DIFFERENTIAL CALCULUS	**(9+3)**
Curvature – Curvature in Cartesian co-ordinates-Centre and Radius of curvature-Circle of curvature-Evolutes and Involutes.	
UNIT IV - FUNCTIONS OF SEVERAL VARIABLES	**(9+3)**
Partial derivatives - Euler's theorem on homogeneous function-Jacobian-Maxima and Minima of functions of two variables-Constrained Maxima and Minima by Lagrange's multiplier method.	
UNIT V - MULTIPLE INTEGRALS	**(9+3)**
Double integration in Cartesian Co-ordinates-Change of order of integration-Area as double integral- Triple integration in Cartesian Co-ordinates-Volume as triple integrals.	
TOTAL (L:45+T:15) :60 PERIODS	

LIST OF PROGRAMS USING MATLAB (Assignment/Online Test**)**:

1. Introduction to MATLAB
2. Matrix operations – Addition, Multiplication, Transpose and Inverse
3. Characteristic equation of a Matrix
4. Eigen values and Eigen vectors of Higher order Matrices.
5. Curve Tracing
6. Determining Maxima and Minima of a function of one variable.
7. Determining Maxima and Minima of a function of two variables.
8. Evaluating double integrals
9. Evaluating triple integrals
10. Finding area between two curves.

TEXT BOOKS:

1. Dr.B.S.Grewal, Higher Engineering mathematics, 42nd Edition, Khanna publications, 2012.
2. Erwin Kreyszig , Advanced Engineering mathematics , 9th Edition , John Wiley & sons ,2013
3. Veerarajan.T, Engineering Mathematics of semester I & II, 3rd Edition, Tata McGraw Hill. ,2016

REFERENCES:

1. N.P.Bali, Manish Goyal, "A text book of Engineering Mathematics -Sem-II", 6th Edition, Laxmi Publications, 2014.
2. Kandasamy.P, Thilagavathy.K, Gunavathy .K, "Engineering Mathematics for first year", 9th Rev.Ed, S.Chand & Co Ltd, 2013.
3. Glyn James, "Advanced Engineering Mathematics", 7th Edition, Wiley India, 2007.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		2												
3		2											2	1
4	3													
5	3				2				3			2	2	2
CO (W.A)	**3**	**2**			**2**				**3**			**2**	**2**	**1.5**

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22CYB04 - ENGINEERING CHEMISTRY *(Common to ECE and EEE Branches)*				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**
PRE REQUISITE : NIL				
Course Objective:	• To facilitate the students to achieve a clear conceptual understanding of electrochemistry, technical and commercial aspects of energy sources and storage devices. • To make the students conversant with water treatment, boiler feed water techniques, surface chemistry, polymers and various analytical techniques.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Predict the nature, oxidation and reduction potential of an electrode.	An	20%
CO2	Investigate on renewable energy sources like nuclear, solar, wind energy and also on storage devices.	E	20%
CO3	Identify the types of hardness in water and its removal by various water treatment techniques.	Ap	20%
CO4	Compare the relationship between various types of adsorption, polymers and polymer processing.	An	20%
CO5	Illustrate the principles, theory of analytical techniques and study about the nature of chemical compounds.	Ap	20%

UNIT I - ELECTROCHEMISTRY	**(9)**
Electrode potential - Nernst equation - derivation and problems - reference electrodes - standard hydrogen electrode -calomel electrode - electrochemical series - significance - Types of cell - electrolytic and electrochemical cells -reversible and irreversible cells - potentiometric titrations (redox) - conductometric titrations (acid-base).	
UNIT II - ENERGY SOURCES AND STORAGE DEVICES	**(9)**
Nuclear energy - nuclear fission - nuclear fusion - light water nuclear power plants - breeder reactor - solar energy conversion - solar cells - solar water heater - Recent developments in solar cell materials - wind energy - batteries - types of batteries - lead acid storage battery - lithium-ion battery, Electric vehicles - working principles.	
UNIT III - WATER TECHNOLOGY AND NANO MATERIALS	**(9)**
Municipal water treatment - disinfection methods (UV, ozonation, chlorination) - desalination of brackish water - reverse osmosis - boiler troubles (scale, sludge , priming, foaming and caustic embrittlement) - treatment of boiler feed water - internal treatment (carbonate, phosphate and calgon conditioning) - external treatment - demineralization process. Nanomaterials - synthesis (laser ablation, and chemical vapour deposition method) and applications of nanomaterials.	

UNIT IV - SURFACE CHEMISTRY AND POLYMERS	(9)
Surface chemistry - Adsorption - types - Differentiate between physical and chemical adsorption - Freundlich adsorption isotherm - Langmuir adsorption isotherm. Polymers - classification - addition - condensation - copolymerization – plastics - thermoplastics and thermosetting plastics - preparation, properties and uses of PVC and nylon- polymer processing - compression and injection moulding techniques.	
UNIT V - ANALYTICAL TECHNIQUES	**(9)**
Colorimetry - principles- estimation of Iron by colorimetry - UV-Visible spectroscopy- principles - instrumentation (block diagram only) - IR spectroscopy - principles - instrumentation (block diagram only) - Flame Photometry - principles - instrumentation (block diagram only) - estimation of sodium by flame photometry - Atomic absorption spectroscopy - principles - instrumentation (block diagram only) - estimation of nickel by atomic absorption spectroscopy.	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:

1. Dr.Ravikrishnan, A," Engineering Chemistry I & Engineering Chemistry II , Sri Krishna Hitech Publishing chem. Co. Pvt Ltd., 13th ed., Chennai, 2020.
2. S.S. Dara," A text book of Engineering Chemistry", S.Chand & Co. Ltd. New Delhi, 2019.

REFERENCES:

1. P.C.Jain and Monica Jain, "Engineering Chemistry", Vol I &II, Dhanpat Rai Pub, Co, New Delhi 15th ed.,2018.
2. B.Sivasankar, "Engineering Chemistry", Tata McGraw- Hill Pub. Co. Ltd., New Delhi,2018

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1			2				2							
2		2							2					2
3	3						2						3	
4		2	2											2
5						2						2		
CO (W.A)	**3**	**2**	**2**			**2**	**2**		**2**			**2**	**3**	**2**

22CSC01 - PROBLEM SOLVING AND C PROGRAMMING *(Common to All Branches)*					
		L	**T**	**P**	**C**
		3	**0**	**0**	**3**
PRE REQUISITE : NIL					
Course Objectives:	• To equip students with the essential skills and knowledge to solve computational problems using the C programming language.				

Course Outcomes The student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply basic syntax and semantics of C language to write clear and structured code.	Ap	20%
CO2	Make use of both conditional statements and iterative control structures for developing applications.	Ap	20%
CO3	Apply knowledge of arrays and strings to solve computational problems.	Ap	20%
CO4	Identify modular solutions that integrate problem-solving techniques to solve complex computational problems.	An	20%
CO5	Analyze the performance implications using pointers and to manage file operations efficiently.	An	20%

UNIT I -PROBLEM SOLVING AND C PROGRAMMING BASICS	**(9)**
General Problem Solving: Algorithms, Flowcharts and Pseudo-codes, implementation of algorithms **Basics of C Programming** : Introduction to C - Structure of C program - Programming Rules – Compilation – Errors - C Declarations: Tokens - keywords - identifiers - constants - data types - variable declaration and initialization - type conversion - constant and volatile variables - operators and expressions.	
UNIT II - DECISION CONTROL STATEMENTS	**(9)**
Managing Input and Output operations, Decision Control Statements: Decision control statements, Selection/conditional branching Statements: if, if-else, nested if statements. Basic loop Structures/Iterative statements: while loop, for loop, selecting appropriate loop. Nested loops break and continue statements.	
UNIT III - ARRAYS AND STRINGS	**(9)**
Introduction to Array - Definition - Array initialization - Characteristics - One Dimensional Array - Array operations -Two dimensional arrays -Strings and String handling functions.	
UNIT IV - FUNCTIONS	**(9)**
Functions: Basics - definition - Elements of User defined Functions - return statement, Function types, Parameter Passing Techniques, Function returning more values - Passing Array to Functions - Recursion - Storage classes.	

UNIT V - POINTERS AND FILE MANAGEMENT	(9)
Pointer concepts - Pointers & Arrays, Structure concepts - Defining, Declaring, Accessing Member Variables, Structure within Structure - Union - File Management in C- Dynamic Memory Allocation	
TOTAL (L:45) :45 PERIODS	

TEXT BOOKS:

1. Ashok N. Kamthane, "Programming in C", 2nd Edition, Pearson Education, 2013.
2. Sumitabha Das, "Computer Fundamentals and C Programming", 1st Edition, McGraw Hill, 2018.

REFERENCES:

1. R. G. Dromey, "How to Solve it by Computer", Pearson Education India; 1st Edition, ISBN10: 8131705625, ISBN-13: 978-8131705629
2. Maureen Spankle, "Problem Solving and Programming Concepts", Pearson; 9th Edition, India, ISBN-10: 9780132492645, ISBN-13: 978- 0132492645
3. Yashavant Kanetkar, "Let us C", 16th Edition, BPB Publications, 2018.
4. ReemaThareja., "Programming in C ", 2nd Edition, Oxford University Press, New Delhi, 2018.
5. Balagurusamy E., "Programming in ANSI C", 7th Edition, Mc Graw Hill Education, 2017.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												3	
2	3												3	
3	3											3	3	
4		3										3	3	
5		3											3	2
CO (W.A)	**3**	**3**										**3**	**3**	**2**

22ECC02 - BASICS OF ELECTRICAL AND INSTRUMENTATION ENGINEERING *(Common to ECE and BME Branches)*				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**

PRE REQUISITE : NIL

Course Objective:	• To understand the basics of Electrical Motor concepts, electrical transformer induction motor and synchronous motor. • To impart knowledge on the concepts of measuring and electronics instruments and various types of transducers.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the principles of electromagnetic induction in electrical applications.	Ap	30%
CO2	Apply the EMF equation and different starting methods in transformers and induction motors.	Ap	20%
CO3	Apply knowledge of various transducers and digital meters to select appropriate types for specific measurement applications.	Ap	30%
CO4	Analyze the various parameters to employ appropriate instruments to measure given sets of parameters.	An	20%
CO5	Give a presentation on recent technological development in the Analog Electronics domain.	U	Internal Assessment

UNIT I - D.C. MACHINES	**(9)**
DC Generators: Constructional details – Principle of operation – EMF Equation – Methods of excitation – Applications – DC Motor: Constructional details – Principle of operation – Torque Equation – Applications – Types of starters.	
UNIT II - TRANSFORMERS	**(9)**
Single phase Transformers: Constructional details – Principle of operation – EMF Equation – Transformation ratio – Equivalent circuit – Efficiency and Voltage Regulation – Applications.	
UNIT III - INDUCTION MOTORS	**(9)**
Three phase Induction Motor: Construction – Types – Principle of operation – Applications – Single phase Induction Motor: Construction – Principle of operation – Starting methods – Applications.	
UNIT IV - MEASUREMENTS AND INSTRUMENTATION	**(9)**
Functional elements of an instrument – Standards and calibration – Measurement Errors - types of error – Moving coil meters – Moving iron meters – CRO – Digital voltmeter: successive Approximation type.	
UNIT V -TRANSDUCERS	**(9)**
Transducers: Basic Requirements – Classification – Resistive: Strain gauge – Resistance Thermometer – Thermistor – Inductive: LVDT – Piezoelectric – Thermocouples.	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:

1. Kothari DP and I.J Nagrath, "Basic Electrical and Electronics Engineering", 2nd Edition, McGraw Hill Education, 2020.
2. A.K. Sawhney, Puneet Sawhney "A Course in Electrical & Electronic Measurements & Instrumentation", Dhanpat Rai and Co, New Delhi, 2015.

REFERENCES:

1. S. K, Bhattacharya, "Basic Electrical and Electronics Engineering", 2nd Edition, Pearson Education, 2017.
2. R.K.Rajput, "Electronic Measurements and Instrumentation", S.Chand & company Ltd, 2015.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3	3													
4		3											2	2
5						2			2	2				
CO (W.A)	**3**	**3**				**2**			**2**	**2**			**2**	**2**

22CSP01 - PROBLEM SOLVING AND C PROGRAMMING LABORATORY

(Common to All Branches)

	L	T	P	C
	0	0	4	2

PRE REQUISITE : NIL

Course Objective:	To develop programs to solve basic problems by understanding basic concepts in C language

Course Outcomes The student will be able to		**Cognitive Level**
CO1	Formulate the algorithms for simple problems	Ap
CO2	Apply the concept of pointers of different types	Ap
CO3	Apply and manipulate data with arrays, strings and structures	Ap
CO4	Apply the concept of functions and dynamic memory allocation	Ap
CO5	Analyze and correct logical errors encountered during execution	An

C-Programming:

1. Draw the flowchart for the following using Raptor tool.

a) Simple interest calculation
b) Greatest among three numbers
c) Find the sum of digits of a number

2. Programs for demonstrating the use of different types of operators like arithmetic, logical, relational and ternary operators (Sequential and Selection structures).
3. Programs for demonstrating repetitive control statements like 'for', 'while' and 'do-while' (Iterative structures).
4. Programs for demonstrating one-dimensional and two-dimensional numeric array.
5. Programs to demonstrate modular programming concepts using functions.
6. Programs to implement various character and string operations with and without built-in library functions.
7. Programs to demonstrate the use of pointers.
8. Programs to illustrate the use of user-defined data types.
9. Programs to implement various file management.
10. Program Using Dynamic memory allocation functions.

HARDWARE / SOFTWARE REQUIRED FOR A BATCH OF 30 STUDENTS:
Hardware: • LAN System with 33 nodes (OR) Standalone PCs – 33 Nos. • Printers – 3 Nos. **Software:** • RAPTOR Tool • Compiler – C
TOTAL (P:60) : 60 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												3	
2	3												2	
3	3												2	
4	3												2	
5		3			2							2	3	
CO (W.A)	**3**	**3**			**2**							**2**	**2.4**	

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22CYP01 CHEMISTRY LABORATORY *(Common to AGRI, BME, CHEM, CIVIL, ECE, EEE and MECH Branches)*				
	L	**T**	**P**	**C**
	0	**0**	**2**	**1**
PRE REQUISITE : NIL				

Course Objective:	• To determine the copper in brass in the given solution and explain the origin of hardness, alkalinity, chloride and dissolved oxygen in water. • To perform a potentiometric, conductometric titration and pH of an acidic solution of known Normality.

	Course Outcomes The Student will be able to	**Cognitive Level**
CO1	Predict the various water quality parameters by volumetric analysis.	An
CO2	Evaluate the amount of copper in the given solution by titration method.	E
CO3	Analyze the conductance and emf of the different solutions.	An
CO4	Analyze and gain experimental skill about potential of hydrogen ion.	An
CO5	Examine the pH of various acidic, basic and neutral solutions.	An

LIST OF EXPERIMENTS :

1. Determination of total, temporary & permanent hardness of water by EDTA method.
2. Determination of alkalinity in water sample.
3. Determination of chloride content of water sample by Argentometric method.
4. Determination of DO content of water sample by Winkler's method.
5. Estimation of copper in brass by EDTA.
6. Conductometric titration of strong acid vs strong base.
7. Estimation of iron content of the given solution using potentiometer.
8. Determination of strength of given hydrochloric acid using pH meter.

Total (30 P) = 30 periods

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1					3									
2							2							
3							2							
4					3									
5							2							
CO (W.A)					**3**		**2**							

- **Ratified by Eleventh Academic Council**

22GEP01 - ENGINEERING PRACTICES LABORATORY *(Common to AGRI, BME, CHEM, CIVIL, ECE, EEE and MECH Branches)*				
	L	**T**	**P**	**C**
	0	**0**	**4**	**2**
PRE REQUISITE : NIL				
Course Objective:	• To provide hands on training on various basic engineering practices in civil engineering • To provide hands on training on welding in mechanical engineering • To provide hands on training on various basic engineering practices in mechanical engineering • To understand the basic working principle of electric components • To understand the basic working principle of electronic components			

Course Outcomes The Student will be able to		**Cognitive Level**
CO1	Design new layouts of civil work for residential and industrial buildings.	Ap
CO2	Apply the concepts of welding in repairing works and making various components	Ap
CO3	Design new components using machining processes in real life and industries	Ap
CO4	Apply the skills of basic electrical engineering for wiring in different areas and Measure various electrical quantities	Ap
CO5	Apply electronic principles to measure various parameters of a signal.	Ap

GROUP-A (MECHANICAL AND CIVIL ENGINEERING)	
I - CIVIL ENGINEERING PRACTICE	**(15)**
Buildings: a. Study of plumbing and carpentry components of residential and industrial buildings, Safety aspects	
Plumbing: a. Study of tools and operations b. Hands-on-exercise: External thread cutting and joining of pipes	
Carpentry: a. Study of tools and operations b. Hands-on-exercise: "L" joint and "T" joint	
II - MECHANICAL ENGINEERING PRACTICE	**(15)**
Welding: a. Study of arc welding, gas welding tools and equipments b. Arc welding- Butt joints, Lap joints and Tee joints	

Basic Machining:

a. Study of lathe and drilling machine

b. Facing and turning

c. Drilling and Tapping

Sheet Metal Work:

a. Study of tools and operations

b. Rectangular tray

GROUP - B (ELECTRICAL AND ELECTRONICS)

I - ELECTRICAL ENGINEERING PRACTICE (15)

a. Residential house wiring using Switches, fuse, indicator, lamp.
b. Fluorescent lamp wiring.
c. Stair Case Wiring.
d. Measurement of electrical quantities –Voltage, current, power in R Circuit.
e. Study of Electrical apparatus-Iron box & water heater.
f. Study of Electrical Measuring instruments- Megger.

II - ELECTRONICS ENGINEERING PRACTICE (15)

a. Study of Electronic components and various use of multi meter.
b. Measurement of AC signal parameter (peak-peak, RMS period, frequency) using CRO.
c. Study of logic gates AND, OR, XOR and NOT.
d. Study of Clock Signal.
e. Soldering practice -Components Devices and Circuits - Using general purpose PCB.
f. Study of Half Wave Rectifier (HWR) and Full Wave Rectifier (FWR).

TOTAL (P: 60) = 60 PERIODS

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3												
3			2											
4	3												1	
5	3												1	
CO (W.A)	**3**	**3**	**2**										1	

22MAN01 - INDUCTION PROGRAMME
(Common To All Branches)

L	T	P	C
-	-	-	-

PRE REQUISITE : NIL

This is a mandatory 2 week programme to be conducted as soon as the students enter the institution. Normal classes start only after the induction program is over.

The induction programme has been introduced by AICTE with the following objective:

"Engineering colleges were established to train graduates well in the branch/department of admission, have a holistic outlook, and have a desire to work for national needs and beyond. The graduating student must have knowledge and skills in the area of his/her study. However, he/she must also have broad understanding of society and relationships. Character needs to be nurtured as an essential quality by which he/she would understand and fulfill his/her responsibility as an engineer, a citizen and a human being. Besides the above, several meta-skills and underlying values are needed."

"One will have to work closely with the newly joined students in making them feel comfortable, allow them to explore their academic interests and activities, reduce competition and make them work for excellence, promote bonding within them, build relations between teachers and students, give a broader view of life, and build character. "

Hence, the purpose of this programme is to make the students feel comfortable in their new environment, open them up, set a healthy daily routine, create bonding in the batch as well as between faculty and students, develop awareness, sensitivity and understanding of the self, people around them, society at large, and nature.

The following are the activities under the induction program in which the student would be fully engaged throughout the day for the entire duration of the program.

(i) Physical Activity

This would involve a daily routine of physical activity with games and sports, yoga, gardening, etc.

(ii) Creative Arts

Every student would choose one skill related to the arts whether visual arts or performing arts. Examples are painting, sculpture, pottery, music, dance etc. The student would pursue it everyday for the duration of the program. These would allow for creative expression. It would develop a sense of aesthetics and also enhance creativity which would, hopefully, grow into engineering design later.

(iii) Universal Human Values

This is the anchoring activity of the Induction Programme. It gets the student to explore oneself and allows one to experience the joy of learning, stand up to peer pressure, take decisions with courage, be aware of relationships with colleagues and supporting stay in the hostel and department, be sensitive to others, etc. A module in Universal Human Values provides the base. Methodology of teaching this content is extremely important. It must not be through do's and dont's, but get students to explore and think by engaging them in a dialogue. It is best taught through group discussions and real life activities rather than lecturing.

Discussions would be conducted in small groups of about 20 students with a faculty mentor each. It would be effective that the faculty mentor assigned is also the faculty advisor for the student for the full duration of the UG programme.

(iv) Literary Activity

Literary activity would encompass reading, writing and possibly, debating, enacting a play etc.

(v) Proficiency Modules

This would address some lacunas that students might have, for example, English, computer familiarity etc.

(vi) Lectures by Eminent People

Motivational lectures by eminent people from all walks of life should be arranged to give the students exposure to people who are socially active or in public life.

(vii) Visits to Local Area

A couple of visits to the landmarks of the city, or a hospital or orphanage could be organized. This would familiarize them with the area as well as expose them to the under privileged.

(viii) Familiarization to Dept./Branch & Innovations

They should be told about what getting into a branch or department means what role it plays in society, through its technology. They should also be shown the laboratories, workshops & other facilities.

(ix) Department Specific Activities

About a week can be spent in introducing activities (games, quizzes, social interactions, small experiments, design thinking etc.) that are relevant to the particular branch of Engineering/Technology/Architecture that can serve as a motivation and kindle interest in building things (become a maker) in that particular field. This can be conducted in the form of a workshop. For example, CSE and IT students may be introduced to activities that kindle computational thinking, and get them to build simple games. ECE students may be introduced to building simple circuits as an extension of their knowledge in Science, and so on. Students may be asked to build stuff using their knowledge of science.

Induction Programme is totally an activity based programme and therefore there shall be no tests / assessments during this programme.

REFERENCES:

1.Guide to Induction program from AICTE

22MAN02 - SOFT/ANALYTICAL SKILLS – I *(Common to All Branches)*				
	L	**T**	**P**	**C**
	1	**0**	**2**	**0**
PRE REQUISITE : NIL				
Course Objective:	• To understand the basic concepts of grammar and apply them in a structured Manner. • To solve mathematical problems and thereby reducing the time taken for performing job functions.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of Continuous Assessment Test**
CO1	Recognize and apply fundamental grammatical rules in both written and spoken contexts.	U	40%
CO2	Solve real-time problems for performing job functions easily.	Ap	30%
CO3	Enhance their aptitude round clearing ability in interview process.	An	30%

UNIT I – VERBAL ABILITY	**(5+10)**
Tenses - One word substitution- Articles – Preposition - Conjunction	
UNIT II – BASIC APTITUDE	**(5+10)**
Percentage – Ratio and Proportion – Blood Relations – Analogy	
UNIT III – LOGICAL REASONING	**(5+10)**
Probability – Profit and Loss - Syllogism - Statement Assumptions.	
TOTAL (L:15, P: 30) : 45 PERIODS	

REFERENCES:

1. Dr. R.S. Aggarwal, "A Modern Approach to Verbal & Non-Verbal Reasoning", S Chand and Company Limited, New Delhi, 2014.
2. Ashish Aggarwal, "Quick Arithmetic", S Chand and Company Limited, New Delhi, 2014.
3. Raymond Murphy, "English grammar in use", Fourth Edition, Cambridge University 2012.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1								2	3					
2	2		2										2	
3	2		2										2	
CO (W.A)	**2**		**2**					**2**	**3**				**2**	

22MAN03 - YOGA – I *(Common To All Branches)*				
	L	**T**	**P**	**C**
	0	**0**	**I**	**0**
PRE REQUISITE : NIL				

Course Objective:	• To make students in understanding the importance of yoga in shaping mental and physical wellness. • To provide awareness about the significance of leading a peaceful life by following yoga exercises and principles. • To develop mental wellbeing through meditation and breathing exercises. • To strengthen the body through physical exercises. • To inculcate the knowledge about different types of Asanas and their benefits

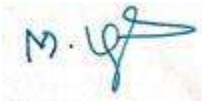
Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Understand the importance of yoga for physical and mental goodness.	U	Internal Assessment
CO2	Perform the yoga exercises for hand, leg, eye and sun salutation etc.	Ap	
CO3	Learn and practice meditation techniques for keeping good mental health	Ap	
CO4	Develop their body by performing yoga exercises.	Ap	
CO5	Demonstrate different types of yoga Asanas for improving their personal fitness.	Ap	

UNIT I – INTRODUCTION TO YOGA	(3)
Meaning and Importance of Yoga - Elements of Yoga - Introduction - Asanas, Pranayama, Meditation and Yogic Kriyas - Yoga for concentration & related Asanas (Sukhasana; Tadasana; Padmasana and Shashankasana) - Relaxation Techniques for improving concentration - Yog-nidra.	
UNIT II - YOGA AND LIFE STYLE	(3)
Asanas as Preventive measures – Hypertension:Tadasana, Vajrasana, Pavan Muktasana, Ardha Chakrasana, Bhujangasana, Sharasana – Obesity: Procedure, Beneits and contraindications for Vajrasana, Hastasana, Trikonasana, Ardh Matsyendrasana – Back Pain: Tadasana, Ardh Matsyendrasana, Vakrasana, Shalabhasana, Bhujangasana - Diabetes: Procedure, Benefits and contraindications for Bhujangasana, Paschimottasana, Pavan Muktasana, Ardh Matsyendrasana – Asthema: Procedure, Benfits and contraindications for Sukhasana, Chakrasana, Gomukhasana, Parvatasana, Bhujangasana, Paschimottasana, Matsyasana.	
UNIT III – MIND EXERCISES	(3)
Naadi sudhi – Thanduvada sudhi – Breathing meditation – Silent meditation – Relax meditation.	

UNIT IV – PHYSICAL EXERCISES (PART– I)	(3)
Hand Exercises – Leg Exercises – Eye Exercises – Sun Salutation.	
UNIT V – ASANAS (PART-I)	(3)
Asanas –Tadasana – Yegapadhasana – Chakrasana – Udkaddasana – Thirikosana – Thandasana – Paschimottanasana.	
TOTAL (P:15) : 15 PERIODS	

TEXT BOOKS/REFERENCES:
1. Light On Yoga by B.K.S. Iyengar.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1								3	2			3		
2								3	2			3		
3								3	2			3		
4								3	2			3		
5								3	2			3		
CO (W.A)								**3**	**2**			**3**		



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22EYA02- PROFESSIONAL COMMUNICATION- II *(Common to All Branches)*					
		L	**T**	**P**	**C**
		2	**0**	**2**	**3**
PRE REQUISITE : 22EYA01					
Course Objective:	• To enhance the students with necessary English language skills • To enable students to communicate effectively in an academic setting				
Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**		
CO1	Frame sentences both in written and spoken forms with accuracy and fluency.	R	20%		
CO2	Use linguistic structures to read and understand well-structured texts encountered in academic or social contexts.	U	20%		
CO3	Gain essential competency to express one's thoughts orally and in writing in a meaningful way.	U	20%		
CO4	Attain and enhance competence in the four modes of literacy: Listening, Speaking, Reading and Writing.	Ap	20%		
CO5	Perform various tasks, such as role plays, debates, group discussions apart from the use of correct spelling and punctuation.	U	20%		

UNIT I – LANGUAGE RUDIMENTS	**(6+6)**
Grammar – Active and Passive Voice – Impersonal Passive Voice – Numerical Expressions - **Listening** – Listening for Specific Information and Match / Choose / Fill in the texts - **Speaking** – Describing a Person - Making Plans -**Reading** – Intensive Reading -**Writing** – Job Application with Resume.	
UNIT II - RHETORIC ENHANCERS	**(6+6)**
Grammar – Reported Speech – Infinitive and Gerund - **Listening** – Listening to Iconic Speeches and making notes - Listening news / documentaries - **Speaking** –Talking over Phone – Narrating Incidents - **Reading** – Extensive Reading (Motivational Books) - **Writing** – Recommendation	
UNIT III – TECHNICAL CORRESPONDENCE	**(6+6)**
Grammar – If Conditionals – Blended Words - **Listening** – Listening to business conversation on audio and video of Short Films, News, Biographies - **Speaking** – Synchronous communication and Asynchronous communication - Opportunities and threats in using digital platform- **Reading** - Finding key information in a given text - **Writing** –Netiquettes- Inviting Dignitaries - Accepting & Declining Invitation	
UNIT IV - CORPORATE COMMUNICATION	**(6+6)**
Grammar – Concord – Compound Words - **Listening** – Listening to Roles and Responsibilities in Corporate - Listening to technical videos - **Speaking** – Introduction to Technical Presentation - Story Telling - **Reading** – Reading and Understanding Technical Articles - **Writing** – Report Writing (Accident, Survey and feasibility)	

UNIT V - LANGUAGE BOOSTERS	(6+6)
Grammar - Idiomatic Expressions – Relative Clauses – Confusable words - **Listening** – Listening to different kinds of Interviews - Listening to Group Discussion - **Speaking** – Group Discussion - **Reading** – Reading and Interpreting Visual Materials - **Writing** – Analytical Paragraph Writing	

LIST OF SKILLS ASSESSED IN THE LABORATORY

1. Grammar.
2. Listening Skills.
3. Speaking Skills.
4. Reading Skills
5. Writing Skills

TOTAL (L:30 , P:30) = 60 PERIODS

TEXT BOOK:

1. Sudharshana, N.P and Saveetha.C, "English for Technical Communication", Cambridge University Press, New Delhi, 2016 (Reprint 2017).

REFERENCES:

1. Rizvi, M Ashraf, "Effective Technical Communication", Second Edition, McGraw Hill Education India Pvt Ltd, 2017.
2. Rodney Huddleston, Geoffrey K. Pullum and Brett Reynolds, "A Student's Introduction to English Grammar", Second Edition, Cambridge University Press, New Delhi, 2022

WEB REFERENCE:

1. http://youtu.be/URtdGiutVew

Mapping of COs with POs / PSOs

Cos	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1									2	3				
2									2	3				
3									2	3				
4									2	3				
5									2	3				
CO (W.A)									**2**	**3**				

22MYB04 – TRANSFORMS AND PARTIAL DIFFERENTIAL EQUATIONS
(Common to BME and ECE Branches)

	L	T	P	C
	3	1	0	4

PRE REQUISITE : NIL

Course Objective:	• To make the Conversant with concepts of Z-transforms, Fourier series, Fourier transforms to represent periodical physical problems in engineering analysis. • To provide adequate knowledge in partial differential equation and to analyze the boundary value problems

	Course Outcomes The Student will be able to	Cognitive Level	Weightage of COs in End Semester Examination
CO1	Interpret the Fourier series in various fields such as signal processing, communications, control systems, and biomedical engineering.	Ap	30%
CO2	Solve the initial and boundary value problems by using Fourier series in wave equation.	Ap	30%
CO3	Apply the methods of partial differential equations in Circuit Analysis and Biomedical Signal Processing.	Ap	20%
CO4	Analyze the concepts of Transform Techniques to solve the engineering problem.	An	20%
CO5	Identify the mathematical tools for solving transform techniques in real time applications.	Ap	Internal Assessment

UNIT I – FOURIER SERIES	**(9+3)**
Dirichlet's condition – Fourier series – Odd and even functions – Half range sine series – Half range cosine series – Parseval's identity – RMS value – Harmonic Analysis.	
UNIT II – PARTIAL DIFFERENTIAL EQUATIONS	**(9+3)**
Formulation of partial differential equations by eliminating arbitrary constants and functions – Solution of standard types first order partial differential equations of the type f(p,q)=0,Clairaut's form – Lagrange's linear equations –Linear partial differential equation of second and higher order with constant coefficient of homogeneous types.	
UNIT III – APPLICATIONS OF PARTIAL DIFFERENTIAL EQUATIONS	**(9+3)**
Classification of second order Quasi linear partial differential equations – Solution of one dimensional wave equation (Zero and non-zero velocity) – One dimensional heat equation (Temperature reduced to zero and non zero boundary conditions) – Steady state solution of two dimensional heat equation(Finite and infinite plate).	
UNIT IV – FOURIER TRANSFORM	**(9+3)**
Fourier integral theorem(Statement only) – Fourier transform pair - Sine and Cosine transforms – Properties -Transforms of simple functions – Convolution theorem – Parseval's identity(Excluding proof).	

UNIT V – Z-TRANSFORM AND DIFFERENCE EQUATIONS	(9+3)
Z-transforms – Elementary properties – Inverse Z-transform (Partial fraction method and Residue method) – Convolution theorem (Excluding proof) – formation of difference equations – Solution of difference equation using Z transform.	
TOTAL (L:45+T:15) :60 PERIODS	

TEXT BOOKS:

1. Veerajan.T,"Engineering Mathematics (for semester III), 3rd ed., Tata Mc Graw Hill, New Delhi.
2. Kandasamy.P, Thilagavathy.K, and Gunavathy. K., "Engineering Mathematics; Volume III", S.Chand & Coltd., 2008.
3. Grewal B.S,"Higher Engineering Mathematics", 42nd ed., Khanna publishers, New Delhi, 2012.

REFERENCES:

1. Goyal Manish and Bali. N.P,"A Text book of Engineering mathematics", 6th ed., Laxmi Publication (P) Ltd, New Delhi, 2012.
2. Kreyszig, Erwin,"Advanced Engineering Mathematics", 9th ed., Wiley Publications, New Delhi, 2006.
3. Singaravelu.A,"Transforms and Partial Differential Equations", Reprint Edition 2013, Meenakshi Publications, Tamilnadu.

WEB REFERENCES:

1. https://youtu.be/B025y1UWkvI
2. https://youtu.be/IkAvgVUvYvY
3. https://youtu.be/RtVE2Gt-KQ4
4. https://youtube.com/playlist?list=PLs7oDAL8_ouKSagWiC_IwrEsRwvD2WJ73

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3	3													1
4		2											2	
5	3				2				3			3	1	
CO (W.A)	**3**	**2**			**2**				**3**			**3**	**1.5**	**1**

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22PYB03 - SOLID STATE PHYSICS (*Common to ECE, EEE & BME)*				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**
PRE REQUISITE : NIL				
Course Objective:	• To gain adequate information about the properties of matter and properties of nanostructures. • To expose the concepts of Photonics and fiber optics and Advanced new engineering materials			

Course Outcomes The student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply principles of semiconductor physics to the design and optimization of semiconductor-based biomedical equipment.	Ap	20%
CO2	Employ their knowledge of dielectric properties to optimize and enhance the performance of electronic components such as capacitors and transformer.	Ap	20%
CO3	Examine how magnetic moments and superconductivity are utilized in the design of biomedical devices like MRI machines and magnetic sensors.	An	20%
CO4	Analyze the impact of fabrication techniques on enhancing the performance and efficiency of microprocessors.	An	20%
CO5	Evaluate how the properties and preparation methods of advanced materials can be utilized to develop innovative solutions in material science.	Ev	20%

UNIT I – SEMICONDUCTING MATERIALS	**(9)**
Introduction to semiconducting materials – Elemental and compound semiconductors – Intrinsic semiconductor – carrier concentration derivation – variation of Fermi level with temperature – electrical conductivity – band gap determination – extrinsic semiconductors (qualitative) – Hall effect – determination of Hall coefficient – Applications	
UNIT II – DIELECTRIC MATERIALS	**(9)**
Electrical susceptibility – dielectric constant – electronic, ionic, orientation and space charge polarization – frequency and temperature dependence of polarization – internal field – Claussius – Mosotti relation (derivation) – dielectric loss – dielectric breakdown – uses of dielectric materials (capacitor and transformer) – ferro electricity and applications.	
UNIT III – MAGNETIC AND SUPERCONDUCTING MATERIALS	**(9)**
Origin of magnetic moment – Bohr Magneton – Types of magnetic materials – Domain theory – Hysteresis – soft and hard magnetic materials – Ferrites – applications – Superconductivity – properties – types of superconductors – BCS theory of superconductivity (qualitative) – High Tc superconductors – Application of superconductors – Magnetic levitation.	

UNIT IV – FABRICATION PROCESS OF INTERGATED CIRCUITS	(9)
Bulk crystal growth – Epitaxial growth – masking and etching-diffusion of impurities-selective diffusion – Formation of PN junction – resistors – capacitors – inductors – isolation methods – metal semiconductor contact – Introduction to integrated circuit – monolithic and hybrid circuits – Thin film and Thick film technology – Definition of LSI, MSI, VLSI circuits.	
UNIT V – ADVANCED MATERIALS AND NANO TECHNOLOGY	**(9)**
Metallic glasses: preparation, properties and applications – Shape Memory Alloys (SMA): Characteristics, properties of NiTi alloy, application – Nano materials: Properties, Preparation – Pulsed laser deposition – chemical vapour deposition of nano particles and applications – Carbon nano tubes: fabrication – arc method – structure – properties and application.	
TOTAL (L: 45) = 45 PERIODS	

TEXT BOOKS:

1. M.N.Avadhanulu and P.G.Kshirsagar, "A text book of Engineering Physics", S. Chand and Company, New Delhi, 2019.
2. A.Marikani, "Materials Science", PHI Learning Private Limited, Eastern Economy Edition, 2017.
3. M.A.Wahab, "Solid State Physics", 3rd edition ,Narosa Publishing House Pvt.Ltd., 2016.

REFERENCES:

1. B.Rogers , J. Adams and S.Pennathur, "Nanotechnology : Understanding Small System" CRC Press, 2017.
2. Jacob Millman, Charistos C Halkilas, SatyabrataJit "Electronic Devices & Circuits", Tata McGraw Hill, Education Private Limited, 2016, Third Edition.
3. Subrahmanyam N, Brijlal, "A Text Book Of Optics" S.Chand & Co. Ltd, New Delhi, 2019.

WEB LINKS:

1. https://bayanbox.ir/view/7764531208313247331/Kleppner-D.-Kolenkow-R.J.-Introduction-to-Mechanics-2014.pdf.
2. https://physicaeducator.files.wordpress.com/2017/11/electricity_and_magnetism-by-purcell-3ed-ed.pdf.
3. https://rajeshvcet.home.blog/regulation-2021/ph3151-engineering-physics-study-materials/
4. https://zenodo.org/record/243407#.ZEgPZXZBzIU
   https://farside.ph.utexas.edu/teaching/qmech/qmech.pdf.
6. https://web.pdx.edu/~pmoeck/phy381/workbook%20nanoscience.pdf.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3	2												
2	3													
3	3													
4	3		3				2						3	
5	3					2	2					2		
CO (W.A)	**3**	**2**	**3**			**2**	**2**					**2**	**3**	

22CSC02 –DATA STRUCTURES USING C *(Common to 22AIC01, 22CCC01, 22CIC01 and 22ITC01)*					
		L	**T**	**P**	**C**
		3	**0**	**0**	**3**
PRE REQUISITE : 22CSC01					
Course Objective:	• To develop skills to apply appropriate data structures in problem solving. • To apply abstract data types (ADTs), recursion, algorithms for searching and sorting, and basic algorithm analysis.				

Course Outcomes The student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply pointer and array concepts in functions.	Ap	20%
CO2	Solve problems using various implementations of linked list.	Ap	20%
CO3	Make use of ADTs like stack and queue for solving real world problems	Ap	20%
CO4	Analyze the tree traversal algorithms for various non-linear data structures.	An	20%
CO5	Analyze appropriate graph algorithms for computing problems	An	20%

UNIT I - POINTERS USING ARRAYS AND STRINGS	**(9)**
Pointers: Introduction – Pointers and arrays– passing an array to a function– returning an array from function – NULL pointers –Array of pointers – Pointer-to-pointer – Dangling Pointer. Function pointers: calling a function using function pointer- Using pointer as a function argument	
UNIT II - LIST	**(9)**
Abstract Data Types (ADTs) – List ADT – Array-based implementation – Linked list implementation – Singly linked lists – Circularly linked lists – Doubly-linked lists – Applications of lists – Polynomial ADT	
UNIT III - STACKS AND QUEUES	**(9)**
Stack ADT – Operations – Applications – Balancing Symbols – Evaluating arithmetic expressions Infix to Postfix conversion – Function Calls – Queue ADT – Operations – Circular Queue – DeQueue – Applications of Queues	
UNIT IV - TREE	**(9)**
Tree ADT – Tree Traversals - Binary Tree ADT – Expression trees – Binary Search Tree ADT – AVL Trees – Priority Queue (Heaps) – Binary Heap.	
UNIT V - GRAPHS	**(9)**
Definitions – Representation of Graphs – Types of Graph – Graph Traversal: Depth-First Search (DFS) – Breadth-First Search (BFS) – Topological Sort – Applications of DFS: Bi-connectivity – Euler Circuits – Finding Strongly Connected Components – Applications of BFS: Bipartite Graph.	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:

1. Sumitabha Das, "Computer Fundamentals &C Programming", McGraw Hill Education (India) Private Limited, 1st Edition, 2018.
2. Weiss M. A., "Data Structures and Algorithm Analysis in C", 2nd Edition, Pearson Education, 2016.

REFERENCES:

1. Yashavant Kanetkar, "Pointers in C", BPP Publications, 4th Edition, 2017.
2. Pradip Dey, Manas Ghosh, "Programming in C", Oxford Higher Education, 2nd Edition, 2016.

Mapping of COs with POs / PSOs

Cos	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3											3		
2	3											3		
3	3											3		
4		3										3	1	
5		3										3	1	1
CO (W.A)	**3**	**3**										**3**	**1**	**1**

- **Ratified by Eleventh Academic Council**

22ECC04 - ELECTRONIC DEVICES AND CIRCUITS *(Common to ECE and BME Branches)*					
		L	**T**	**P**	**C**
		3	**0**	**2**	**4**
PRE REQUISITE : NIL					
Course Objective:	• To examine the basics of Semiconductor Diodes and its characteristics • To analyze the characteristics of Bipolar Junction Transistor and FET and operation of Special semiconductor diodes. • To design simple network by exploring circuit theorems using basics of Electrical circuits				

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the Ohm's law and Kirchhoff's law to investigate the behavior of electric circuits by analytical techniques	Ap	20%
CO2	Analyze the characteristics and operational principles of Diodes, BJT, FET and MOSFET.	An	30%
CO3	Analyze the laws applicable for Mesh current method and Nodal voltage method and solve the circuits.	An	30%
CO4	Design a fundamental electrical network using circuit theorems, encompassing both AC and DC principles.	E	20%
CO5	Engage in collaborative learning sessions aimed at creating fundamental electronic projects.	U	Internal Assessment

UNIT I – PN DIODE AND BJT	**(9)**
Formation of PN junction – working principle – VI characteristics – PN diode currents – Switching Characteristics. NPN and PNP transistors – Current equations – Input and Output characteristics of CE, CB, CC Configurations.	
UNIT II – FET AND SPECIAL DIODES	**(9)**
JFET – Drain and Transfer Characteristics - MOSFET – Characteristics. Zener diode, Varactor diode, Tunnel diode, PIN diode, LDR	
UNIT III – BASICS OF CIRCUIT ANALYSIS	**(9)**
Ohms Law, Kirchhoff's Current Law, Kirchhoff's voltage law, Resistors in Series and Parallel, voltage and current division, Nodal analysis, Mesh analysis. Delta-Wye Conversion	
UNIT IV - NETWORK THEOREMS FOR DC	**(9)**
Linearity and superposition, Thevenin and Norton Equivalent Circuits, Maximum Power Transfer, Reciprocity theorem.	
UNIT V -NETWORK THEOREMS FOR AC	**(9)**
Thevenin's theorem, Norton's theorem, Superposition theorem, Maximum power transfer theorem-Reciprocity theorem	

LIST OF EXPERIMENTS :

1. Plot the Characteristics of PN Junction Diode and Zener Diode.
2. Plot the Input-Output characteristics of common Emitter and common Base configuration.
3. Plot FET Characteristics.
4. Verification of KVL and KCL
5. Verification of Thevenin and NortonsTheorem.
6. Verification of Superposition Theorem and Reciprocity Theorem.

TOTAL (L:45+P:30) : 75 PERIODS

TEXT BOOKS:

1. Robert L. Boylestad, Louis Nashelsky, "Electronic Devices and Circuit Theory", 2nd ed., Pearson Education, 2019.
2. Charles K. Alexander, Matthew N. O. Sadiku, "Fundamentals of Electric Circuits", 2nd ed, McGraw-hill Education, 2017 .

REFERENCES:

1. S. Salivahanan, N. Suresh kumar and A. Vallavanraj, "Electronic Devices and Circuits", Tata McGrawHill Third Edition, 2013
2. David A. Bell, "Electronic Devices and Circuits", Oxford University Press, 5th Edition, 2008
3. William H. Hayt Jr, Jack E. Kemmerly and Steven M. Durbin, "Engineering Circuits Analysis", 8th edition., Tata McGraw Hill publishers, New Delhi, 2013

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3		3											2	
4			3										2	2
5									2		2	3	2	2
CO (W.A)	**3**	**3**	**3**						**2**		**2**	**3**	**2**	**2**

22PYP01 - PHYSICS LABORATORY (*Common to All Branches*)					
		L	**T**	**P**	**C**
		0	**0**	**2**	**1**
PRE REQUISITE : NIL					
Course Objective:	• To infer the practical knowledge by applying the experimental methods to correlate with the Physics theory. • To introduce different experiments to test basics of physics concepts applied in optics and electronics				

Course Outcomes The Student will be able to		**Cognitive Level**
CO1	Examine the effects of material type and loading conditions on the results of the non-uniform bending experiment.	An
CO2	Utilize principles of light interaction to determine the particle size of materials using laser diffraction techniques.	Ap
CO3	Evaluate the accuracy of the wavelength of different colors with the accepted values in the literature	E
CO4	Measure the effectiveness of the solar cell based on its V-I characteristics.	E
CO5	Analyze the principles underlying the Air wedge method for the determination of the thickness of a thin wire,	An

LIST OF EXPERIMENTS

1. Determination of Young's modulus by non-uniform bending method
2. Determination of (a) wavelength and (b) particle size using Laser.
3. Determination of thermal conductivity of a bad conductor – Lee's Disc method.
4. Determination of wavelength of mercury spectrum – spectrometer grating
5. Determination of band gap of a semiconductor.
6. Determination of thickness of a thin wire – Air wedge method.
7. Determination of V-I characteristics of solar cell.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3	3												
2	3											2		
3	3	3												
4	3											2		
5	3	3											1	1
CO (w.a)	**3**	**3**										**2**	**1**	**1**

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22CSP02 – DATA STRUCTURES LABORATORY *(Common to 22AIP01, 22CCP01, 22CIP01 and 22ITP01)*					
		L	**T**	**P**	**C**
		0	**0**	**4**	**2**
PRE REQUISITE : 22CSP01					
Course Objective:	To understand the fundamental concepts of data structures, including arrays, linked lists, stacks, queues, trees, and graphs.				
Course Outcomes The students will be able to					**Cognitive Level**
CO1	Applying pointers and implement array operations				Ap
CO2	Analyze different steps on linked lists.				An
CO3	Capable of working with stack and queue principles.				An
CO4	Cable to creating and modifying a variety of tree operations.				C
CO5	Possible for executing numerous graph functions				Ap

LIST OF EXPERIMENTS:

1. Pointer using 1D, 2D array
2. Dynamic memory allocation
3. Implementation of singly linked list and its operations
4. Implementation of doubly linked list and its operations
5. Implementation of circular linked list and its operations
6. Implementation of Infix to postfix conversion using stack ADT
7. Implement the application for evaluating postfix expressions using array of stack ADT
8. Implementation of reversing a queue using stack
9. Binary Search Tree
10. AVL Tree
11. Priority Queues (Heaps)
12. Implementation of Graph Traversals(BFS, DFS)

HARDWARE / SOFTWARE REQUIRED FOR A BATCH OF 30 STUDENTS:

Hardware:

LAN System with 33 nodes (OR) Standalone PCs – 33 Nos.

Software:

Compiler – C

TOTAL (P:60) : 60 PERIODS

Mapping of COs with POs / PSOs														
Cos	**POs**												**PSOs**	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3											1	
3		3											1	
4			3											1
5	3				3									
CO (W.A)	**3**	**3**	**3**		**3**								**1**	**1**

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22MEP01 - ENGINEERING GRAPHICS LABORATORY

(Common to AI & DS, BME, CSE, CSE (IoT), CSE (CS), ECE and IT Branches)

L	T	P	C
0	0	4	2

PRE REQUISITE : NIL

Course Objective:	• To construct various plane curves drawing by Modeling software with dimensions • To construct the concept of first angle projection of points, lines and plane drawing by Modeling software with dimensions • To develop the projection of solids drawing by Modeling software with dimensions • To solve problems in sectioning of solids and developing the surfaces drawing by Modeling software with dimension. • To apply the concepts of orthographic and isometric drawing by Modeling software with dimensions

Course Outcomes The Student will be able to		**Cognitive Level**
CO1	Apply the concept of Drawing standards in AutoCAD software,	Ap
CO2	Apply the drawing tools in AutoCAD software to create 2D drawing	Ap
CO3	Apply the drawing tools in AutoCAD software to draw the projections of solids	Ap
CO4	Apply the drawing tools in AutoCAD software to draw the Section and Development of surface	Ap
CO5	Apply the drawing tools in AutoCAD software to create 3D drawing	Ap

LIST OF EXPERIMENTS

1. Study of basic tools, commands and coordinate systems (absolute, relative, polar, etc.) used in 2D software.
2. Draw the conic curves and special curves by using drafting software.
3. Draw the front view, top view, side view of objects from the given isometric view.
4. Draw the projections of straight line inclined to both the principal planes.
5. Draw the projections of polygonal surface.
6. Draw the projections of prism, pyramid inclined to anyone of the principal plane.
7. Draw the sectional view and the true shape of the given cylinder and cone.
8. Draw the development of surfaces like prism and pyramid.
9. Draw the isometric projections of cylinder and cone.
10. Draw the isometric projections of Prism and Pyramid.

TOTAL (P:60) = 60 PERIODS

REFERENCES:

1. K.Venugopal and V.Prabhu Raja,―Engineering Graphics‖, New Age International (P) Limited,2022

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3			3										
2	3			3										1
3	3			3										
4	3			3									1	
5	3			3										
CO (W.A)	**3**			**3**									**1**	**1**

22MAN04 - SOFT/ANALYTICAL SKILLS – II *(Common to All Branches)*				
	L	**T**	**P**	**C**
	1	**0**	**2**	**0**

PRE REQUISITE : NIL

Course Objective:	• To acquire satisfactory competency in verbal reasoning. • To develop skill to meet the competitive examinations for better job opportunity.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of Continuous Assessment test**
CO1	Enhance vocabulary which in turn will help in developing language competency.	U	40%
CO2	Solve the problems easily by using Short-cut method with time management.	Ap	30%
CO3	Analyze the problems logically and approach the problems in a different manner.	An	30%

UNIT I - VERBAL COMPETENCY	(5+10)
Voice - Modal Verbs - Synonyms & Antonyms - Confusable Words	
UNIT II - NUMERICAL REPRESENTATION	(5+10)
Average - Data Interpretation - Simple Interest and Compound Interest - Venn Diagram.	
UNIT III - RESOLUTION TENDENCY	(5+10)
Time and Work - Pipes and Cistern - Number Series and Odd man Out - Cube Problems	
TOTAL (L:45) = 45 PERIODS	

REFERENCES:

1. Murphy, Raymond. English Grammar in Use. Fourth Edition, Cambridge University, 2012.
2. Dr. R.S. Aggarwal. A Modern Approach to Verbal & Non-Verbal Reasoning. S Chand and Company Limited, New Delhi, 2014.
3. Aggarwal, Ashish. Quick Arithmetic. S Chand and Company Limited, New Delhi, 2014.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1									2	3				
2		2		2										
3		2		2										
CO (W.A)		**1**		**1**					**1**	**1**				

22MAN05 - YOGA – II (*Common To All Branches*)				
	L	**T**	**P**	**C**
	0	**0**	**I**	**0**
PRE REQUISITE : NIL				

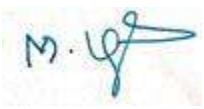
Course Objective:	• To strengthen the body through physical exercises. • To understand the importance of value system and ethics. • To know the life philosophy of yogis and maharishis. • To understand the nature laws, cause and effect theory. • To inculcate knowledge about different types of Asanas and their benefits.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Perform physical exercises like spine exercises, massage and acupressure.	Ap	Internal Assessment
CO2	Learn the human values, ethics, time management and the importance of introspection.	U	
CO3	Analyze various life philosophies of yogi's and rishi's.	An	
CO4	Understand life lessons and nature laws.	U	
CO5	Demonstrate different types of yoga Asanas and improve their personal fitness.	Ap	

UNIT I – PHYSICAL EXCERCISES (PART-II)	(3)
Breathing Exercises – Kapalapathi – Maharasanam (Spine Exerices) – Massage and Acupressure.	
UNIT II – HUMAN VALUE	(3)
Divine power – Life force (Bio magnetism) – Importance of Introspection – Time management – Punctuality – self confidence – mind control.	
UNIT III – PHILOSOPHY OF LIFE	(3)
Basic needs for life – Hunger and thirst – climatic/weather changes – Body wastes – pressure of excretory organs – safety measures – protection from natural disaster – protection from enmity – protection from accidents – ethics – morality – duty – charity – Wisdom of perfection stages – faith – understanding – realization.	
UNIT IV – NATURE'S LAW OF CAUSE AND EFFECT	(3)
Food transformation into seven minerals – Natural actions – pattern – precision – regularity – Required skills – planned work – awareness – introspection.	
UNIT V – ASANAS (PART-II)	(3)
Ustrasana – Vakrasana –Komugasana – Padmasana – Vajrasana – Sukhasana – Yogamudra – mahamudra.	
TOTAL (P:15) : 15 PERIODS	

TEXT BOOK/REFERENCE:
1. Light On Yoga by B.K.S. Iyengar.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1								3	2			3		
2								3	2			3		
3								3	2			3		
4								3	2			3		
5								3	2			3		
CO (W.A)								**3**	**2**			**3**		



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22GYA01 HERITAGE OF TAMILS *(For Common To All Branches)*				
	L	**T**	**P**	**C**
	I	**0**	**0**	**I**
PRE REQUISITE : NIL				

UNIT I - LANGUAGE AND LITERATURE	**(3)**
Language Families in India - Dravidian Languages – Tamil as aClassical Language - Classical Literature in Tamil – Secular Nature of Sangam Literature – Distributive Justice in Sangam Literature - Management Principles in Thirukural - Tamil Epics and Impact of Buddhism & Jainism in Tamil Land - Bakthi Literature Azhwars and Nayanmars - Forms of minor Poetry - Development of Modern literature in Tamil - Contribution of Bharathiyar and Bharathidhasan.	
UNIT II - HERITAGE - ROCK ART PAINTINGS TO MODERN ART – SCULPTURE	**(3)**
Hero stone to modern sculpture - Bronze icons - Tribes and their handicrafts - Art of temple car making - Massive Terracotta sculptures, Village deities, Thiruvalluvar Statue at Kanyakumari, Making of musical instruments - Mridhangam, Parai, Veenai, Yazh and Nadhaswaram - Role of Temples in Social and Economic Life of Tamils.	
UNIT III - FOLK AND MARTIAL ARTS	**(3)**
Therukoothu, Karagattam, Villu Pattu, Kaniyan Koothu, Oyillattam, Leatherpuppetry, Silambattam, Valari, Tiger dance - Sports and Games of Tamils.	
UNIT IV - THINAI CONCEPT OF TAMILS	**(3)**
Flora and Fauna of Tamils & Aham and Puram Concept from Tholkappiyam and Sangam Literature - Aram Concept of Tamils - Education and Literacy during Sangam Age - Ancient Cities and Ports of Sangam Age - Export and Import during Sangam Age - Overseas Conquest of Cholas.	
UNIT V - CONTRIBUTION OF TAMILS TO INDIAN NATIONAL MOVEMENT AND INDIAN CULTURE	**(3)**
Contribution of Tamils to Indian Freedom Struggle - The Cultural Influence of Tamils over the other parts of India – Self-Respect Movement - Role of Siddha Medicine in Indigenous Systems of Medicine – Inscriptions & Manuscripts – Print History of Tamil Books.	
TOTAL (L:15) : 15 PERIODS	

TEXT-CUM-REFERENCE BOOKS

1. தமிழக வரலாறு – மக்களும் பண்பாடும் –கே.கே.பிள்ளை (வெளியீடு: தமிழ்நாடு பாடநூல் மற்றும் கல்வியியல் பணிகள் கழகம்).
2. கணினித் தமிழ் – முனைவர் இல.சுந்தரம். (விகடன் பிரசுரம்).
3. கீழடி – வைகை நதிக்கரையில் சங்ககால நகர நாகரிகம் (தொல்லியல் துறை வெளியீடு)
4. பொருநை – ஆற்றங்கரை நாகரிகம். (தொல்லியல் துறை வெளியீடு)

5. Social Life of Tamils (Dr.K.K.Pillay) A joint publication of TNTB & ESC and RMRL – (in print)
6. Social Life of the Tamils - The Classical Period (Dr.S.Singaravelu) (Published by: International Institute of Tamil Studies.
7. Historical Heritage of the Tamils (Dr.S.V.Subatamanian, Dr.K.D. Thirunavukkarasu) (Published by: International Institute of Tamil Studies).
8. The Contributions of the Tamils to Indian Culture (Dr.M.Valarmathi) (Published by: International Institute of Tamil Studies.)
9. Keeladi - 'Sangam City Civilization on the banks of river Vaigai' (Jointly Published by: Department of Archaeology & Tamil Nadu Text Book and Educational Services Corporation, Tamil Nadu)
10. Studies in the History of India with Special Reference to Tamil Nadu (Dr.K.K.Pillay) (Published by: The Author)
11. Porunai Civilization (Jointly Published by: Department of Archaeology & Tamil Nadu Text Book and Educational Services Corporation, Tamil Nadu)
12. Journey of Civilization Indus to Vaigai (R.Balakrishnan) (Published by: RMRL) – Reference Book.

22GYA01 தமிழர் மரபு (அனைத்து பாடப்பிரிவினருக்கும்)				
	L	T	P	C
	1	0	0	1
முன் தேவை: இல்லை				

அலகு 1 மொழி மற்றும் இலக்கியம்	(3)
இந்திய மொழிக் குடும்பங்கள் – திராவிட மொழிகள் – தமிழ் ஒரு செம்மொழி – தமிழ் செவ்விலக்கியங்கள் – சங்க இலக்கியத்தின் சமயச் சார்பற்ற தன்மை– சங்க இலக்கியத்தில் பகிர்தல் அறம் – திருக்குறளில் மேலாண்மைக் கருத்துக்கள் – தமிழ்க காப்பியங்கள், தமிழகத்தில் சமண பௌத்த சமயங்களின் தாக்கம் – பக்தி இலக்கியம், ஆழ்வார்கள் மற்றும் நாயன்மார்கள் – சிற்றிலக்கியங்கள் – தமிழில் நவீன இலக்கியத்தின் வளர்ச்சி – தமிழ் இலக்கிய வளர்ச்சியில் பாரதியார் மற்றும் பாரதிதாசன் ஆகியோரின் பங்களிப்பு.	
அலகு 2 மரபு – பாறை ஓவியங்கள் முதல் நவீன ஓவியங்கள் வரை – சிற்பக்கலை:	**(3)**
நடுகல் முதல் நவீன சிற்பங்கள் வரை – ஐம்பொன் சிலைகள் – பழங்குடியினர் மற்றும் அவர்கள் தயாரிக்கும் கைவினைப் பொருட்கள், பொம்மைகள்– தேர் செய்யும் கலை– சுடுமண் சிற்பங்கள் – நாட்டுப்புறத் தெய்வங்கள் – குமரிமுனையில் திருவள்ளுவர் சிலை – இசைக் கருவிகள் – மிருதங்கம், பறை, வீணை, யாழ், நாதஸ்வரம் – தமிழர்களின் சமூக பொருளாதார வாழ்வில் கோவில்களின் பங்கு.	
அலகு 3 நாட்டுப்பறக் கலைகள் மற்றும் வீர விளையாட்டுகள்:	**(3)**
தெருக்கூத்து, கரகாட்டம், வில்லுப்பாட்டு, கணியான் கூத்து, ஒயிலாட்டாம், தோல்பாவைக்கூத்து, சிலம்பாட்டம், வளரி, புலியாட்டம், தமிழர்களின் விளையாட்டுகள்.	
அலகு 4 தமிழர்களின் திணைக் கோட்பாடுகள்:	**(3)**
தமிழகத்தின் தாவரங்களும், விலங்குகளும் – தொல்காப்பியம் மற்றும் சங்க இலக்கியத்தில் அகம் மற்றும் புறக் கோட்பாடுகள் – தமிழர்கள் போற்றிய அறக்கோட்பாடு – சங்ககாலத்தில் தமிழகத்தில் எழுத்தறிவும், கல்வியும் – சங்ககால நகரங்களும் துறை முகங்களும் – சங்ககாலத்தில் ஏற்றுமதி மற்றும் இறக்குமதி – கடல்கடந்த நாடுகளின் சோழர்களின் வெற்றி.	
அலகு 5 இந்திய தேசிய இயக்கம் மற்றும் இந்திய பண்பாட்டிற்குத் தமிழர்களின் பங்களிப்பு:	**(3)**
இந்திய விடுதலைப்போரில் தமிழர்களின் பங்கு – இந்தியாவின் பிறப்பகுதிகளில் தமிழ்ப் பண்பாட்டின் தாக்கம் – சுயமரியாதை இயக்கம் –இந்திய மருத்துவத்தில் சித்த மருத்துவத்தின் பங்கு, கல்வெட்டுகள், கையெழுத்துப்படிகள் – தமிழ் புத்தக்களின் அச்சு வரலாறு.	
TOTAL (L:15) : 15 PERIODS	

TEXT-CUM-REFERENCE BOOKS

1. தமிழக வரலாறு – மக்களும் பண்பாடும் –கே.கே.பிள்ளை (வெளியீடு: தமிழ்நாடு பாடநூல் மற்றும் கல்வியியல் பணிகள் கழகம்).
2. கணினித் தமிழ் – முனைவர் இல.சுந்தரம். (விகடன் பிரசுரம்).
3. கீழடி – வைகை நதிக்கரையில் சங்ககால நகர நாகரிகம் (தொல்லியல் துறை வெளியீடு)
4. பொருநை – ஆற்றங்கரை நாகரிகம். (தொல்லியல் துறை வெளியீடு)

5. Social Life of Tamils (Dr.K.K.Pillay) A joint publication of TNTB & ESC and RMRL – (in print)
6. Social Life of the Tamils - The Classical Period (Dr.S.Singaravelu) (Published by: International Institute of Tamil Studies.
7. Historical Heritage of the Tamils (Dr.S.V.Subatamanian, Dr.K.D. Thirunavukkarasu) (Published by: International Institute of Tamil Studies).
8. The Contributions of the Tamils to Indian Culture (Dr.M.Valarmathi) (Published by: International Institute of Tamil Studies.)
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10. Studies in the History of India with Special Reference to Tamil Nadu (Dr.K.K.Pillay) (Published by: The Author)
11. Porunai Civilization (Jointly Published by: Department of Archaeology & Tamil Nadu Text Book and Educational Services Corporation, Tamil Nadu)
12. Journey of Civilization Indus to Vaigai (R.Balakrishnan) (Published by: RMRL) – Reference Book.

22GYA02 TAMILS AND TECHNOLOGY *(For Common To All Branches)*				
	L	**T**	**P**	**C**
	1	**0**	**0**	**1**
PRE REQUISITE : NIL				

UNIT I - WEAVING AND CERAMIC TECHNOLOGY	**(3)**
Weaving Industry during Sangam Age – Ceramic technology – Black and Red Ware Potteries (BRW) – Graffiti on Potteries.	
UNIT II - DESIGN AND CONSTRUCTION TECHNOLOGY	**(3)**
Designing and Structural construction House & Designs n household materials during Sangam Age - Building materials and Hero stones of Sangam age – Details of Stage Constructions in Silappathikaram - Sculptures and Temples of Mamallapuram - Great Temples of Cholas and other worship places - Temples of Nayaka Period - Type study (Madurai Meenakshi Temple)- Thirumalai Nayakar Mahal - Chetti Nadu Houses, Indo - Saracenic architecture at Madras during British Period.	
UNIT III - MANUFACTURING TECHNOLOGY	**(3)**
Art of Ship Building - Metallurgical studies - Iron industry - Iron smelting,steel -Copper and gold- Coins as source of history - Minting of Coins – Beads making-industries Stone beads -Glass beads - Terracotta beads -Shell beads/ bone beats - Archeological evidences - Gem stone types described in Silappathikaram.	
UNIT IV - AGRICULTURE AND IRRIGATION TECHNOLOGY	**(3)**
Dam, Tank, ponds, Sluice, Significance of Kumizhi Thoompu of Chola Period, Animal Husbandry - Wells designed for cattle use - Agriculture and Agro Processing - Knowledge of Sea - Fisheries – Pearl - Conche diving - Ancient Knowledge of Ocean - Knowledge Specific Society.	
UNIT V - SCIENTIFIC TAMIL & TAMIL COMPUTING	**(3)**
Development of Scientific Tamil - Tamil computing – Digitalization of Tamil Books – Development of Tamil Software – Tamil Virtual Academy – Tamil Digital Library – Online Tamil Dictionaries – Sorkuvai Project.	
TOTAL (L:15) : 15 PERIODS	

TEXT-CUM-REFERENCE BOOKS

1. தமிழக வரலாறு – மக்களும் பண்பாடும் –கே.கே.பிள்ளை (வெளியீடு: தமிழ்நாடு பாடநூல் மற்றும் கல்வியியல் பணிகள் கழகம்).
2. கணினித் தமிழ் – முனைவர் இல.சுந்தரம். (விகடன் பிரசுரம்).
3. கீழடி – வைகை நதிக்கரையில் சங்ககால நகர நாகரிகம் (தொல்லியல் துறை வெளியீடு)
4. பொருநை – ஆற்றங்கரை நாகரிகம். (தொல்லியல் துறை வெளியீடு)

5. Social Life of Tamils (Dr.K.K.Pillay) A joint publication of TNTB & ESC and RMRL – (in print)
6. Social Life of the Tamils - The Classical Period (Dr.S.Singaravelu) (Published by: International Institute of Tamil Studies.
7. Historical Heritage of the Tamils (Dr.S.V.Subatamanian, Dr.K.D. Thirunavukkarasu) (Published by: International Institute of Tamil Studies).
8. The Contributions of the Tamils to Indian Culture (Dr.M.Valarmathi) (Published by: International Institute of Tamil Studies.)
9. Keeladi - 'Sangam City Civilization on the banks of river Vaigai' (Jointly Published by: Department of Archaeology & Tamil Nadu Text Book and Educational Services Corporation, Tamil Nadu)
10. Studies in the History of India with Special Reference to Tamil Nadu (Dr.K.K.Pillay) (Published by: The Author)
11. Porunai Civilization (Jointly Published by: Department of Archaeology & Tamil Nadu Text Book and Educational Services Corporation, Tamil Nadu)
12. Journey of Civilization Indus to Vaigai (R.Balakrishnan) (Published by: RMRL) – Reference Book.

22GYA02 தமிழரும் தொழில்நுட்பமும் (அனைத்து பாடப்பிரிவினருக்கும்)				
	L	T	P	C
	I	0	0	I
முன் தேவை: இல்லை				

அலகு 1 நெசவு மற்றும் பானைத் தொழில்நுட்பம்:	(3)
சங்ககாலத்தில் நெசவுத்தொழில் – பானைத் தொழிலநுட்பம் – கருப்பு சிவப்பு பாண்டங்கள் – பாண்டங்களில் கீறல் குறியீடுகள்.	
அலகு 2 வடிவமைப்பு மற்றும் கட்டிடத் தொழில்நுட்பம்:	**(3)**
சங்ககாலத்தில் வடிவமைப்பு மற்றும் கட்டுமானங்கள் மற்றும் சங்ககாலத்தில் வீட்டுப் பொருட்களில் வடிவமைப்பு – சங்ககாலத்தில் கட்டுமான பொருட்களும் நடுக்கல்லும் – சிலப்பதிகாரத்தில் மேடை அமைப்பு பற்றிய விவரங்கள் – மாமல்லபுரச் சிற்பங்களும், கோவில்களும் – சோழர் காலத்துப் பெருங்கோயில்கள் மற்றும் பிற வழிபாட்டுத் தலங்கள் – நாயக்கர் காலக் கோயில்கள் – மாதிரி கட்டமைப்புகள் பற்றி அறிதல், மதுரை மீனாட்சி அம்மன் ஆலயம் மற்றும் திருமலை நாயக்கர் மஹால் – செட்டிநாட்டு வீடுகள் – பிரிட்டிஷ் காலத்தில் சென்னையில் இந்தோ–சாரோசெனிக் கட்டிடக் கலை.	
அலகு 3 உற்பத்தி தொழில் நுட்பம்:	**(3)**
கப்பல் கட்டும் கலை – உலோகவியல் – இரும்புத் தொழிற்சாலை – இரும்பை உருக்குதல், எக்கு – வரலாற்றுச் சான்றுகளாக செம்பு மற்றும் தங்க நாணயங்கள் – நாணயங்கள் அச்சடித்தல் – மணி உருவாக்கும் தொழிற்சாலைகள் – கல்மணிகள், கண்ணாடி மணிகள் – சுடுமண் மணிகள் – சங்கு மணிகள் – எலும்புத் துண்டுகள் – தொல்லியல் சான்றுகள் – சிலப்பதிகாரத்தில் மணிகளின் வகைகள்.	
அலகு 4 வேளாண்மை மற்றும் நீர்பாசனத் தொழில் நுட்பம்:	**(3)**
அணை, ஏரி, குளங்கள், மதகு – சோழர்காலக் குமுழித் தூம்பின் முக்கியத்துவம் – கால்நடை பராமரிப்பு – கால்நடைகளுக்காக வடிவமைக்கப்பட்ட கிணறுகள் – வேளாண்மை மற்றும் வேளாண்மைச் சார்ந்த செயல்பாடுகள் – கடல்சார் அறிவு – மீன்வளம் – முத்து மற்றும் முத்துக்குளித்தல் – பெருங்கடல் குறித்த பண்டைய அறிவு – அறிவுசார் சமூகம்.	
அலகு 5 அறிவியல் தமிழ் மற்றும் கணித்தமிழ்:	**(3)**
அறிவியல் தமிழின் வளர்ச்சி – கணித்தமிழ் வளர்ச்சி – தமிழ் நூல்களை மின் பதிப்பு செய்தல் – தமிழ் மென்பொருட்கள் உருவாக்கம் – தமிழ் இணையக் கல்விக்கழகம் – தமிழ் மின் நூலகம் – இணையத்தில் தமிழ் அகராதிகள் – சொற்குவைத் திட்டம்.	
	TOTAL (L:15) : 15 PERIODS

TEXT-CUM-REFERENCE BOOKS

1. தமிழக வரலாறு – மக்களும் பண்பாடும் –கே.கே.பிள்ளை (வெளியீடு: தமிழ்நாடு பாடநூல் மற்றும் கல்வியியல் பணிகள் கழகம்).
2. கணினித் தமிழ் – முனைவர் இல.சுந்தரம். (விகடன் பிரசுரம்).
3. கீழடி – வைகை நதிக்கரையில் சங்ககால நகர நாகரிகம் (தொல்லியல் துறை வெளியீடு)
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12. Journey of Civilization Indus to Vaigai (R.Balakrishnan) (Published by: RMRL) – Reference Book.

22MYB06 – PROBABILITY AND RANDOM PROCESSES *(Common to BME and ECE Branches)*				
	L	**T**	**P**	**C**
	3	**I**	**0**	**4**
PREREQUISITE : NIL				
Course Objective:	• Develop probability distribution of discrete and continuous random variables, Joint probability distribution occurs in digital signal processing, design engineering and microwave engineering • To learn about the classification of random processes and strict stationary, wide sense stationary and Ergodic, correlation functions and power spectral density and solve the signal problems.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the basic principles of probability to solve the problems involving multiple events and practical problems in communication engineering, including signal processing and information theory.	Ap	30%
CO2	Interpret the distribution to model and solve problems involving binary outcomes, such as error detection and correction in digital communications.	Ap	30%
CO3	Determine and enhance problem-solving skills through practical examples, case studies, and applications in fields such as signal processing, time series analysis, and system modeling.	An	20%
CO4	Analyze and interpret signals and their interactions in the frequency domain.	An	20%
CO5	Demonstrate the methods to solve the spectrum estimation and spectral density function by using mathematical tools in analog communication.	Ap	Internal Assessment mode

UNIT I – ONE DIMENSIONAL RANDOM VARIABLES	**(9+3)**
Probability: Random variable – Probability mass function – Probability density functions – Properties – Moments –Moment generating functions and their properties	
UNIT II-STANDARD DISTRIBUTIONS	**(9+3)**
Discrete distributions: Binomial, Poisson and Geometric distribution – Continuous distributions: Uniform, Exponential and Normal distribution and its properties.	
UNIT III –TWO DIMENSIONAL RANDOM VARIABLES	**(9+3)**
Joint distributions – Marginal distributions and conditional distribution – Covariance – correlation and Regression – Transformation of random variables – Central limit theorem (Excluding proof).	
UNIT IV–RANDOM PROCESSES	**(9+3)**
Definition and examples – first order, second order strictly stationary, wide-sense stationary and Ergodic process- Markov process – Binomial, Poisson processes.	

UNIT V – CORRELATION AND SPECTRAL DENSITIES	(9+3)
Auto correlation – Cross correlation – Properties –Power spectral density – Cross spectral density – Properties – Wiener – Khintchine relation (statement only) – Relationship between cross power spectrum and cross correlation function.	
TOTAL (L:45+T:15) :60 PERIODS	

TEXT BOOKS:

1. Veerarajan.T, "Probability, Statistics and Random Processes,"3rded.,NewDelhi,Tata McGraw-Hill,2008
2. Venkatarama Krishnan, "Probability and Random Process,"2ndEdition,John Wiley & Sons , New Jersey,2016
3. Scott L. Miller and Donald Childers, "Probability and Random Processes with applications to Signal Processing and communications," Elsevier, 2012.

REFERENCES:

1. GubnerA.John, "Probability and Random Processes for Electrical and Computer Engineers", Cambridge University press, Newyork, 2006.
2. Charles W.Therrien, MuraliTummala, "Probability and random process for electrical and computer Engineers",CRC Press, Newyork, 2012.
3. Singaravelu.A, Sivasubramanian, Ramaa, "Probability, Statistics and Random Processes," 2nd ed., Meenakshi Publication, Chennai, 2003.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3	2												
2		2												
3	3													
4		2												
5	3				2				3			2		
CO (W.A)	**3**	**2**			**2**				**3**			**2**		

22ECC05 - DIGITAL LOGIC DESIGN				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To understand and design combinational and sequential logic circuits. • To Model sequential circuits using Verilog programming. • To implement a project using digital electronics components.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the concepts and simplification methods of Digital Electronic circuits for the specified application.	Ap	20%
CO2	Analyze combinational circuits, Sequential circuits, memory structures and programmable logic families.	An	20%
CO3	Design combinational, sequential circuits and modeling of sequential circuit using Verilog programming.	Ap	40%
CO4	Implement and demonstrate the project using suitable digital electronic components.	An	20%
CO5	Give an oral presentation on the design project and prepare documents individually/ teams.	U	Internal Assessment

UNIT I - DIGITAL FUNDAMENTALS	**(9)**
Number Systems – Decimal, Binary, Octal, Hexadecimal, Complements -1's and 2's complements, Codes– Binary, BCD, Excess-3, Gray code, Boolean Algebra-Boolean rule, Laws, theorems, Boolean Functions- Sum of products (SOP) and product of sums (POS, Karnaugh map (K-Map) Minimization (upto 4 variables)- NAND and NOR implementation.	
UNIT II - COMBINATIONAL LOGIC DESIGN	**(9)**
Design of Half and Full Adders, Half and Full Subtractor, BCD Adder, Multiplexer, Demultiplexer, Magnitude Comparator, Decoder, Encoder, Priority Encoder, Parity generator and checker, Hardware Description Language (HDL) - Modeling of Combinational circuits using Verilog.	
UNIT III - SYNCHRONOUS SEQUENTIAL LOGIC DESIGN	**(9)**
Flip flops – SR, JK, T, D, Master/Slave FF – Operation and Excitation tables, Design of Counters- Ripple Counters, Ring Counters, Johnson's Counter, Modulo-N counters, Shift registers- SISO,SIPO,PIPO,PISO. Modeling of Sequential Circuits using Verilog.	
UNIT IV - ASYNCHRONOUS SEQUENTIAL LOGIC DESIGN	**(9)**
Analysis and Design Procedure - State table and State diagrams, State Reduction Techniques. Cycles and races, race free assignments, Hazards, Essential Hazards, Design of Hazard free circuits.	
UNIT V - MEMORY AND PROGRAMMABLE LOGIC FAMILIES	**(9)**
Basic memory structure – ROM -PROM – EPROM – EEPROM , RAM – Static and dynamic RAM - Programmable Logic Devices – Programmable Logic Array (PLA) - Programmable Array Logic (PAL) – Field Programmable Gate Arrays (FPGA) – Implementation of combinational logic circuits using PLA, PAL	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. M. Morris Mano & Michael D.Ciletti, "Digital Design with an Introduction to the Verilog HDL, 5th Edition, Prentice Hall of India Pvt.Ltd. 2015.
2. Dr. Sanjay Sharma, "Digital Electronics and Logic Design" 4th Edition., S.K.Kataria & Sons, 2017

REFERENCES:

1. Stephan D.Brown & Zvonko G.Vranesic, "Fundamentals of Digital Logic with VHDL Design, 2'nd Edition, Tata Mc Graw – Hill, 2003.
2. Samir Palnitkar, "Verilog HDL: A Guide to Digital Design and Synthesis,"2'nd Edition, Prentice Hall, 2009.
3. Thomas L. Floyd & R P Jain, "Digital Fundamentals," 10th Edition, PHI, 2011.
4. Ronald J Tocci & Neal S. Widmer, "Digital Systems, Principles and Applications," 10th Edition, Pearson education, 2011.
5. Frank Vahid, "Digital Design with RTL Design, Verilog and VHDL," 10'th Edition, John Wiley and Sons, 2010

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												3	
2		3											3	
3			3		3								3	3
4						1			3		3	3	3	3
5									3	3		2		3
CO (W.A)	3	3	3	3	3	1			3	3	3	3	3	3

22ECC06 – SIGNALS AND SYSTEMS				
	L	T	P	C
	3	0	0	3
PREREQUISITE : 22MYB01,22MYB04				

Course Objective:	• To make the basic properties of signal & systems and its various methods of classification. • To learn Laplace Transform & Fourier transform and their properties. • To know the frequency representation of CT signals with Fourier series and transform. • To motivate the students to implement the discrete time system using impulse response and inputs. • To characterize LTI systems in the discrete time domain and various Transform domains.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Obtain the specified parameter/representation for the given continuous time signal/system using time domain, frequency domain and transform domain representation	Ap	20%
CO2	Apply the Fourier Series and Transform to CT signals to convert them from the time domain to the frequency domain.	Ap	20%
CO3	Analyze and classify the given signal/system using time domain, frequency domain and transform domain representation	An	30%
CO4	Analyze the response of discrete-time LTI systems for various input signals	An	20%
CO5	Make an oral presentation of the application concepts of the course for transmission of audio /image/ video/ data signal for benefit of society	U	Internal Assessment

UNIT I - CLASSIFICATION OF SIGNALS AND SYSTEMS	**(9)**
Standard Signals: Unit impulse, unit step, unit ramp, exponential, and sinusoidal signals, Classification of Continuous and discrete time signals, Types of signals: power, energy, periodic, even and odd, Basic Operations on Signals, Basic System Properties: Linearity, Time Invariant, causality, stability and invertibility, LTI.	
UNIT II - TIME DOMAIN CHARACTERISATION OF CONTINUOUS TIME LTI SYSTEM	**(9)**
Classification of systems - CT systems and DT systems - Linear & Nonlinear, Time-variant & Time-invariant, Causal & Non-causal, Stable & Unstable - Convolution Integral, Properties of continuous time LTI system-Causality, stability, Causal continuous time LTI system described by differential equations	
UNIT III- FREQUENCY DOMAIN REPRESENTATION IN CT SIGNALS	**(9)**

Fourier series representation of continuous time periodic signals, properties of continuous time Fourier series, Fourier transform of continuous time aperiodic signals and periodic signals, properties of continuous time Fourier transform, Laplace transform, Region of Convergence, Inverse Laplace transform.	
UNIT IV – TIME DOMAIN CHARACTERISATION OF DISCRETE TIME LTI SYSTEM	**(9)**
Sampling theorem (Low Pass) – Reconstruction of a Signal from its samples, aliasing, Convolution sum, properties of discrete time LTI system, Causal discrete time LTI system described by difference equations.	
UNIT V- FREQUENCY DOMAIN REPRESENTATION IN DT SIGNALS	**(9)**
Z Transform, Inverse Z transform – Long division – partial fraction, ROC, Properties of Z Transform: Linearity, time shifting, change of scale, Z-domain differentiation, differencing, accumulation, convolution in discrete time, initial and final value theorems.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:
1. Simon S. Haykin and Barry Van Veen, "Signals and Systems,"2 Nd Edition. Wiley India, 2008(Reprint).
REFERENCES:
1. B. P. Lathi, "Principles of Linear Systems and Signals", Second Edition, Oxford, 2009.
2. R.E.Zeimer, W.H.Tranter and R.D.Fannin, "Signals & Systems - Continuous and Discrete", Pearson, 2007.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													2
2		3												
3		2												
4		2												
5						1				1		1	2	
CO (W.A)	3	2				1				3		1	2	2

22ECC07 - ANALOG ELECTRONICS				
	L	T	P	C
	3	0	0	3

PREREQUISITE : 22ECC04

Course Objective:	• To understand the different biasing techniques of amplifier. • To study about small signal analysis of amplifiers, frequency response of amplifiers and different types of power amplifiers. • To get awareness about the analysis of feedback amplifiers and tuned amplifiers and knowledge about oscillators design and multivibrators.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the knowledge of network theorems and device models to solve given analog electronic circuits.	Ap	20%
CO2	Analyze a given analog electronic circuit to compute required parameters.	An	30%
CO3	Analyze the various parameters and choose the analog electronic circuits in terms of performance and application of the circuits.	An	30%
CO4	Design analog electronic circuits for a given specification.	E	20%
CO5	Give a presentation on recent technological development in the Analog Electronics domain.	U	Internal Assessment

UNIT I - TRANSISTOR BIAS STABILITY	**(9)**
BJT – Need for biasing – Stability factor - Load line and quiescent point. - Different types of biasing circuits – Method of stabilizing the Q point - Bias compensation – Diode, Thermister and Sensistor compensations – Biasing of FET.	
UNIT II - SMALL SIGNAL AMPLIFIERS	**(9)**
Introduction –Analysis of transistor amplifier circuit using h parameters- Simplified CB, CE & CC - Darlington connection for high input impedance, BJT Differential Amplifiers.	
UNIT III - FREQUENCY RESPONSE OF AMPLIFIERS AND POWER AMPLIFIERS	**(9)**
Frequency response of amplifiers: cutoff frequencies and bandwidth –Multistage amplifiers: coupling methods-CE-CC amplifier- frequency response of multi stage amplifiers. Classification of amplifiers; Class A, Transformer coupled Class A audio amplifier - Class B amplifier - Push-Pull Class B amplifier – Distortion in Power Amplifiers.	
UNIT IV - FEEDBACK AMPLIFIERS AND TUNED AMPLIFIERS	**(9)**
Feedback amplifiers: Effect of negative feedback on amplifiers, Nyquist criterion. Tuned Amplifier: single and double tuned amplifiers- Stagger tuned amplifiers. Stability of tuned amplifiers - Neutralization - Hazeltine neutralization method.	
UNIT V- OSCILLATORS AND MULTIVIBRATORS	**(9)**
Barkhausen Criterion - Analysis of LC oscillators: Hartley – Colpitts oscillator, RC oscillators: RC Phase shift oscillator - Wien bridge oscillator. Multivibrators - Astable multivibrator - Monostable multivibrator - Bistable multivibrator - Schmitt trigger	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:

1. Robert L. Boylestad and Louis Nashelsky, Electronic Devices and Circuit Theory, 11th Edition, Pearson Education / PHI, 2011.

REFERENCES:

1. Millman J and Halkias .C, Integrated Electronics, 4th Edition, Tata McGraw Hill, 2015.
2. S. Salivahanan and N. Suresh Kumar, Electronic Devices and Circuits, McGraw Hill Private limited, Fifth Edition 2022.
3. David A. Bell, Electronic Devices & Circuits, Oxford Higher Education Press, 5th Edition, 2010.
4. Muhammad H. Rashid, Microelectronic Circuits: Analysis and Design, 2nd Edition, Cengage Learning, 2011.
5. Donald .A. Neamen, Electronic Circuit Analysis and Design –3rd edition, TMH, 2009.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3		3											3	
4			3										2	
5							2	1	2				2	
CO (W.A)	3	3	3				2	1	2				2.2	

22ECC08 – ELECTROMAGNETIC WAVES				
	L	T	P	C
	3	0	0	3
PREREQUISITE : 22PYB03				

Course Objective:	• To make students to learn and understand the basics of Vector Calculus and laws. • To enable the student to evaluate the electric field and magnetic field due to charge distribution and boundary conditions. • To make the students to analyze about time varying electric and magnetic fields. • To make the students to know about the electromagnetic wave equation and wave polarization

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the concept of vector calculus to determine the characteristics of electromagnetic fields.	Ap	30%
CO2	Determine the parameters by applying properties, laws and theorems of electric and magnetic fields.	Ap	30%
CO3	Analyze static and time varying electromagnetic fields.	An	20%
CO4	Examine the wave equations in free space and polarization of electromagnetic waves.	An	20%
CO5	Engage in independent study and make an oral presentation on the applications and hazards of Electromagnetic radiation	U	Internal Assessment

UNIT I - VECTOR ANALYSIS, DIVERGENCE, CURL	**(9)**
Scalar and Vector analysis - Vector algebra - Coordinate systems: Cartesian coordinate system, cylindrical coordinate system and spherical coordinate system - Divergence, gradient and curl – Divergence and Stokes theorems- Coulomb's Law - Gauss Law & its applications	
UNIT II - STATIC ELECTRIC FIELDS	**(9)**
Electric field intensity – Continuous Charge Distribution, Electric Field due to charges distributed uniformly on an infinite, finite line and circular disc. Relationship between potential and electric field - Electric flux density. Current and Current Density – Boundary conditions for electric fields between free space and conductors, and between dielectrics	
UNIT III - STATIC MAGNETIC FIELD AND MAGNETIC MATERIALS	**(9)**
Biot-Savart Law in vector form – Magnetic Field intensity due to a finite and infinite wire carrying a current I – Magnetic field intensity on the axis of a circular and rectangular loop carrying a current I – Ampere's circuital law and simple applications-Lorentz Force Equation, Magnetic flux density, Magnetic boundary conditions. Inductance – Inductance of loops and solenoids –Mutual inductance – simple examples	
UNIT IV - TIME VARYING ELECTRIC AND MAGNETIC FIELDS	**(9)**

Faraday's law –Conduction and Displacement current density –Maxwell's four equations in integral form and differential form- Maxwell's equation in Phasor form -Poynting Vector and the flow of power – Power flow in a co-axial cable	
UNIT V- ELECTROMAGNETIC WAVES	**(9)**
Wave equations for conducting medium and in free space - Wave equations in Phasor form –Reflection of plane waves by a perfect dielectric at normal incidence - wave polarizations-Introduction to EM Shielding Case Study: Biological Effects of Electromagnetic Waves.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:
1. William H. Hayt, Jr and John A. Buck, "Engineering Electromagnetics", 9th Edition, Tata McGraw Hill Publishing Company, Noida, 2020
REFERENCES:
1. Matthew N.O. Sadiku, S.V.Kulkarani, "Principles of Electromagnetics", 6th Edition, Oxford University Press, 2015. 2. Edward .C.Jordan and Keith.G.Balmain "Electromagnetic Waves and Radiating Systems", 2nd Edition, Pearson Education, 2015.

Mapping of COs with POs / PSOs

COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3													
2	3													
3	2	3											3	
4		3											3	
5						3		2	3	3		2		
CO (W.A)	**2.6**	**3**				**3**		**2**	**3**	**3**		**2**	**3**	

22CSC05 - ALGORITHMS *(Common to 22AIC06, 22CCC04,22CIC04 and 22ITC04)*					
		L	**T**	**P**	**C**
		3	**0**	**0**	**3**
PREREQUISITE : 22CSC02					
Course Objective:	• To develop problem-solving skills through algorithms and prepare students to apply the skills in various domains such as software development, research, and engineering.				

Course Outcomes The students will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Analyze the time and space complexities of algorithms using asymptotic notations	An	20%
CO2	Apply algorithmic concepts and techniques to design and develop efficient solutions for real-world problems	Ap	40%
CO3	Apply the knowledge of complexity classes P, NP and NP-Completeness problem	An	20%
CO4	Design efficient algorithms to solve graph problems	Ap	20%
CO5	Optimized the existing algorithms by reducing the lines of code	An	Internal mode

UNIT I - INTRODUCTION	**(9)**
Notion of an Algorithm – Fundamentals of Algorithmic Problem Solving – Important Problem Types – Fundamentals of the Analysis of Algorithmic Efficiency –Asymptotic Notations and their properties. Analysis Framework – Empirical analysis - Mathematical analysis for Recursive and Non-recursive algorithms – Visualization.	
UNIT II - BRUTE FORCE AND DIVIDE-AND-CONQUER	**(9)**
Brute Force – Computing an – String Matching - Selection Sort and Bubble Sort – Sequential Search - Closest-Pair and Convex-Hull Problems - Exhaustive Search: Travelling Salesman Problem - Knapsack Problem - Assignment problem. Divide and Conquer Methodology – Binary Search – Merge sort – Quick sort –Closest-Pair and Convex - Hull Problems.	
UNIT III - DYNAMIC PROGRAMMING AND GREEDY TECHNIQUE	**(9)**
Dynamic Programming : Computing a Binomial coefficient – Warshall's and Floyd's Algorithm – Optimal Binary Search trees - 0/1 Knapsack Problem. Greedy Technique: Prim's algorithm and Kruskal's Algorithm - Huffman Trees.	
UNIT IV - ITERATIVE IMPROVEMENT AND LIMITATIONS OF ALGORITHM POWER	**(9)**
Iterative Improvement - The Simplex Method - The Maximum-Flow Problem- Maximum Matching in Bipartite Graphs. Limitations of Algorithm Power: Lower bound arguments – Decision trees – P, NP and NP complete Problems.	

UNIT V - STATE SPACE SEARCH ALGORITHMS	(9)
Backtracking: N Queen's problem – Hamiltonian Circuit problem – Subset problem - Graph colouring problem. Branch and Bound: Solving 15-Puzzle problem - Assignment problem – Knapsack Problem – Travelling Salesman Problem.	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOK:

1. Anany Levitin, "Introduction to the Design and Analysis of Algorithm", Pearson Education Asia, 3rd ed., 2017.

REFERENCES:

1. Ellis Horowitz, Sartaj Sahni, Sanguthevar Rajasekaran "Computer Algorithms/C++" Orient Blackswan, 2nd Edition, 2019.
2. S. Sridhar, "Design and Analysis of Algorithms ", Oxford university press, 2014.
3. Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest and Clifford Stein, "Introduction to Algorithms", 3rd Edition, Prentice Hall of India, 2009.

Mapping of COs with POs / PSOs

Cos	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1		3											2	1
2	3													
3		3												
4	3												1	1
5			3	3					3					
CO (W.A)	**3**	**3**	**3**	**3**					**3**				**1.5**	**1.5**

22ECP02 - DIGITAL LOGIC DESIGN LABORATORY				
	L	T	P	C
	0	0	4	2

PREREQUISITE : NIL

Course Objective:	• To make students to learn and practice the basics of logic gates • To enable the students to design the combinational logic circuits. • To make the students to learn and practice with design of sequential logic circuits. • To enable the students to learn about Verilog code for combinational and sequential circuits • To motivate the students to implement the project using basic digital logics.

The Student will be able to **Course Outcomes**		**Cognitive Level**
CO1	Apply the fundamental logic functions to realize basic building blocks of digital logic design.	Ap
CO2	Apply digital concepts in the design of combinational and sequential circuits to obtain the expected output.	Ap
CO3	Analyze the design of combinational and sequential circuits using Verilog programming.	An
CO4	Design the asynchronous sequential circuits using flip flops and counters	E
CO5	To work as an individual/team and conduct experiments in digital logic design for real time applications.	E

LIST OF EXPERIMENTS :

Hardware Experiments

1.Verification of Boolean expressions
2. Construct a Half Adder, Full Adder
3. Construct a Code Converter circuit.(Binary to gray and BCD to Excess-3)
4. Implementation of Magnitude Comparator circuit using logic gates.
5. Design Encoder and decoder circuits using logic gates
6. Construct a Multiplexer and De-Multiplexer circuit using logic gates
7. Verification of SR, JK, D and T Flip Flops
8. Design of Synchronous Counter using flip-flops
9. Design of Shift Registers using flip-flops

Software Experiments (Using Model Sim)

1. Modeling and Simulation of Half adder, Full adder using Verilog.
2. Modeling and Simulation of Multiplexer, Demultiplexer using Verilog.
3. Modeling and Simulation of Synchronous Counters using Verilog.
4. Modeling and Simulation of D-FF using Verilog.

TOTAL (P: 60) = 60 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3													
2	3													2
3		3												2
4			3										2	2
5									2			2	2	
CO (W.A)	**3**	**3**	**3**						**2**			**2**	**2**	**2**

22ECP03 - ANALOG ELECTRONICS LABORATORY				
	L	T	P	C
	0	0	4	2

PREREQUISITE : 22ECC04

Course Objective:	• To design and construct different amplifiers biasing circuits. • To gain design knowledge of negative feedback amplifier and various types of oscillators. • To construct and analyze the working of different power amplifiers and multivibrators.

	Course Outcomes The Student will be able to	**Cognitive Level**
CO1	Apply the knowledge of network theorems, device models and basics of analog electronics to verify the designed values.	Ap
CO2	Identify and analyze analog electronic circuits to obtain the expected output for the given parameters.	An
CO3	Design analog electronic circuits for the given specifications and substantiate the output through experiments.	E
CO4	Simulate analog electronic circuits for the given specifications using PSPICE.	Ap
CO5	Involve in team learning, communicate effectively and maintain record for the experiments.	Ap

LIST OF EXPERIMENTS :

1. Design and Construct BJT CE amplifier using Biasing Techniques (Fixed bias and Voltage follower Bias).
2. Construct Darlington Amplifier using BJT and measure its bandwidth.
3. Design and implementation of Class B Power Amplifier.
4. Design and implementation of Negative feedback amplifier (Current Series and Voltage Series).
5. Design and implementation of Single tuned amplifier.
6. Design and implementation of RC phase shift oscillator.
7. Design and implementation of Hartely oscillator.
8. Design and implementation of Astable and Monostable multivibrators.
9. Simulation of Class A amplifiers using PSPICE.
10. Simulation of Astable Multivibrator using PSPICE.
11. Simulation of Schmitt Trigger using PSPICE.

TOTAL (P:60) = 60 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3												2	3
2		3											2	
3			3										3	
4					3									
5									2	2		2		
CO (W.A)	**3**	**3**	**3**		**3**				**2**	**2**		**2**	**2.3**	**3**

22MAN07-SOFT / ANALYTICAL SKILLS – III (Common to All Branches and Applicable for (2022-2026) Batch only)				
	L	T	P	C
	1	0	2	0

PREREQUISITE : NIL

Course Objective:	• Improving overall language proficiency for personal or professional reasons • To develop problem solving skills across all levels

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of Continuous Assessment Test**
CO1	Write grammatically correct and coherent sentences.	U	40%
CO2	Develop problem solving skills across all levels.	Ap	30%
CO3	Solve reasoning problems with ease.	An	30%

UNIT I – Verbal Competency	**(5+10)**
Sentence Selection-Paragraph Formation- Sentence Correction- Spellings.	
UNIT II - Aptitude	**(5+10)**
Clocks, Calendar, Age Problems-Problem on Trains- Problems on Numbers - Partnerships.	
UNIT III – Logical & Reasoning	**(5+10)**
Coding and Decoding - Logical Equivalent- Venn Diagram Problem.	
TOTAL (L:15, P:30) : 45 PERIODS	

REFERENCES:

1. Dr. R.S. Aggarwal, "A Modern Approach to Verbal & Non-Verbal Reasoning", S Chand and Company Limited, New Delhi, 2014.
2. Ashish Aggarwal, "Quick Arithmetic", S Chand and Company Limited, New Delhi, 2014.
3. Raymond Murphy, "English grammar in use", Fourth Edition, Cambridge University, 2012.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1									2	3				
2		2		2										
3		2		2										
CO (W.A)		**2**		**2**					**2**	**3**				

- **Ratified by Twelfth Academic Council**

22MAN07R - SOFT/ANALYTICAL SKILLS – III *(Common to All Branches and Applicable for (2023-2027) Batch only)*				
	L	**T**	**P**	**C**
	1	**0**	**2**	**0**
PREREQUISITE : NIL				

Course Objective:	• To improve language proficiency for personal or professional reasons • To enhance students' mathematical problem-solving and critical thinking skills

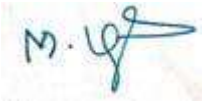
Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in Continuous Assessment Test**
CO1	Demonstrate effective communication skills by listening actively, speaking clearly, reading critically, and writing coherently in contexts.	U	40%
CO2	Develop proficiency in applying mathematical concepts of time, speed, distance, and financial calculations involving simple and compound interest.	Ap	30%
CO3	Analyse logical reasoning skills through various forms of statements.	An	30%

UNIT I – VERBAL ABILITY	**(5+10)**
Grammar - Concord - Relative Clause - **Listening** - IELTS Listening (Advanced) and Gap Filling - **Speaking** - Introducing Others - Formal Conversations - **Reading** - Reading Comprehension - **Writing** - Hints Development.	
UNIT II – APTITUDE	**(5+10)**
Simple and Compound Interest - Time, Speed and Distance - Problems on Trains - Boats and Streams - Chain Rule - Time and Work - Pipe and Cisterns.	
UNIT III - REASONING	**(5+10)**
Seating Arrangements - Syllogism - Statement and Conclusion - Statement and Assumption - Statement and Course of Action.	
TOTAL(L:45) = 45 PERIODS	

REFERENCES:

1. Rizvi, M.Ashraf. *Effective Technical Communication.* Tata McGraw-Hill Education, 2017.
2. Aggarwal R S. *Quantitative* Aptitude for Competitive Examinations. S.Chand Publishing Company Ltd(s)., 2022.
3. Sharma, Arun. *How to Prepare for Quantitative Aptitude for the CAT*. Tata McGraw – Hill Publishing, 2022.
4. Praveen R V. *Quantitative Aptitude and Reasoning.* PHI Learning Pvt. Ltd., 2016.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1									2	3				
2		2		2										
3		2		2										
CO (W.A)		**1**		**1**					**1**	**1**				



- **Ratified by Twelfth Academic Council**

22MAN09 - INDIAN CONSTITUTION (*Common to All Branches*)				
	L	**T**	**P**	**C**
	I	**0**	**0**	**0**

PREREQUISITE : NIL

Course Objective:	• To educate students to learn about the Constitutional Law of India. • To motivate students to understand the role of Union Government. • To make students to understand about State Government. • To understand about District Administration, Municipal Corporation and Zila Panchayat. • To encourage students to Understand about the election commission.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Gain Knowledge about the Constitutional Law of India.	U	Internal Assessment
CO2	Know the Union Government and role of President and Prime Minister.	R	
CO3	Gain knowledge about State Government and role of Governor, Chief Minister.	U	
CO4	Understand the District Administration, Municipal Corporation and Zila Panchayat.	U	
CO5	Understand the role and function of election commission.	U	

UNIT I - THE CONSTITUTION INTRODUCTION	(3)
The History of the Making of the Indian Constitution - Preamble and the Basic Structure, and its interpretation - Fundamental Rights and Duties and their interpretation - State Policy Principles.	
UNIT II - UNION GOVERNMENT	(3)
Structure of the Indian Union - President - Role and Power - Prime Minister and Council of Ministers - Lok Sabha and Rajya Sabha	
UNIT III - STATE GOVERNMENT	(3)
Governor - Role and Power - Chief Minister and Council of Ministers - State Secretariat	
UNIT IV - LOCAL ADMINISTRATION	(3)
District Administration - Municipal Corporation - Zila Panchayat	
UNIT V - ELECTION COMMISSION	(3)
Role and Functioning - Chief Election Commissioner - State Election Commission	
TOTAL (L:15) : 15 PERIODS	

TEXT BOOKS:
1. Rajeev Bhargava, "Ethics and Politics of the Indian Constitution", Oxford University Press, New Delhi, 2008. 2. B.L. Fadia, "The Constitution of India", Sahitya Bhawan; New edition (2017). 3. DD Basu, "Introduction to the Constitution of India", Lexis Nexis; Twenty-Third 2018 edition.
REFERENCES:
1. Steve Blank and Bob Dorf, "The Startup Owner's Manual: The Step-by-Step Guide for Building a Great Company", K & S Ranch ISBN – 978-0984999392 2. Eric Ries, "The Lean Startup: How Today's Entrepreneurs Use Continuous Innovation to Create Radically Successful Businesses" , Penguin UK ISBN - 978-0670921607 3. Adrian J. Slywotzky with Karl Weber, "Demand: Creating What People Love Before They Know They Want It", Headline Book Publishing ISBN - 978-0755388974 4. Clayton M. Christensen, "The Innovator's Dilemma: The Revolutionary Book That Will Change the Way You Do Business", Harvard business ISBN: 978-142219602.
REFERENCES: Web link
1. https://www.fundable.com/learn/resources/guides/startup 2. https://corporatefinanceinstitute.com/resources/knowledge/finance/corporate- structure/ 3. https://www.finder.com/small-business-finance-tips 4. https://www.profitbooks.net/funding-options-to-raise-startup-capital-for-your-business/

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1						3		3		2		3		
2						3		3		2		3		
3						3		3		2		3		
4						3		3		2		3		
5						3		3		2		3		
CO (W.A)						**3**		**3**		**2**		**3**		

22ITC06 - JAVA PROGRAMMING *(Common to 22AIC04 ,22CSC07, 22CCC06,22CIC06 and 22ITC06)*				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**

PREREQUISITE : NIL

Course Objective:	• To understand object-oriented programming concepts, and apply them in solving problems. • To introduce the design of Graphical User Interface using applets and swing controls.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the concepts of classes and objects to solve simple problems using Java	Ap	20%
CO2	Analyze how oops concepts like inheritance, polymorphism improves code organization and enhances flexibility.	An	20%
CO3	Build interactive applications using applets and swing	An	20%
CO4	Conduct practical experiments for demonstrating exception handling, multithreaded applications with synchronization.	An	40%
CO5	Build the Java Project for engineering applications and make an individual study being member of team.	An	Internal Assessment

UNIT I -INTRODUCTION TO OOP AND JAVA FUNDAMENTALS	**(9)**
Object Oriented Programming - Abstraction – objects and classes - Encapsulation- Inheritance - Polymorphism- OOP in Java – Characteristics of Java – The Java Environment - Java Source File -Structure – Compilation. Fundamental Programming Structures in Java – Defining classes in Java – constructors, methods -access specifiers - static members -Comments, Data Types, Variables, Operators, Control Flow, Arrays , Strings, Packages - JavaDoc comments.	
UNIT II - INHERITANCE AND INTERFACES	**(9)**
Inheritance – Super classes- sub classes –Protected members – constructors in sub classes- the Object class – abstract classes and methods-Keywords: Static-final-this- final methods and classes – Method overloading-Method overriding-Interfaces – defining an interface, implementing interface, differences between classes and interfaces and extending interfaces	
UNIT III -EXCEPTION HANDLING AND I/O	**(9)**
Exceptions - exception hierarchy - throwing and catching exceptions – built-in exceptions, creating own exceptions, Stack Trace Elements. Input / Output Basics – Streams – Byte streams and Character streams – Reading and Writing Console – Reading and Writing File	

UNIT – IV –THREADS	(9)
Java Thread Model – Main Thread – Creating a Thread – Creating Multiple Threads — Thread Priorities – Synchronization – Inter thread Communication – Suspending, Resuming, and Stopping Threads – Using Multithreading.	
UNIT – V EVENT DRIVEN PROGRAMMING	**(9)**
Graphics programming - Frame – Components Basics of event handling - event handlers - adapter classes - actions - mouse events - AWT event hierarchy - Introduction to Swing – layout management - Swing Components – Text Fields , Text Areas – Buttons- Check Boxes – Radio Buttons – Lists- choices- Scrollbars – Windows –Menus – Dialog Boxes.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Herbert Schildt, "Java: The Complete Reference", 11th Edition, McGraw Hill Education, New Delhi, 2019 for Units I, II, III, IV.
2. Herbert Schildt, "Introducing JavaFX 8 Programming", 1st Edition, McGraw Hill Education, New Delhi, 2015 for Unit V.

REFERENCES:

1. Cay. S. Horstmann, Gary Cornell, "Core Java-JAVA Fundamentals", Prentice Hall, 10th ed., 2016.
2. Paul Deitel, Harvey Deitel, "Java SE 8 for programmers", 3rd Edition, Pearson, 2015.3. SCJP Sun Certified Programmer for Java 6 Study Guide. 6th edition, McGraw Hill.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3												1
3		3											1	
4		3											2	
5		3				2			2		2	1		2
CO (W.A)	**3**	**3**				**2**			**2**		**2**	**1**	**1.5**	**1.5**

22ECC09 - ANALOG CIRCUIT DESIGN				
	L	T	P	C
	3	0	0	3
PREREQUISITE : 22ECC04				
Course Objective:	• To apply PLL fundamentals to demodulate AM, FM and FSK signals • To analyze the Slew rate and CMRR of operational amplifier • To construct Active load Resistance, ADC-DAC and waveform generators • To implement the application circuits of operational amplifier and voltage regulators			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the concepts of current sources, active loads and differential amplifiers in analyzing the characteristics of operational amplifiers.	Ap	20%
CO2	Apply operational amplifiers in linear and nonlinear circuit applications and analyze their performance in signal processing systems.	Ap	20%
CO3	Analyze the operation and applications of analog multipliers and phase-locked loops in communication and modulation systems.	An	20%
CO4	Apply and analyze the working of various digital-to-analog and analog-to-digital converters for interfacing analog signals with digital systems.	An	20%
CO5	Apply waveform generators, timer ICs, voltage regulators and special function ICs in designing analog circuits and analyze their role in real-world electronic applications.	Ap	20%

UNIT I - CIRCUIT CONFIGURATION FOR LINEAR ICS	**(9)**
Introduction-Current mirror and current sources, Current sources as active loads, Voltage sources, Voltage References, BJT Differential amplifier with active loads, Ideal operational amplifier, General operational amplifier stages ,IC 741 Op-Amp, slew rate, CMRR, Open and closed loop configurations.	
UNIT II -APPLICATIONS OF OPERATIONAL AMPLIFIERS	**(9)**
Sign Changer, Scale Changer, Voltage Follower, V-to-I and I-to-V converters, Summing amplifier, Instrumentation amplifier, Integrator, Differentiator, Logarithmic amplifier, Antilogarithmic amplifier, Comparators, Schmitt trigger, Low-pass, high-pass and band-pass filters.	
UNIT III - ANALOG MULTIPLIER AND PLL	**(9)**
Analog Multiplier- Applications- Squarrer and frequency doubler, Gilbert Multiplier cell – Variable transconductance technique, Operation of the basic PLL, Capture range, Lock in range and Pull in time, Application of PLL for AM detection, FM detection, FSK modulation and demodulation.	

UNIT IV - DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERTERS	(9)
Introduction- D/A converter – specifications -Binary weighted resistor type, R-2R Ladder type, High speed sample-and-hold circuits, A/D Converters –specifications - Flash type - Successive Approximation type - Single Slope type – Dual Slope type.	
UNIT V -WAVEFORM GENERATORS AND SPECIAL FUNCTION IC's	**(9)**
Sine-wave generators, and Triangular wave generator, Saw-tooth wave generator, CL8038 function generator, Timer IC 555- Astable and Monostable operation, IC Voltage regulators – Three terminal fixed and adjustable voltage regulators - IC 723 general purpose regulator -Monolithic switching regulator, Frequency to Voltage and Voltage to Frequency converters, Video Amplifier, Opto-couplers and fiber optic IC.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. S. Salivahanan and V.S. Kanchana Bhaaskaran, "Linear Integrated Circuits", Tata McGraw Hill (2008).
2. Sergio Franco, "Design with Operational Amplifiers and Analog Integrated Circuits" , 3'rd Edition,TMH,2007.

REFERENCES:

1. Robert F. Coughlin and Driscoll, "Operational Amplifiers and Linear Integrated Circuits", 6th ed., Pearson Education. 2009.
2. P. R. Gray and R. G. Meyer, "Analysis and Design of Analog Integrated Circuit", John Wiley, 2009.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												3	
2		3											3	
3			3										3	
4			3										3	
5												2		
CO (W.A)	3	3	3									2	3	

22ECC10 - TRANSMISSION LINES AND RF SYSTEMS				
	L	T	P	C
	3	0	0	3

PREREQUISITE : 22ECC08

Course Objective:	• Introduce various types of transmission lines and analyze the lumped circuit model of a transmission line and their characteristics. • To find SWR, Reflection Coefficient and impedance matching using Smith Chart. • To investigate the propagation of electromagnetic waves in Parallel plane, Rectangular and Circular waveguides. • To illustrate about the basic RF components

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply fundamental principles of Electromagnetic theory to derive problems in transmission lines.	Ap	30%
CO2	Perform impedance matching to reduce losses in transmission lines	Ap	30%
CO3	Analyze guided wave propagation in different structures and calculate characteristic parameters.	An	20%
CO4	Comprehend the basic concepts of RF components and use them in modern communication systems.	An	20%
CO5	Make an oral presentation on topics related to transmission lines and broadcast standards and acquire skills require for sustained lifelong learning.	U	Internal Assessment

UNIT I - TRANSMISSION LINE THEORY	**(9)**
Line Parameters, The transmission line – general solution, Physical significance of the equation, Wavelength and velocity of wave propagation, Waveform distortion, The distortion less line, the telephone cable, Loading of Transmission Line, Line not terminated in Z0- Reflection coefficient, Open circuit and short circuit line, Reflection factor and Reflection loss, Insertion Loss.	
UNIT II - IMPEDANCE MATCHING	**(9)**
Standing waves and standing wave ratio, Impedance matching- Half wavelength and Quarter wave transformer, single stub matching and Double stub matching. Smith chart, Applications - Measurement of VSWR, impedance, single stub and double stub using smith chart.	
UNIT III - GUIDED WAVES	**(9)**
Wave between the parallel planes, Transmission of TM waves between Parallel planes – Transmission of TE waves between Parallel planes. Transmission of TEM waves between Parallel planes –Velocities of the waves. Characteristic impedance of parallel plane.	
UNIT IV - RECTANGULAR AND CIRCULAR WAVEGUIDES	**(9)**
Applications of Maxwell's equations to the rectangular waveguide, TM waves in Rectangular waveguide, TE waves in Rectangular waveguide, Dominant mode in Rectangular waveguide - TM waves in Circular waveguide, TE waves in Circular waveguide , Dominant mode in Circular waveguide.	

UNIT V - RF COMPONENTS	(9)
Active RF components: Semiconductor basics in RF, bipolar junction transistors, RF Field Effect Transistors, High Electron Mobility Transistors Basic concepts of RF design, Mixers, Low Noise Amplifiers, Voltage Control Oscillators, Power Amplifiers, Transducer power gain and stability considerations.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. John D Ryder, "Networks, lines and fields", 2nd Edition, Prentice Hall India, 2015.
2. Mathew M. Radmanesh, "Radio Frequency & Microwave Electronics", Pearson India, First Edition, 2015.

REFERENCES:

1. Reinhold Ludwig and Gene Bogdanov," RF Circuit Design – Theory and Applications", Pearson Education Asia, Second Edition, 2009.
2. D. K. Misra, "Radio Frequency and Microwave Communication Circuits- Analysis and Design", John Wiley & Sons, 2004.
3. E.C.Jordan and K.G. Balmain, "Electromagnetic Waves and Radiating Systems", Pearson Education India, 2015.
4. G.S.N Raju, "Electromagnetic Field Theory and Transmission Lines Pearson Education", First edition 2005.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	2	3											2	
3		3											3	
4	3													
5						3		2	3	3		3		
CO (W.A)	2.6	3				3		2	3	3		3	2.5	

22ECC11 - DIGITAL SIGNAL PROCESSING				
	L	T	P	C
	3	0	2	4

PREREQUISITE : 22ECC06

Course Objective:	• To learn discrete Fourier transforms and Fast Fourier Transform and its properties. • To know the characteristics of FIR filters and IIR filters and design of filters for filtering undesired signals. • To understand Finite word length effects. • To understand the fundamental concepts of multi rate signal processing and its applications

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the knowledge of signal processing to solve Engineering problems on Discrete Fourier Transform and Filters.	Ap	30%
CO2	Apply transform technique concepts in realizing Discrete time signal and Digital filters.	Ap	20%
CO3	Analyze the concepts of finite word length effects and multirate signal processing to minimize the errors and improve the performance.	An	20%
CO4	Design of Digital Filters for given specifications.	An	30%
CO5	Use appropriate tools to conduct experiments involving implementation of DSP concepts and filters.	E	Laboratory Exam

UNIT I - FAST FOURIER TRANSFORMS	**(9)**
Introduction to DFT and IDFT, Properties of DFT, FFT Algorithm-Radix-2 - Decimation in Time (DIT)-Decimation in Frequency (DIF)Fast Convolution-Overlap Save method-Overlap Add Method.	
UNIT II – DIGITAL IIR FILTERS	**(9)**
Review of design techniques for analog low pass filter (Butterworth and Chebyshev type-1), Frequency transformation in Analogue domain, IIR filter Design: Bilinear and Impulse Invariant Techniques, Realization structures for IIR filters	
UNIT III - DIGITAL FIR FILTERS	**(9)**
Design characteristics of FIR filters with linear phase – Frequency response of linear phase FIR filters - Design of FIR filters using window functions (Rectangular, Hamming, Hanning, and Blackman) - Realization structures for FIR filters(Direct form I and II).	
UNIT IV - FINITE WORD LENGTH EFFECTS	**(9)**
Fixed point and floating point number representation - ADC - quantization - truncation and rounding-quantization noise - input / output quantization - coefficient quantization error - product quantization error - overflow error - limit cycle oscillations due to product quantization and summation	

UNIT V - MULTIRATE SIGNAL PROCESSING	(9)
Multirate signal processing: Decimation, Interpolation, Sampling rate conversion by a rational factor I/D – Implementation of sampling rate conversion: Polyphase filter Structures- Interchange of filters and Downsamplers /Upsamplers –Application of Multirate signal processing.	

LIST OF PROGRAMS USING MATLAB (Laboratory Component**):**

1. Implementation of DIT and DIF Algorithms.
2. Implementation of Low pass and high pass FIR filter for a given sequence.
3. Implementation of Low pass and high pass IIR filter for a given sequence.
4. Verification of Sampling Theorem.
5. Implementation of Decimation Process

TOTAL (L:45 P:30) : 75 PERIODS

TEXT BOOKS:

1. J.G.Proakis, D.G.Manolakis and D.Sharma, "Digital Signal Processing, Algorithms and Applications", Pearson Education, 2012.

REFERENCES:

1. S. Salivahanan, A. Vallavaraj and G.Gnanapriya, "Digital Signal Processing", Tata McGraw-Hill Company Publication Limited, 21 st Reprint 2007.
2. Oppenheim V.A.V and Schaffer R.W, "Discrete – time Signal Processing", 2nd Edition, Prentice Hall, 2013.
3. S.K.Mitra, Digital Signal Processing, 4th Edition, TMH, 2010.
4. Lawrence R Rabiner and Bernard Gold, "Theory and Application of Digital Signal Processing", PHI 2010.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	1
2	3												1	2
3		3											2	2
4			3										1	2
5					3				2	1			2	3
CO (W.A)	**3**	**3**	**3**		**3**				**2**	**1**			**2**	**2**

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22ECC12 - ANALOG AND DIGITAL COMMUNICATION				
	L	T	P	C
	3	0	0	3
PREREQUISITE : 22ECC06				

Course Objective:	• To provide knowledge on complete analysis of Amplitude and Angle modulation schemes. • To deliberate the performance of Pass band, base band and spread spectrum communication. • To learn the concepts of information theory and basics of error control coding.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply various concepts of theorems and Transforms for computing parameters of Communication systems.	Ap	20%
CO2	Analyze performance of different types of Analog modulation Techniques and information theory concepts for a given set of parameters.	An	20%
CO3	Analyze performance of different types of Digital modulation Techniques for a given set of parameters.	An	40%
CO4	Design Analog and Digital Communication subsystems for given set of specifications.	C	20%
CO5	Engage in independent/team learning, communicate effectively, develop a project that implement Analog and Digital Communication concepts and give oral presentation.	U	Internal Assessment

UNIT I - AMPLITUDE MODULATION	**(9)**
Functional block diagram of communication systems- Linear modulation schemes: Generation of AM: DSBFC using balanced modulator- Introduction to DSBSC, SSBSC and VSB Signals- Comparison of Amplitude Modulation Systems. Super heterodyne receivers- Noise in AM receivers - coherent detection, envelope detection.	
UNIT II - ANGLE MODULATION	**(9)**
Frequency modulation and Phase modulation, Relation between FM and PM waves Narrowband FM, Wideband FM-Generation of FM: indirect method-FM demodulation: frequency discriminator-Non linear effects in FM systems-Noise in FM receivers-capture effect-pre emphasis and de-emphasis in FM.	
UNIT III - INFORMATION THEORY AND CODING	**(9)**
Entropy and its properties-source coding theorem: Shanon-Fano coding, Discrete memory less channel-mutual information and its properties-channel coding theorem-information capacity theorem; Hamming codes,	

UNIT IV - PULSE MODULATION AND BASEBAND TRANSMISSION	(9)
Sampling process-PAM, PPM, PWM-Quantization process-PCM-DPCM-Delta Modulation-Adaptive delta modulation-Classification of line coding and Decoding-Matched Filter –Error rate due to noise –Inter symbol Interference-Eye patterns - Nyquist criterion for distortion less base band Binary Transmission-Correlative level coding: Duo binary with and without precoder- Modified duo binary with and without precoder.	
UNIT V - PASSBAND DATA AND SPREAD SPECTRUM MODULATION	**(9)**
Pass band Transmission model-Generation, detection ,signal space diagram, bit error probability and power spectra of Binary Modulation schemes (ASK,FSK,PSK), Quadrature Modulation schemes (QPSK,QAM) – Comparison of Binary and Quadrature modulation techniques. Spread Spectrum: PN sequence and its properties- Direct sequence spread spectrum-Frequency Hopping spread spectrum.	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:

1. Simon Haykin, "Communications Systems", Wiley Education, 5th Edition, 2009.
2. T L Singal, "Analog & Digital Communications", Tata McGraw-Hill Education, 4th Edition, 2012

REFERENCES:

1. Taub H and Schilling D L, "Principles of Communication Systems", McGraw Hill, 4th Edition, 2017.
2. Wayne Tomasi, "Electronic Communications Systems–Fundamentals Through advanced", Pearson Education, 4th Edition, 2007.
3. Praokis J.G., "Digital Communications" 5th Edition, McGraw Hill, 2014.
4. Bernard Sklar, Pabitra Kumar Ray "Digital Communications: Fundamentals & Applications", Pearson Education, 2nd Edition, 2009.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3		3											2	
4			3										3	
5									2	2	2	2	2	
CO (W.A)	**3**	**3**	**3**						**2**	**2**	**2**	**2**	**2.2**	

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22ITP04 - JAVA PROGRAMMING LABORATORY *(Common to 22AIP03, 22CSP06, 22CCP05, 22CIP05 and 22ITP04)*				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**
PREREQUISITE : Nil				

Course Objective:	• To learn Java Programming concepts and develop applications based on Java.

Course Outcomes The Student will be able to		**Cognitive Level**
CO1	Apply the concepts of Java to solve problems	Ap
CO2	Analyze the efficiency of using appropriate programming constructs.	An
CO3	Demonstrate the usage of different programming structures through example programs	Ap
CO4	Develop simple applications using swing.	C
CO5	Engage in independent study and learn to use Java for real time applications.	An

LIST OF EXPERIMENTS

1. Write simple Java programs using operators, arrays and control statement
2. Programs using Static, final and this keyword.
3. Demonstrate the concepts of inheritance
4. Programs illustrating overloading and overriding methods in Java
5. Programs to use packages and Interfaces in Java.
6. Implement exception handling and creation of user defined exception.
7. Implement program to demonstrate multithreading and inter thread communication.
8. Write a program to perform file operations
9. Develop Applications using Swing Layouts.

TOTAL (P:60) = 60 PERIODS

HARDWARE OR SOFTWARE REQUIREMENT:

HARDWARE:

1. LAN System with 33 nodes (OR) Standalone PCs – 33 Nos.
2. Printers – 3 Nos.

SOFTWARE:

1. Java / Equivalent Compiler

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3													
2		3												1
3	3													
4			3										2	
5						2			2				1	
CO (W.A)	**3**	**3**	**3**			**2**			**2**				**1.5**	**1**

22ECP04 - ANALOG CIRCUIT DESIGN LABORATORY				
	L	T	P	C
	0	0	4	2
PREREQUISITE : 22ECC04, 22ECC07				

Course Objective:	• To design various voltage amplifiers, linear application circuits using Op-amp. • To design the Oscillator circuit using Op-amp and construct active filters and the application circuits of 555 timer IC.

	Course Outcomes The Student will be able to	**Cognitive Level**
CO1	Apply the fundamentals of Electronics in amplifier and oscillator design.	Ap
CO2	Analysis the frequency response of Active Filters.	An
CO3	Design the PWM and timers using multivibrators.	E
CO4	Evaluate the frequency of oscillator circuits.	E
CO5	Solve the problems occur in analog circuit by team work using PSPICE.	An

LIST OF EXPERIMENTS

1. Design of Inverting and Non Inverting amplifier for a specified gain using IC741.
2. Design of Inverting and Non Inverting Summing amplifier and using IC-741.
3. Design of differentiator and integrator for a specified gain using IC741.
4. Design of a sinusoidal oscillator for specified frequency based on RC phase shift oscillators using IC-741.
5. Design of Astable Multivibrators using NE555 Timer.
6. Design of Pulse Width Modulator circuit using NE555 Timer.
6. Design of Monostable Multivibrators using NE555 Timer.
8. Design of Active LPF and HPF and plot their frequency response.
9. Study of Voltage Regulator using IC723

TOTAL (P: 60) = 60 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3												3	
2		3											3	
3			3										3	
4				3									3	
5					2				2					
CO (W.A)	**3**	**3**	**3**	**3**	**2**				**2**				**3**	

22ECP05 – ANALOG AND DIGITAL COMMUNICATION LABORATORY				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**

PREREQUISITE : 22ECC06

Course Objective:	• To demonstrate the concepts of generation and detection of AM and FM modulation schemes. • To demonstrate the concepts of generation and detection of digital modulation and pulse modulation schemes.

Course Outcomes The Student will be able to		**Cognitive Level**
CO1	Apply the concepts of modulation, demodulation of AM,FM and ASK,FSK,PSK communication systems	Ap
CO2	Analyze the concepts and related to Pre-emphasis and De-emphasis, QPSK modulation schemes.	An
CO3	Conduct experiments to demonstrate concepts related to Analog and digital pulse modulation using suitable electronic components.	E
CO4	Use modern tools to design and analyze the Amplitude and Angle modulation systems.	C
CO5	Involve in Independent/team learning effectively and engage in lifelong learning.	E

LIST OF EXPERIMENTS

1. Generation and Detection of Amplitude modulation signals.
2. Generation and Detection of Frequency Modulation.
3. Response of Pre-Emphasis / De-emphasis Circuits.
4. Sampling process - (PAM)
5. Generation of Pulse Modulation waveforms– PWM / PPM
6. Generation of Line Coding and Decoding techniques.
7. Generation and detection of digital modulation schemes- ASK, PSK, FSK.
8. Generation and detection of QPSK waveforms.
9. Generation and detection of Delta Modulation waveforms.
10. Implementation of Error Control Coding using MATLAB.

TOTAL : 60 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	1
2		2											1	2
3					3								3	
4		2			2								2	2
5									2			1	1	1
CO (W.A)	**3**	**2**			**2.5**				**2**			**1**	**1.8**	**1.5**

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22MAN08- SOFT / ANALYTICAL SKILLS – IV *(Common to All Branches and Applicable for (2022-2026) Batch only)*				
	L	**T**	**P**	**C**
	1	**0**	**2**	**0**
PREREQUISITE : NIL				

Course Objective:	• To recollect the functional understanding of basic grammar and its structure • To enrich their knowledge and to develop their logical reasoning ability

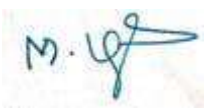
Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of Continuous Assessment test**
CO1	Construct the sentences with basic grammar.	U	40%
CO2	Analyze quantitative aptitude problems and find solutions.	Ap	30%
CO3	Develop the ability to solve problems through logical reasoning.	An	30%

UNIT I - VERBAL	**(5+10)**
Articles - Fill in the blanks - Grammatical Error - Sentence improvement	
UNIT II - APTITUDE	**(5+10)**
Speed and Distance-Time and Work- Mixture And Alligations- Permutation and Combinations	
UNIT III - LOGICAL AND REASONING	**(5+10)**
Seating Arrangement- Directions and Distance- Non verbal Reasoning	
TOTAL (L:45) = 45 PERIODS	

REFERENCES:

1. Murphy, Raymond. *English Grammar in Use*. Fourth Edition, Cambridge University, 2012.
2. Dr. R.S. Aggarwal. *A Modern Approach to Verbal & Non-Verbal Reasoning.* S Chand and Company Limited, New Delhi, 2014.
3. Aggarwal, Ashish. *Quick Arithmetic*. S Chand and Company Limited, New Delhi, 2014.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1									2	3				
2		2		2										
3		2		2										
CO (W.A)		**1**		**1**					**1**	**1**				



- **Ratified by Twelfth Academic Council**

22MAN08R - SOFT/ANALYTICAL SKILLS – IV *(Common to All Branches and Applicable for (2023-2027 Batch only)*				
	L	**T**	**P**	**C**
	1	**0**	**2**	**0**

PREREQUISITE : NIL

Course Objective:	• To enhance the ability to communicate coherently and effectively across contexts. • To develop quantitative aptitude and analytical reasoning skills.

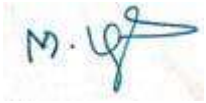
Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in Continuous Assessment Test**
CO1	Develop proficiency to communicate accurately, fluently, and appropriately in various academic, professional and social contexts.	U	40%
CO2	Solve quantitative aptitude problems with more confidence.	Ap	30%
CO3	Draw valid conclusions, identify patterns, and solve problems.	An	30%

UNIT I – VERBAL ABILITY	**(10+5)**
Grammar - Sentence Completion – Sentence Improvement - Error Spotting - **Listening** - TOEFL Listening Practice Tests - **Speaking** – Interview Skills - **Reading** - GRE Reading Passages - **Writing** - Paragraph Writing.	
UNIT II – APTITUDE	**(10+5)**
Probability - Permutations and Combinations - Data Interpretation on Multiple Charts - Mensuration - Area, Shapes, Perimeter - Races and Games.	
UNIT III - REASONING	**(10+5)**
Data Sufficiency - Mathematical Operations - Pattern Completion - Cubes - Embedded Images.	
TOTAL(L:45) = 45 PERIODS	

REFERENCES:

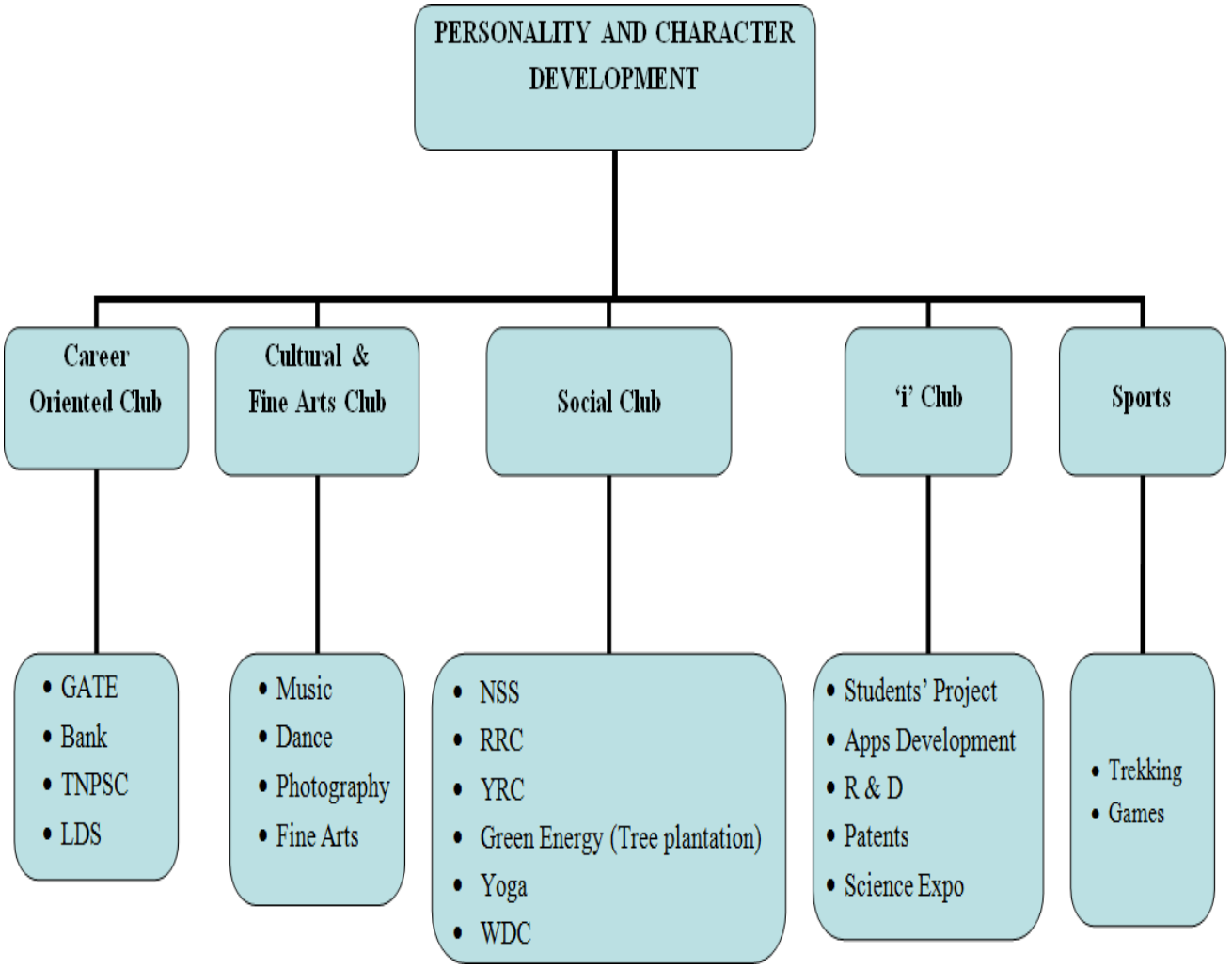
1. Rizvi, M.Ashraf. "Effective Technical Communication", Tata McGraw-Hill Education, 2017.
2. Aggarwal R S. "Quantitative Aptitude for Competitive Examinations", S.Chand Publishing Company Ltd(s)., 2022.
3. Sharma, Arun. "How to Prepare for Quantitative Aptitude for the CAT", Tata McGraw – Hill Publishing, 2022.
4. Praveen R V. "Quantitative Aptitude and Reasoning", PHI Learning Pvt. Ltd., 2016.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1									2	3				
2		2		2										
3		2		2										
CO (W.A)		**1**		**1**					**1**	**1**				



- **Ratified by Twelfth Academic Council**

22GED01 – PERSONALITY AND CHARACTER DEVELOPMENT				
	L	T	P	C
	0	0	I	0
PRE REQUISITE : NIL				



*LDS - Leadership Development Skills

OBJECTIVES :				
Career Oriented Club	**Cultural & Fine Arts Club**	**Social Club**	**'i' club**	**Sports**
• To provide support for identifying specific career field of interests and career path • To provide support for preparing for competitive exams	• To bring out the hidden talent of students in music, dance and other fine arts. • To promote photography skill among the students • To develop and enhance the performance of students by participating in various events. • To inculcate managerial capabilities such as event management and stage organization.	• To create social awareness and develop a sense of social and civic responsibility • To inculcate socially and environmentally sound practices and be aware of the benefits • To encourage the students to work along with the people in rural areas, thereby developing their character, social consciousness, commitment, discipline and being helpful towards the community.	• To inculcate the basic concepts of innovation • To foster the networking between students, build teams, exchange ideas, do projects and discuss entrepreneurial opportunities. • To enrich the academic experience, build competencies and relationships beyond the classroom	• To provide opportunities to excel at sports • To promote an understanding of physical and mental well-being through an appreciation of stress, rest and relaxation. • To develop an ability to observe, analyze and judge the performance of self and peers in sporting activities. • To develop leadership skills and nurture the teambuilding qualities. **Trekking:** • To provide opportunities to explore nature and educating about the purityof nature • To improve physical and mental health.

OUTCOMES : At the end of this course, the students will be able to				
• Find a better career of their interest. • Make use of their knowledge during competitive exams and interviews.	• Take part in various events. • Develop team spirit, leadership and managerial qualities.	• Develop socially responsive qualities by applying acquired knowledge. • Build character, social consciousness, commitment and discipline.	• Apply the acquired knowledge in creating better solutions that meet new requirements and market needs. • Develop skills on transforming new knowledge or new technology into viable products and services on commercial markets as a team.	• Demonstrate positive leadership skills that contribute to the organizational effectiveness • Take part an active role in their personal wellness (emotional, physical, and spiritual) that supports a healthy lifestyle • Create inclination towards outdoor activity like nature study and Adventure.

TOTAL [2 x (P: 15)]: 30 PERIODS

(Cumulatively for Two Semesters)

22ECC13 - MICROPROCESSOR AND MICROCONTROLLER INTERFACING				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				

Course Objective:	• To know the internal architecture, instruction set, and operational principles of 8-bit Microprocessor, 8-bit Microcontroller and their associated peripherals. • To expertise in assembly language and high level language programming for 8-bit Microprocessor and Microcontroller. • To illustrate the methods and techniques for interfacing various peripherals with microcontrollers and providing practical examples.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the Architectural concepts to operate and interface an 8-bit microprocessor, microcontroller, and its peripherals in various practical scenarios.	Ap	20%
CO2	Apply diverse programming techniques for developing Microprocessor and Microcontroller based systems.	AP	30%
CO3	Analyze memory and input/output systems for efficient data handling and processing in Microprocessor and Microcontroller environment.	An	30%
CO4	Design Microprocessor and Microcontroller based real time applications using modern engineering tools.	C	20%
CO5	Engage independently or collaboratively, demonstrate designs and deliver oral presentations on the applications of Microprocessor and Microcontroller based systems.	U	Internal Assessment

UNIT I - 8 BIT MICROPROCESSOR &MEMORY ORGANIZATION	**(9)**
Origin and classification of Microprocessor - 8085 Architecture- Addressing mode – Instruction Set- Computer system Memory Overview- Cache Memory Principles – Elements of Cache Design.	
UNIT II - 8051 MICROCONTROLLER	**(9)**
8051 Microcontroller: Architecture– Signals – Memory Organization - Interrupts – Timer/counter - Serial communication	
UNIT III - 8051 ASSEMBLY LANGUAGE PROGRAMMING	**(9)**
8051 Addressing mode – Instruction Set – Programming 8051 Timers – Serial Port programming – Interrupt Programming.	
UNIT IV - HIGH LEVEL LANGUAGE PROGRAMMING	**(9)**
Data types and time delay in 8051 C – I/O Programming in 8051 C – Logical operations in 8051 C – Accessing code ROM space in 8051 C – Timer programming in C – Serial port programming in C – Interrupt programming in C	
UNIT V- UNIT V - 8051 EXTERNAL INTERFACING	**(9)**
LCD & Keyboard Interfacing - ADC, DAC & LM35 Temperature Sensor Interfacing - External Memory Interface- Stepper Motor Interfacing	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:
1. Mohamed Ali Mazidi, Janice GillispieMazidi, RolinMcKinlay, "The 8051 Microcontroller and Embedded Systems: Using Assembly and C", Second Edition, Pearson education, 2011 2. Ramesh S. Goankar, "Microprocessor Architecture: Programming and Applications with the8085", Sixth edition, Penram International, 2015 Reprint 3. William Stallings, "Computer organization and architecture Designing for Performance", Tenth Edition, Pearson Education, 2016.
REFERENCES:
1. Senthilkumar, Saravanan, Jeevanantham, Shan "Microprocessor & Interfacing", Oxford University press, 2012. 2. K.UmaRao. AndhePallavi, "The 8051 Microcontroller Architecture, Programming and Applications" Pearson Education 2011, Second Impression.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3													
2	3												3	
3		3											3	
4			3								2			3
5				3	2				3	2		1		3
CO (W.A)	**3**	**3**	**3**	**3**	**2**				**3**	**2**	**2**	**1**	**3**	**3**

22ECC14 – DATA COMMUNICATION AND NETWORKS				
	L	T	P	C
	3	0	0	3

PREREQUISITE :NIL

Course Objective:	• To understand the concepts of computer networks. • To study about multiple access techniques, network protocols. • To get awareness about the performance of internetworking and networking technologies.

	Course Outcomes The Student will be able to	**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the Communication and Networking concepts to Communicate across computer networks.	Ap	20%
CO2	Analyze the data communication systems, including network architecture, Protocols and performance metrics	An	30%
CO3	Implement network protocols and technologies to ensure efficient data transfer.	An	30%
CO4	Evaluate and analyze the security and privacy aspects of data communication systems including encryption, firewalls and access control	E	20%
CO5	Give a presentation on recent technological development in data communication and network protocols	U	Internal Assessment

UNIT I –FUNDAMENTALS OF DATA COMMUNICATION	**(9)**
Introduction – Data communication-network types – Connecting devices: Hubs-Link layer Switches, Routers- ISO/OSI model-TCP/IP- Transmission Media: Guided and Unguided media-Switching Techniques	
UNIT II –DATA LINK LAYER	**(9)**
Introduction –- Data Link Control-Error Control: types of errors-Redundancy- coding: block coding-Hamming Distance- parity check codes – cyclic codes – Media Access Control-Link layer Addressing-Ethernet – WiFi, IEEE 802.11 Project – Bluetooth	
UNIT III –NETWORK LAYER	**(9)**
Network Layer services – Packet Switching –Network Layer performance - IPv4 Addresses- ICMPv4-Forwarding of IP Packets- Next Generation Internet Protocol(IPV6)- Transition from IPV4 to IPV6 - Routing Algorithms: Distance Vector Routing, Link State Routing, Path Vector Routing	
UNIT IV –TRANSPORT LAYER	**(9)**
Transport Layer Services- Transport Layer Protocols: – User Datagram Protocol (UDP) –Transmission Control Protocol (TCP) –SCTP- Quality of service – Data flow characteristics – Flow control to improve QoS: Token Bucket and Leaky Bucket	
UNIT V- APPLICATION LAYER	**(9)**
Introduction- Client/Server Paradigm- Standard Applications: World wide web and HTTP – FTP- Email – Telnet – SSH- Domain Name System- Multimedia Data- Multimedia in the Internet -Cryptography and Network security: Introduction –Confidentiality – Other aspects of Security	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:
1. Behrouz A. Forouzan, "Data Communication and Networking", 6th India Edition, Tata McGraw-Hill, 2017.
REFERENCES:
1. Tanenbaum, Andrew S and David Wetherall, —Computer Networks, 5th Edition, PHI Learning, New Delhi, 2010. 2. Kurose, James F. and Ross, Keith W., —Computer Networking: A Top-Down Approach Featuring the Internet,6th Edition, Pearson Education, New Delhi, 2012.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3											3	
3			3										3	
4				3	3								3	
5							2		2				2	
CO (W.A)	3	3	3	3	3		2		2				2.8	

22CYB06 - ENVIRONMENTAL SCIENCE AND SUSTAINABILITY *(Common to CHEM-2nd, BME-3rd, ECE-5th AND EEE-4th SEM)*				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**

PREREQUISITE : NIL

Course Objective:	• To impart knowledge on ecosystem, biodiversity, environmental pollution and familiarize about sustainable development, carbon credit and green materials. • To make the students conversant with the global and Indian scenario of renewable resources, causes of their degradation and measures to preserve them.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Illustrate the values and conservation methods of biodiversity.	Ap	20%
CO2	Predict the causes, effects of environmental pollution and contribute the preventive measures to the society.	An	20%
CO3	Analyze the renewable and non-renewable resources and preserve them for future generations.	An	20%
CO4	Examine the different goals of sustainable development and apply them for suitable technological advancement and societal development.	Ap	20%
CO5	Execute the sustainability practices, identify green materials and energy cycles.	E	20%

UNIT I - ENVIRONMENT AND BIODIVERSITY	(9)
Environment - scope and importance - Eco-system- Structure and function of an ecosystem - types of biodiversity- genetic - species and ecosystem diversity– Values of biodiversity - India as a mega-diversity nation – Hot-spots of biodiversity – Threats to biodiversity - habitat loss - poaching of wildlife - man-wildlife conflicts – endangered and endemic species of India – Conservation of biodiversity - In-situ and ex-situ.	
UNIT II - ENVIRONMENTAL POLLUTION	(9)
Pollution – Causes - Effects and Preventive measures of Water – Soil - Air - Noise Pollution - Solid waste management - methods of disposal of solid waste – various steps of Hazardous waste management - E-Waste management - Environmental protection – Air acts – water acts.	
UNIT III - RENEWABLE SOURCES OF ENERGY	(9)
Energy management and conservation -New Energy Sources - Different types new energy sources – Hydrogen energy – Geothermal energy - Solar energy – wind energy – biomass energy - Applications of Hydrogen energy - Ocean energy resources -Tidal energy conversion.	
UNIT IV – SUSTAINABILITY AND MANAGEMENT	(9)
Development – Factors affecting development – advantages – disadvantages – GDP - Sustainability- needs – concept - from unsustainability to sustainability - millennium development goal - Sustainable Development goals - Climate change – Concept of carbon credit – carbon footprint - Environmental management.	

UNIT V – SUSTAINABILITY PRACTICES	(9)
Zero waste and R concept - ISO 14000 Series - Environmental Impact Assessment - Sustainable habitat - Green buildings - Green materials- Sustainable energy - Non-conventional Sources - Energy Cycles- carbon cycle and carbon emission - Green Engineering - Sustainable urbanization.	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:

1. Dr. A.Ravikrishan, Environmental Science and Engineering, Sri Krishna Hitech Publishing Co. Pvt. Ltd., Chennai,15thEdition, 2023.
2. Anubha Kaushik and C. P. Kaushik's "Perspectives in Environmental Studies", 6th Edition, New Age International Publishers, 2018.

REFERENCES:

1. Rajagopalan, R, 'Environmental Studies-From Crisis to Cure', Oxford University Press, Third Edition, 2015.
3. Erach Bharucha "Textbook of Environmental Studies for Undergraduate Courses" Orient Blackswan Pvt. Ltd. 2013.

WEB LINK:

1. http://www.jnkvv.org/PDF/0804202021512 8Amit1.pdf
2. https://www.conserve-energy-future.com/types-of-renewable-sources-of-energy.php
3. https://ugreen.io/sustainability-engineering-addressing-environmental-social-and-economic- issues/

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1		2												
2			2				3							
3	2		2					2						
4							3							
5						3						2		
CO (W.A)	2	2	2			3	3	2				2		

22ECP06 - MICROPROCESSOR AND MICROCONTROLLERS LABORATORY					
		L	**T**	**P**	**C**
		0	**0**	**4**	**2**
PREREQUISITE : NIL					
Course Objective:	• To enable the student to analyze various arithmetic, logical and control transfer operations using 8085 Microprocessor. • To provide the student with practice in the 8051 Microcontroller arithmetic, Logical operations. • To motivate the students to learn the I/O interfacing concepts in 8051 using HLP.				

Course Outcomes The Student will be able to		**Cognitive Level**
CO1	Apply the assembly language programming knowledge to operate and interface an 8-bit Microprocessor, Microcontroller, and its peripherals	Ap
CO2	Apply the diverse programming techniques in Microprocessor and Microcontroller based system development for various real-world applications.	An
CO3	Examine the functionalities of arithmetic, logical, and control transfer operations performed by 8-bit Microprocessors and Microcontrollers.	E
CO4	Verify the operational capabilities of different peripherals within a Microcontroller environment through High level language programming.	E
CO5	Implement the functionality of fundamental peripherals for various real-world applications using modern engineering tools.	C

LIST OF EXPERIMENTS :

1. Assembly language programming for 8/16 bit Arithmetic operators Using 8085.
2. Assembly language programming with control instructions Using 8085 (Increment / Decrement, Ascending / Descending order, Maximum / Minimum of numbers.
3. Assembly language programming for arithmetic and logical operations using 8051.
4. Interfacing and Programming of DC Motor Speed control using 8051.
5. Interfacing and Programming of Stepper Motor control using 8051.

High Level Language Programming:

The following programs have to be tested on 8051 Development board/equivalent using Embedded C Language on KEIL IDE or Equivalent.

1. Program to toggle all the bits of Port P1 continuously with delay.
2. Program to toggle P1.5 continuously with delay. Use Timer in mode 0, mode 1, mode 2 and mode 3 to create delay using 8051.
3. Program to interface 7 segments display to display a message on it using 8051.
4. Program to interface keypad. Whenever a key is pressed, it should be displayed on LCD using 8051.
5. Program to get analog input from Temperature sensor and display the temperature Value on LCD using ADC with 8051 Microcontroller.
6. Program to handle interrupts with 8051 Microcontroller.

TOTAL (P:60) = 60 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3													
2	3												3	
3		3											3	
4				3										3
5			3		2									3
CO (W.A)	**3**	**3**	**3**	**3**	**2**								**3**	**3**

22ECP07 – DATA COMMUNICATION AND NETWORKS LABORATORY				
	L	T	P	C
	0	0	4	2

PREREQUISITE : NIL

Course Objective:	• To learn the various routing algorithms. • To gain knowledge about the various open source simulation tools for packet tracing and network design. • To understand the peer to peer communication application using different protocols.

Course Outcomes The Student will be able to		**Cognitive Level**
CO1	Demonstrate working knowledge of computer hardware & Operating Systems, software and networking skills.	U
CO2	Design and simulate simple networking models using the Network simulator modeling.	Ap
CO3	Compare and analyze the concepts of protocols, network interfaces and design LAN, MAN and WAN.	An
CO4	Troubleshoot and repair network problems demonstrating professionalism, team work and adaptability.	E
CO5	Develop and test network applications using socket programming.	C

LIST OF EXPERIMENTS :

1. Implementation of Stop and Wait Protocol and sliding window.
2. Implementation and study of Go back-N and selective repeat protocols.
3. Create scenario Transfer of files from PC to PC using Windows socket processing.
4. Analyze the performance of CSMA/CD protocol through simulation.
5. Evaluate the performance of network with CSMA / CA protocol and compare with CSMA/CD protocols.
6. Implementation of distance vector routing algorithm.
7. Implementation of Link state routing algorithm.
8. Data encryption and decryption using Data Encryption Standard algorithm.
9. Implement and realize the Network Topology - Star, Bus and Ring using NS2.
10. Implement and perform the operation of CSMA/CD and CSMA/CA using NS2.

TOTAL (P:60) = 60 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3												2	
2			3		3								3	
3			3										3	
4		3		2									3	
5									2				2	
CO (W.A)	**3**	**3**	**3**	**2**	**3**				**2**				**3**	

22MAN10R - COMMUNICATION AND QUANTITATIVE REASONING *(Common to All Branches)*				
	L	**T**	**P**	**C**
	1	**0**	**2**	**0**

PRE-REQUISITE : NIL

Course Objectives:	• To enhance the proficiency of the students in both spoken and written communication • To acquire skills required to solve quantitative aptitude problems

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in Continuous Assessment Test**
CO1	Converse and draft ideas clearly and persuasively in various contexts.	U	40%
CO2	Solve quantitative aptitude problems with confidence.	Ap	30%
CO3	Draw valid conclusions, identify patterns, and solve problems.	An	30%

UNIT I - LANGUAGE BOOSTERS	**(5+10)**
JAM - General Topic Presentation - Group Discussion - Mock Interview - E Mail Writing - Essay writing	
UNIT II – APTITUDE	**(5+10)**
Mensuration - Area, Shapes, Perimeter - Races and Games - Data Interpretation on Multiple Charts.	
UNIT III - REASONING	**(5+10)**
Venn diagram - Syllogism - Data Sufficiency - Cubes & Embedded Images.	
TOTAL (L:45) = 45 PERIODS	

REFERENCES:

1. Rizvi, M.Ashraf, "*Effective Technical Communication*" Tata McGraw-Hill Education, 2017.
2. Aggarwal R S. "*Quantitative Aptitude for Competitive Examinations",* S.Chand Publishing Company Ltd(s)., 2022.
3. Arun Sharma "*How to Prepare for Quantitative Aptitude for the CAT",* Tata McGraw – Hill Publishing, 2022.
4. Praveen R V., "*Quantitative Aptitude and Reasoning",* PHI Learning Pvt. Ltd., 2016.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1									2	3				
2		2		2										
3		2		2										
CO (W.A)		1		1					1	1				

22ECC15 - VLSI AND CHIP DESIGN				
	L	T	P	C
	3	0	0	3
PREREQUISITE : 22ECC05				

Course Objective:	• To understand the I-V and DC characteristics of MOS transistors and layout of CMOS Circuits by means of stick diagram • To study about the static and dynamic CMOS combinational and sequential circuits using different logic styles • To obtain knowledge about Interconnects, Floor planning, routing and Verilog HDL-modeling Concepts

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the basic knowledge of digital and analog electronics to analyze MOS transistor characteristics and design the layout of CMOS circuits.	Ap	20%
CO2	Apply the various combinational and sequential circuit concepts to realize different logic styles.	An	30%
CO3	Analyze the diverse static and dynamic combinational and sequential CMOS circuits using different logic styles	An	30%
CO4	Design system level physical design and implement the modeling concepts using modern software tools.	C	20%
CO5	Engage independently to deliver oral presentations on the applications of VLSI systems.	U	Internal Assessment

UNIT I - MOS TRANSISTORS AND FABRICATION	**(9)**
Basic MOS Transistors – Enhancement and Depletion Mode Transistor Action - Ideal I-V Characteristics of MOS Transistors - Non Ideal I-V Effects - DC transfer characteristics - CMOS Fabrication: n-well – p-well – twin tub - stick diagram and layout design rules.	
UNIT II - COMBINATIONAL CIRCUITS DESIGN	**(9)**
Circuit Families - Static CMOS - Pseudo NMOS Logic - Clocked CMOS Logic - Domino Logic - Cascode Voltage Switch Logic - Dynamic Logic - Pass transistor Logic -Transmission gate logic.	
UNIT III - SEQUENTIAL CIRCUITS DESIGN	**(9)**
Sequencing static circuits - Circuit design of latches and flip-flops - Conventional CMOS Latches and flip-flops: Pulsed latches - Resettable latches and flip-flops - enabled latches and flip flops - Incorporating Logic into latches - TSPC Latches and flip-flops - Sequencing dynamic circuits	
UNIT IV - VLSI SUBSYSTEMS DESIGN AND MEMORIES	**(9)**
Bit Adders - Ripple Carry Adder - Carry look-ahead adder - Subtractor -One/Zero detectors - Comparators- Shifters - 2's complement array multipliers - Wallace tree multiplier - Series multiplier -Series and Parallel division - SRAM and Dynamic RAM	
UNIT V- SYSTEM LEVEL PHYSICAL DESIGN AND MODELING CONCEPTS	**(9)**
Large Scale physical design - Interconnect delay modeling - cross talk - Interconnect scaling – Floor planning and routing – Power distribution and consumption - Low power design considerations - Overview of Verilog HDL-Modeling Concepts	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:

1. Neil H.E.Weste, David Harris "CMOS VLSI Design - A Circuits and Systems Perspective", Pearson Education, 4th Edition, 2015.
2. John P.Uyemura, "Introduction to VLSI Circuits and Systems", John Wiley & Sons, 2009.

REFERENCES:

1. Jan M. Rabaey, Anantha Chandrakasan, Borivoje Nikolic, "Digital Integrated Circuits A Design Perspective", Prentice Hallof India, 2nd Edition, 2012.
2. Eugene D.Fabricius," Introduction to VLSI Design", Tata McGraw Hill, 1st Edition, 1990.
3. Gary K. Yeap, "Practical Low Power Digital VLSI Design", Kluwer Academic Publishers, Boston, 1st Ediiton, 1998.
4. Neil H.E. Weste and Kamran Eshraghian, "Principles of CMOS VLSI Design: A System Perspective", Addison Wesley, New Delhi, 2nd Edition, 2009.
5. Charles H.Roth and Lizy Kurian John, "Digital System design using VHDL", John Wiley& Sons, 2nd Edition, 2013.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													2
2	3													
3		3											1	3
4			3										1	3
5									2	1				1
CO (W.A)	3	3	3						2	1			1	2

22ECC16 - EMBEDDED SYSTEMS AND IOT DESIGN				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To gain knowledge about PIC Microcontroller. • To understand the embedded systems and IoT.

	Course Outcomes The Student will be able to	Cognitive Level	Weightage of COs in End Semester Examination
CO1	Apply knowledge of 16-bit microcontroller with necessary Input/Output and Memory Operations to build an embedded processor.	Ap	20%
CO2	Analyze the combinational, sequential, and timing circuits in recognizing functional blocks of embedded systems and their working mechanisms	An	20%
CO3	Design simple programming modules in machine and higher-level programming language using simulators to develop logical skills and testing skills	Ap	40%
CO4	Select and implement appropriate IoT techniques to provide valid conclusions.	An	20%
CO5	Build simple Embedded Applications using Input and output devices with IoT and a controller.	U	Internal Assessment

UNIT I- PIC MICROCONTROLLER	**(9)**
PIC 16F877 Microcontroller Architecture - Memory organization -Interrupts Timer/Counter - Compare/Capture/PWM modules (CCP) - Master Synchronous Serial Port module (MSSP).	
UNIT II - EMBEDDED SYSTEMS	**(9)**
Embedded System Design Process – Model Train Controller – Instruction Set : Preliminaries – ARM Processor – CPU: Programming Input and Output – Supervisor Mode, Exceptions and Trap – Co-Processors – Memory System Mechanisms – CPU Performance.	
UNIT III - PROCESSES AND OPERATING SYSTEMS	**(9)**
Introduction – Multiple Tasks and Multiple Processes – Preemptive real time Operating systems – Priority based scheduling – Interprocess Communication Mechanisms– Design Example – Audio Player, Engine Control Unit and Video Accelerator.	
UNIT IV – INTERNET OF THINGS	**(9)**
Introduction – Physical Design – Logical Design – IoT Enabling Technologies – Domain Specific IoTs: Retail, Logistics, Industry, Health and Lifestyle – IoT and M2M – IoT System Management with NETCONF-YANG – IoT Platform Design Methodology: IoT Level Specification, Domain Model.	
UNIT V - IOT SYSTEM DESIGN	**(9)**
Basic building blocks of an IoT device – Raspberry Pi – Board – Linux on Raspberry Pi – Interfaces – Programming with Python – Case Studies: Home Automation, Smart Cities, Environment and Agriculture.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. John B Peatman, "Design with PIC Microcontrollers", Pearson Education Asia, 2013, Twenty third Impression
2. Marilyn Wolf, "Computers as Components – Principles of Embedded Computing System Design", Third Edition, Morgan Kaufmann, 2012
3. Arshdeep Bahga, Vijay Madisetti, "Internet – of- Things – A Hands on Approach", Universities Press, 2015.

REFERENCES:

1. Mayur Ramgir, Internet – of – Things, Architecture, Implementation and Security, First Edition, Pearson Education, 2020.
2. Lyla B.Das, Embedded Systems: An Integrated Approach, Pearson Education 2013.
3. Jane.W.S .Liu, Real – Time Systems, Pearson Education, 2003.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3												2
3			2		2									3
4				2										
5					2									
CO (W.A)	3	3	2	2	2									3

22ECP08 - VLSI DESIGN LABORATORY				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**

PREREQUISITE : 22ECC05

Course Objective:	• To design and simulate combinational logic and sequential logic circuits using Verilog HDL. • To implement the digital logic circuits using Xilinx FPGA. • To understand and design the CMOS logic circuits using Tanner software.

Course Outcomes The Student will be able to		**Cognitive Level**
CO1	Apply the knowledge of digital design and develop code for digital logic circuits using Hardware Description Language.	Ap
CO2	Simulate and Synthesize the place and route for digital logic circuits using ModelSim.	Ap
CO3	Analyze the digital modules in Xilinx FPGA kit.	An
CO4	Design and simulate the CMOS blocks using EDA tool.	Ev
CO5	Prepare an effective record for all the experiments.	U

LIST OF EXPERIMENTS :

1. Design an 8-bit Adder and 8-bit Subtractor and simulate using Xilinx software
2. Design an ALU and simulate using Xilinx software.
3. Simulation and Implementation of Encoder and Decoder using Xilinx.
4. Simulation and Implementation of 4 * 4 Multiplier using Xilinx.
5. Design T, JK and SR flipflops. Simulate and Implement using Xilinx.
6. Design and implementation of Shift registers using Xilinx.
7. Design 3-bit synchronous up/down counters. Simulate and implement using Xilinx.
8. Design 4-bit Asynchronous up/down counter. Simulate and implement using Xilinx.
9. Design and simulation of Frequency Dividers and implement using Xilinx.
10. Design and simulation of CMOS Inverter using Tanner software.
11. Design CMOS NAND and NOR Gates using PMOS and NMOS Transistors and simulate using Tanner software.
12. Design and simulation of CMOS Latch using Tanner software.

TOTAL (P:60) = 60 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3								2					
2					3				2					2
3					3									
4			3		3									3
5								3	2	3		2		
CO (W.A)			**3**		**3**			**3**	**2**	**3**		**2**		**2.5**

22ECP09 - EMBEDDED SYSTEMS AND IOT DESIGN LABORATORY				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To obtain a broad understanding of the emerging technologies in embedded system • To gain knowledge about I/O models.

The Student will be able to **Course Outcomes**		**Cognitive Level**
CO1	Apply the knowledge of PIC, ARM, IoT and Arduino using IDE platform.	Ap
CO2	Analyze the virtual circuits of digital devices using Proteus.	An
CO3	Design and synthesize a digital circuit for the given specifications and conduct the experiment.	Ap
CO4	Develop the high level programming knowledge using Keil and MPLAB.	An
CO5	Involve in independent / team learning, communicate effectively and engage in life long learning.	C

LIST OF EXPERIMENTS :

1. Program to interface Traffic Light Controller using PIC Microcontroller.
2. Program to control the external devices using GPIO ports of PIC16FXX Microcontroller.
3. Program to Develop an IoT Dashboard for Sensors on Android Phone.
4. Program to Develop an IoT Camera System using Android Phones.
5. Program to control the external devices using GPIO ports of ARM Processor.
6. Program to interface the ADC using ARM Processor.
7. Program to interface the DAC using ARM Processor.
8. Program to interface the keyboard using ARM Processor.
9. Program to Design a Heart Beat sensor using Arduino controller.
10. Program to Design a fire detecting system using Arduino controller.

TOTAL (P:60) = 60 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3													
2		3												2
3			3											2
4			3											
5									2	2		1		2
CO (W.A)	**3**	**3**	**3**						**2**	**2**		**1**		**2**

22GEA01- UNIVERSAL HUMAN VALUES *(Common To All Branches)*				
	L	**T**	**P**	**C**
	2	**0**	**0**	**2**
PREREQUISITE : NIL				

Course Objective:	• To help the students appreciate the essential complementarily between 'VALUES' and 'SKILLS' to ensure sustained happiness and prosperity. • To facilitate the development of a holistic perspective among students towards life and profession. • To highlight plausible implications of holistic understanding in terms of ethical human conduct. • To understand the nature and existence. • To understand human contact and holistic way of living

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Evaluate the significance of value inputs in formal education and start applying them in their life and profession.	E	Internal Assessment
CO2	Distinguish between values and skills, happiness and accumulation of physical facilities, the Self and the Body, Intention and Competence of an individual.	Ap	
CO3	Analyze the value of harmonious relationship based on trust and respect in their life and profession.	An	
CO4	Examine the role of a human being in ensuring harmony in society and nature.	Ap	
CO5	Apply the understanding of ethical conduct to formulate the strategy for ethicallife and profession.	Ap	

UNIT I - INTRODUCTION-BASIC HUMAN ASPIRATION, ITS FULFILLMENT THROUGH ALL- ENCOMPASSING RESOLUTION	**(6)**
The basic human aspirations and their fulfillment through Right understanding and Resolution, Right understanding and Resolution as the activities of the Self, Self being central to Human Existence; All-encompassing Resolution for a Human Being, its details and solution of problems in the light of Resolution	
UNIT II - RIGHT UNDERSTANDING (KNOWING)- KNOWER, KNOWN & THE PROCESS	**(6)**
The domain of right understanding starting from understanding the human being (the knower, the experiencer and the doer) and extending up to understanding nature/existence – its interconnectedness and co-existence; and finally understanding the role of human being in existence (human conduct).	
UNIT III - UNDERSTANDING HUMAN BEING	**(6)**
Understanding the human being comprehensively as the first step and the core theme of this course; human being as co-existence of the self and the body; the activities and potentialities of the self; Basis for harmony/contradiction in the self	
UNIT IV - UNDERSTANDING NATURE AND EXISTENCE	**(6)**
A comprehensive understanding (knowledge) about the existence, Nature being included; the need and process of inner evolution (through self-exploration, self- awareness and self-evaluation), particularly awakening to activities of the Self: Realization, Understanding and Contemplation in the Self (Realization of	

Co-Existence, Understanding of Harmony in Nature and Contemplation of Participation of Human in this harmony/ order leading to comprehensive knowledge about the existence).	
UNIT V - UNDERSTANDING HUMAN CONDUCT, ALL-ENCOMPASSING RESOLUTION AND HOLISTIC WAY OF LIVING	**(6)**
Understanding Human Conduct, different aspects of All-encompassing Resolution (understanding, wisdom, science etc.), Holistic way of living for Human Being with All- encompassing Resolution covering all four dimensions of human endeavor viz., realization, thought, behavior and work (participation in the larger order) leading to harmony at all levels from Self to Nature and entire Existence	
TOTAL (L:30) : 30 PERIODS	

TEXT BOOKS:

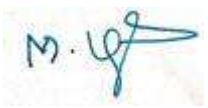
1. R R Gaur, R Asthana, G P Bagaria, 2019 (2nd Revised Edition), A Foundation Course in Human Values and Professional Ethics. ISBN 978-93-87034-47-1, Excel Books, New Delhi

REFERENCES:

1. Ivan Illich, 1974, Energy & Equity, The Trinity Press, Worcester, and Harper Collins,USA
2. E.F. Schumacher, 1973, Small is Beautiful: a study of economics as if people mattered, Blond & Briggs, Britain.
3. Sussan George, 1976, How the Other Half Dies, Penguin Press. Reprinted 1986, 1991
4. Donella H. Meadows, Dennis L. Meadows, Jorgen Randers, William W. Behrens III,1972, Limits to Growth – Club of Rome's report, Universe Books.
5. A Nagraj, 1998, Jeevan Vidya EkParichay, Divya Path Sansthan, Amarkantak.
6. P L Dhar, RR Gaur, 1990, Science and Humanism, Commonwealth Publishers.
7. A N Tripathy, 2003, Human Values, New Age International Publishers
8. E G Seebauer& Robert L. Berry, 2000, Fundamentals of Ethics for Scientists &Engineers, Oxford University Press
9. M Govindrajran, S Natrajan& V.S. Senthil Kumar, Engineering Ethics (including Human Values), Eastern Economy Edition, Prentice Hall of India Ltd.
10. Subhas Palekar, 2000, How to practice Natural Farming, Pracheen (Vaidik) Krishi Tantra Shodh, Amravati
11. B P Banerjee, 2005, Foundations of Ethics and Management, Excel Books
12. B L Bajpai, 2004, Indian Ethos and Modern Management, New Royal Book Co., Lucknow. Reprinted 2008.

Mapping of COs with POs / PSOs

	POs												PSOs	
COs	**1**	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1						2	2	3	2	2		3		
2						2	2	3	2	2		3		
3						2	2	3	2	2		3		
4						2	2	3	2	2		3		
5						2	2	3	2	2		3		
CO (W.A)						**2**	**2**	**3**	**2**	**2**		**3**		



22GED02- INTERNSHIP/INDUSTRIAL TRAINING					
		L	T	P	C
		0	0	4	2
PREREQUISITE : NIL					
Course Objective:	• To obtain a broad understanding of the emerging technologies in Industry • To gain knowledge about I/O models.				
Course Outcomes The Student will be able to		**Cognitive Level**			
CO1	Engage in Industrial activity which is a community service.	U			
CO2	Prepare the project report, three minute video and the poster of the work.	Ap			
CO3	Identify and specify an engineering product that can make their life comfortable.	An			
CO4	Prepare a business plan for a commercial venture of the proposed product, together with complying to relevant norms.	Ap			
CO5	Identify the community that shall benefit from the product.	E			

During semester break, students are encouraged to engage in industrial training or undergo internship in an industry related to the field of study. The duration of the activity shall be of 4 to 6 weeks. The work carried out in the semester break is assessed through an oral seminar accompanied by a written report. It is expected that this association will motivate the student to develop simple Electronic (or other) products to make their life comfortable and convert new ideas into projects.

Every student is required to complete 12 to 16 weeks of internship (with about 40 hours per week), during the Summer/Winter semester breaks. The Internships are evaluated through Internship Reports and Seminars during the VI and VIII semesters. The internships can be taken up in an industry, government organization, a research organization or an academic institution, either in the country or outside the country, that include activities like:

- Successful completion of Internships/ Value Added Programs/Training
- Programs/ workshops organized by academic Institutions and Industries
- Soft skill training by the Placement Cell of the college
- Active association with incubation/ innovation /entrepreneurship cell of the institute
- Participation in Inter-Institute innovation related competitions like Hackathons
- Working for consultancy/ research project within the institutes
- Participation in activities of Institute's Innovation Council, IPR cell, Leadership Talks, Idea/ Design/ Innovation contests
- Internship with industry/ NGO's/ Government organizations/ Micro/ Small/ Medium enterprises
- Development of a new product/ business plan/ registration of a start-up

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1						2								
2										3				
3		1												
4							2	3			2			
5						2								
CO (W.A)		**1**				**2**	**2**	**3**		**3**	**2**			

22ECD01- PROJECT WORK - I					
		L	T	P	C
		3	0	0	3
PREREQUISITE : NIL					
Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**		
CO1	Engage in independent study to research literature in the identified area and consolidate the literature search to identify and formulate the engineering problem.	Ap	10 % - First Review (Internal)		
CO2	Prepare the Gantt Chart for scheduling the project , engage in budget analysis, and designate responsibility for every member in the team and identify the community that shall benefit through the solution to the identified research work and also demonstrate concern for environment	Ap, E	15 % - Second Review (Internal)		
CO3	Identify, apply the mathematical concepts, science concepts, and engineering concepts necessary to implement the identified engineering problem, select the engineering tools /components required to reproduce the identified project.	Ap, An, C	15 % - Third Review (Internal)		
CO4	Engage in effective written communication through the project report, the one-page poster presentation, and effective oral communication through presentation of the project work and demonstration of the project.	E	30 % - Final Review (External)		
CO5	Perform in the team, contribute to the team and mentor/lead the team, demonstrate compliance to the prescribed standards/ safety norms and abide by the norms of professional ethics and clearly specify the outcome of the project work	Ap, An	30 % - Final Review (External)		

DESCRIPTION
Project work may be allotted to a single student or to a group of students not exceeding 4 per group. The title of project work is approved by head of the department under the guidance of a faculty member and student(s) shall prepare a comprehensive project report after completing the work to the satisfaction of the guide. The Head of the department shall constitute a review committee for project work. There shall be three internal reviews and one external review during the semester by the committee to review the progress. Student(s) shall make presentation on the progress made by him / her / them before the committee and evaluation is done as per Rules and Regulations
TOTAL (P: 120) = 120 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1		3										3	3	3
2						3	3				3		3	3
3	3	3	3	3	3								3	3
4								3		3			3	3
5									3		3	3	3	3
CO (W.A)	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**

22ECD02- PROJECT WORK - II					
		L	T	P	C
		3	0	0	3
PREREQUISITE : NIL					

Course Outcomes The Student will be able to		Cognitive Level	Weightage of COs in End Semester Examination
CO1	Design, implement, analyze and interpret results of the implemented project and improvise the performance of the project.	Ap, An, C	10 % - First Review (Internal)
CO2	Preparation of the four page IEEE format of the work, presentation of the project work and demonstration of the project in Project Expo, Presentation in International/ National Conferences, Conversion of project to start-up/ product/ research paper/ patent.	Ap, An, E	15 % - Second Review (Internal)
CO3	Design, implement, analyze and interpret results of the implemented project and improvise the performance of the project.	Ap, An, C	15 % - Third Review (Internal)
CO4	Engage in effective written communication through the project report, the one-page poster presentation, and preparation of the video about the project and effective oral communication through presentation of the project work and demonstration of the project.	E	30 % - Final Review (External)
CO5	Perform in the team, contribute to the team and mentor/lead the team, demonstrate compliance to the prescribed standards/ safety norms and abide by the norms of professional ethics and clearly specify the outcome of the project work.	Ap, An	30 % - Final Review (External)

DESCRIPTION
Project work may be allotted to a single student or to a group of students not exceeding 4 per group. The title of project work is approved by head of the department under the guidance of a faculty member and student(s) shall prepare a comprehensive project report after completing the work to the satisfaction of the guide. The Head of the department shall constitute a review committee for project work. There shall be three internal reviews and one external review during the semester by the committee to review the progress. Student(s) shall make presentation on the progress made by him / her / them before the committee and evaluation is done as per Rules and Regulations
TOTAL (P:300) = 300 PERIODS

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1		3										3	3	3
2						3	3				3		3	3
3	3	3	3	3	3								3	3
4								3		3			3	3
5									3		3	3	3	3
CO (W.A)	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**	**3**

VERTICAL 1: SEMICONDUCTORS

22ECX01 - ASIC DESIGN				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				

Course Objective:	• To understand ASICs, CMOS Logic, ASIC Library and Programmable ASICs. • To identify, apply and design a system using different VLSI design methodologies such as Full custom and Semi-custom approaches. • To apply industry standard CAD tools for designing VLSI systems.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the knowledge of VLSI to design digital integrated circuits.	Ap	20%
CO2	Ability to identify, apply and design a system using different VLSI design methodologies such as Full custom and Semi-custom approaches.	An	30%
CO3	Ability to apply industry standard CAD tools for designing VLSI systems.	An	30%
CO4	Ability to analyze and investigate the performance of VLSI systems.	E	20%
CO5	Understand ASICs, CMOS Logic, ASIC Library and Programmable ASICs.	U	Internal Assessment

UNIT I - Introduction to ASICs, CMOS Logic, ASIC Library Design, Programmable ASICs	**(9)**
Types of ASICs - Design flow – CMOS transistors- Transistor as resistors - Transistor parasitic capacitance – Logical effort-Antifuse - Static RAM - EPROM and EEPROM technology.	
UNIT II - Programmable ASICs, logic cells and I/O Cells	**(9)**
Actel ACT: Multiplexer Logic,ACT2 and ACT3 Logic Modules, timing model, critical path, speed grading, worst case timing, Actel logic module analysis, Xilinx LCA:XC3000CLB, XC4000, XC5200, Xilinx CLB, DC & AC inputs and outputs – Clock & power inputs.	
UNIT III - Programmable Interconnects and Logic Synthesis	**(9)**
Actel ACT – Xilinx LCA – Design synthesis: Xilinx, Actel, Altera, logic synthesis, Combinational logic, multiplexers, Case statement, decoders, arithmetic and Sequential logic.	
UNIT IV - Partitioning, Floor planning and Placement	**(9)**
Physical design flow -System partitioning - FPGA partitioning: KL algorithm –Floor planning –Placement: Constructive and iterative placement algorithms.	
UNIT V- Routing	**(9)**
Global routing - Detailed routing –Area routing-Maze Algorithm-Channel routing- Left Edge Algorithm-Special routing.	
TOTAL (L:45) =45 PERIODS	

TEXT BOOKS:

1. 1. Smith M.J.S, "Application Specific Integrated Circuits", 12th Edition, Pearson Education Pvt. Ltd, New Delhi, 2013.

REFERENCES:

1. Wayne Wolf, "FPGA-Based System Design", 1st Edition, PHI, New Delhi, 2009.
2. Erik larson, "Introduction to Advanced System-on-Chip Test Design and Optimization", 1st Edition, Springer, USA, 2005.
3. Farzad Nekoogar and Faranak Nekoogar, From ASICs to SOCs: A Practical Approach, Prentice Hall PTR, 2003
4. R. Rajsuman, System-on-a-Chip Design and Test. Santa Clara, CA: Artech House Publishers, 2000.
5. F. Nekoogar. Timing Verification of Application-Specific Integrated Circuits (ASICs).Prentice Hall PTR, 1999.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													3
2	3													3
3		2	2											3
4			2											3
5												1		3
CO (W.A)	3	2	2									1		3

22ECX02– SYSTEM ON CHIP DESIGN				
	L	T	P	C
	3	0	0	3
PREREQUISITE :NIL				

Course Objective:	• To understand the system architectures and components in system design. • To study about system level design and co design concepts. • To get awareness about the implementation of SoC and its Testing.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply SoC testing techniques.	Ap	20%
CO2	Discern system level interconnection and co-design concepts.	An	30%
CO3	Compare system level design and interconnection.	An	30%
CO4	Illustrate the co-design concepts.	E	20%
CO5	Understand system architectures and components in system design.	U	Internal Assessment

UNIT I - SYSTEM ARCHITECTURE	**(9)**
Introduction to system Architecture, Components of a system, Hardware and Software: Programmability Versus Performance, Processor Architectures, Memory and Addressing, System-Level Interconnection, An Approach for SOC Design, System Architecture and Complexity	
UNIT II - SYSTEM-LEVEL DESIGN	**(9)**
Processor selection-Concepts in Processor Architecture: Instruction set architecture (ISA), elements in Instruction Handing-Robust processors: Vector processor, VLIW, Superscalar, CISC, RISC—Processor evolution: Soft and Firm processors, Custom-Designed processors-IP based design - on - chip memory.	
UNIT III - SYSTEM-LEVEL INTERCONNECTION	**(9)**
Overview: Interconnect Architecture, On-chip Buses: basic architecture, Bus standards: AMBA, Core Connect, Wishbone, Avalon-Network-on-chip – Architecture – topologies - switching strategies - routing algorithms - flow control, quality-of-service - Reconfigurability in communication architectures	
UNIT IV - CO-DESIGN CONCEPTS	**(9)**
Nature of hardware & software- quest for energy efficiency- driving factors for hardware-software co-design- Co-design space-Dualism of Hardware design and Software design - Modeling Abstraction Level-Concurrency and Parallelism- Hardware Software tradeoffs- Introducing Dataflow modeling.	
UNIT V- SOC IMPLEMENTATION AND TESTING	**(9)**
Study of Microblaze RISC processor - Real-time operating system (RTOS), peripheral interface and components, High-density FPGAs-Introduction to tools used for SOC design: Xilinx SOC based development kit. Manufacturing test of SOC: Core layer, system layer, application layer-P1500 Wrapper Standardization- SOC Test Automation (STAT).	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. Michael J.Flynn, Wayne Luk, "Computer system Design: System-on-Chip", Wiley- India, 2012.

REFERENCES:

1. Patrick Schaumont "A Practical Introduction to Hardware/Software Co-design", 2nd Edition, Springer, 2012.
2. Lin, Y-L.S. (ed.), "Essential issues in SOC design: designing complex systems-on- chip", Springer, 2006
3. Sudeep Pasricha, NikilDutt, "On Chip Communication Architectures: System on Chip Interconnect", Morghan Kaufmann Publishers, 2008
4. W.H.Wolf, "Computers as Components: Principles of Embedded Computing System Design", Elsevier, 2008.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													3
2	3													3
3		3	3											3
4				1	3									3
5												2		3
CO (W.A)	3	3	2	1	3							2		3

22ECX03 –SYSTEM VERILOG				
	L	T	P	C
	3	0	0	3
PREREQUISITE : 22ECC05				

Course Objective:	• To apply the fundamentals of digital electronics and through programming the designs. • To apply object oriented programming concepts for VLSI designs. • To create the test benches to analysis the designs • To implement the advanced design using modern tools. • To enhance their design skill through lifelong learning.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the fundamentals of digital electronics and through programming the designs.	Ap	25%
CO2	Apply object oriented programming concepts for VLSI designs.	Ap	25%
CO3	Create the test benches to analysis the designs.	C	30%
CO4	Implement the advanced design using modern tools.	D	20%
CO5	Enhance their design skill through lifelong learning.	U	Internal Assessment

UNIT I - VERIFICATION GUIDELINES	**(9)**
Introduction, The Verification Process, The Verification Plan, The Verification Methodology, Manual, Basic Testbench Functionality, Directed Testing, Methodology Basics, Constrained-Random Stimulus, Functional Coverage, Testbench Components, Layered Testbench, Building a Layered Testbench, Simulation Environment Phases, Maximum Code Reuse, Testbench Performance.	
UNIT II - DATA TYPES	**(9)**
Introduction to data types, Built-in Data Types, Fixed-Size Arrays, Dynamic Arrays, Queues, Associative Arrays, Linked Lists, Array Methods, choosing a Storage Type, Creating New Types with typedef, Creating User-Defined Structures, Enumerated Types, Constants, Strings, Expression Width, Net Types. Array manipulation methods, Array querying functions, Queue.	
UNIT III - PROCEDURAL STATEMENTS AND ROUTINES	**(9)**
Introduction, Procedural Statements, Tasks, Functions, and Void Functions, Task and Function Overview, Routine Arguments, Returning from a Routine, Local Data Storage, Time Values. Process and process control.	
UNIT IV - BASIC OOPS	**(9)**
Introduction, Think of Nouns, not Verbs, Your First Class, Where to Define a Class, OOP Terminology, Creating New Objects, Object Deallocation, Using Objects, Static Variables vs. Global Variables, Class Routines, Defining Routines Outside of the Class, Scoping Rules, Using One Class Inside Another, Understanding Dynamic Objects, Copying Objects, Public vs. Private Straying Off Course, Building a Testbench.	
UNIT V- CONNECTING THE TEST BENCH AND DESIGN	**(9)**
introduction, Separating the Testbench and Design, The Interface Construct, virtual interface, Stimulus Timing, Interface Driving and Sampling, Connecting It All Together, Top-Level Scope, Program – Module Interactions, System Verilog Assertions, Call back. The Four-Port ATM Router. Modport and Clocking block. Mailbox.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:
1. Chris Spear, Greg Tumbush, "System Verilog for Verification: A Guide to Learning the Test bench Language Features", 3rd Edition, Springer, US, 2012.
REFERENCES:
1. Stuart Sutherland, Simon Davidmann, "System Verilog for design: a guide to using System Verilog for hardware design and modeling", Springer, 2004. 2. Palnitkar Samir, "Verilog HDL: Guide to Digital Design and synthesis", 2nd Edition, Pearson Education, New Delhi, 2017.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													3
2	3													3
3		2	3											3
4					2									3
5												2		3
CO (W.A)	**3**	**2**	**3**		**2**							**2**		**3**

22ECX04 – VLSI TESTING AND TESTABILITY				
	L	T	P	C
	3	0	0	3

PREREQUISITE : 22ECC05

Course Objective:	• To apply the various techniques to diagnosis fault in digital circuit. • To analysis the faults presence and investigate system level faults. • To design testable digital circuit by testability techniques. • To design the self checking systems. • To develop new fault diagnosing algorithms through lifelong learning.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the various techniques to diagnosis fault in digital circuit.	Ap	25%
CO2	Analysis the faults presence and investigate system level faults.	Ap	25%
CO3	Design testable digital circuit by testability techniques.	C	30%
CO4	Design the self checking systems.	E	20%
CO5	Develop new fault diagnosing algorithms through lifelong learning.	E	Internal Assessment

UNIT I - FAULT MODELLING AND SIMULATION	**(9)**
Introduction to Testing - Faults in digital circuits - Modeling of faults - Logical Fault Models - Fault detection- Fault location - Fault dominance – Single stuck fault model and multiple stuck fault model - Logic Simulation- Types of simulation - Delay models - Gate level Event-driven simulation- Fault Simulation Techniques Serial , Parallel and Deductive	
UNIT II - TESTING FOR SINGLE STUCK AT FAULTS	**(9)**
Test Generation algorithms for combinational circuits – Fault oriented ATG – D Algorithm-Examples – PODEM - Fault independent ATG - Random Test generation - ATGs for SSFs in sequential circuits – TG using iterative array models- Random Test Generation.	
UNIT III - DELAY TEST	**(9)**
Delay test problem – Path delay test – Test generation for Combinational circuits, Number of paths in a circuit Transition fault – Delay test methodologies-Slow clock combinational test, Enhanced scan test, normal scan sequential test, Variable- clock Non-scan sequential test, Rated- clock Non-scan sequential test.	
UNIT IV- DESIGN FOR TESTABILITY	**(9)**
Testability- Controllability and observability, Ad-hoc design for testability Techniques – Controllability and observability by means of scan registers- Storage cells for scan design- Level sensitive scan design (LSSD)- Partial scan using I-Paths – Boundary scan standards.	
UNIT V-FAULT DIAGNOSIS	**(9)**
Logical Level Diagnosis – Diagnosis by UUT reduction – Fault Diagnosis for Combinational Circuits – Self checking design – System Level Diagnosis.	
	TOTAL (L:45) = 45 PERIODS

TEXT BOOKS:

1. Parag K. Lala "An Introduction to Logic Circuit Testing" Springer International Publishing, 2022.
2. Abramovici, M., Brever, A., and Friedman, D., "Digital Systems Testing and Testable Design", Jaico Publishing House, 2002.

REFERENCES:

1. Michael L Bushnell and Vishwani D Agarwal, "Essentials of Electronic Testing for Digital, Memory and Mixed Signal Circuits", Springer, verlag 2000.
2. Parag K Lala, "Fault Tolerant and Fault Testable Hardware Design" BS Publications, 2002
3. Sebastian Huhn, Rolf Drechsler "Design for Testability, Debug and Reliability", Springer International Publishing, 2021.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													3
2		3												3
3			3											3
4			3											3
5												2		3
CO (W.A)	**3**	**3**	**3**									**2**		**3**

22ECX05 – ELECTRONIC SYSTEM DESIGN				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**
PREREQUISITE : 22ECC04				
Course Objective:	• To apply design rules for PCB designing of circuits. • To perform various analysis on the designed circuits. • To design the layouts of PCB including R, L, C spacing and spacing requirements. • To design the PCB using different PCB technology. • To Utilize the SPICE tool to design and analysis the electronic circuits.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply design rules for PCB designing of circuits.	Ap	20%
CO2	Perform various analyses on the designed circuits.	An	20%
CO3	Design the layouts of PCB including R, L, C, spacing and spacing requirements.	D	20%
CO4	Design the PCB using different PCB technology.	D	20%
CO5	Utilize the SPICE tool to design and analysis the electronic circuits.	Ap	20%

UNIT I – BASIC ANALYSIS OF CIRCUITS	**(9)**
Introduction to Or CAD capture – DC bias point analysis – DC analysis- AC analysis – Stimulus Editor – Transient Analysis –Convergence problems and Error Messages - Transformers.	
UNIT II – ADVANCED ANALYSIS OF CIRCUITS	**(9)**
Monte Carlo analysis – Worst case analysis – Performance analysis – Noise Analysis – Temperature analysis – Transmission lines – Digital simulation – Mixed simulation.	
UNIT III - PRINTED CIRCUIT BOARD	**(9)**
Layout planning: General considerations - PCB sizes - Layout approaches - Layout, General rules and parameters: Resistance, capacitance, inductance, conductor spacing, cooling requirements and package density, layout check.	
UNIT IV- DESIGN RULES FOR DIGITAL & ANALOG CIRCUIT PCB's	**(9)**
Digital circuit PCB: Introduction – Reflection - Cross talk - Around and supply line noise - Electromagnetic interference from pulse type EM fields. Analog circuit PCB: Component placing - Signal conductor - Supply and ground conductors.	
UNIT V-PCB TECHNOLOGY TRENDS	**(9)**
Introduction - Fine line conductors with ultra-thin copper foil - Multilayer board - Multi wire board - Subtractive additive process - Semi additive process - Additive process - Flexible PCB - Metal core circuit boards - Mechanical milling of PCB.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. Dennis Fitzpatrick "Analog Design and Simulation using OrCAD Capture and PSpice" Elsevier Science Publication, 2017
2. Reinhold Luduig and PavelBretchko, "RF Circuit Design – Theory and Applications", Pearson Education, USA Second Edition, 2012.
3. Walter C.Bosshart, "Printed circuit Boards – Design and Technology", Tata McGraw-Hill, New Delhi, Second Edition, 2012.
4. Douglas Brooks, Johannes Adam "PCB Design Guide to Via and Trace Currents and Temperatures" Artech House, 2021

REFERENCES:

1. Keith H.Billings, "Handbook of Switched Mode Power Supplies" McGraw-Hill Publishing Co., New Delhi, Third Edition 2011.
2. Michael Jacob, "Applications and Design with Analog Integrated Circuits", PHI, New Delhi, Second Edition, 1999.
3. F.H.Mitchell, "Introduction to Electronic Design", Prentice Hall of India, New Delhi, Second Edition, 1992.
4. Sydney Soclof, "Design of Applications of Analog Integrated Circuits", Prentice Hall of India, New Delhi, Second Edition 1997.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	3
2		3											2	3
3			3										2	
4			3										2	
5					3									
CO (W.A)	**3**	**3**	**3**		**3**								**2**	**3**

22ECX06 - ELECTRONIC CIRCUIT BOARD DESIGN				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				

Course Objective:	• To know the basics of electro-magnetic components • To acquire knowledge in basics of PCB and partitioning and traces • To expertise in basics of electrical parameters • To illustrate the methods and effects in PCB design

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the concepts on fundamental components of electro magnetics to the solution of PCB designing and router topology	Ap	20%
CO2	Apply techniques to minimize crosstalk and ensure reliable circuit operation.	Ap	30%
CO3	Analyze the combined effects of parallel capacitors on circuit performance.	An	30%
CO4	Design and implement grounded heat sinks effectively in PCB layouts.	E	20%
CO5	Communicate effectively as an individual and as a part of team during oral presentations	U	Internal Assessment

UNIT I - FUNDAMENTALS	**(9)**
Electromagnetic Compatibility, Electromagnetic Interference, Radio Frequency (RF). Immunity-types-Elements of the electromagnetic environment-Nature of interference-EMC analysis-Standards-Classification of ITE Products-Immunity requirements -Printed circuit board basics-Hidden RF characteristics of passive components	
UNIT II - ROUTING TOPOLOGY CONFIGURATIONS	**(9)**
Microstrip, stripline, Layer stackup assignment, Single-sided assembly, Double-sided assembly, Four-layer stackup, Six-layer stackup, Eight-layer stackup, Radial migration, Common-mode and differential-mode currents, RF current density distribution, Grounding methodologies, Single-point grounding-Multipoint grounding, Ground and signal loops, Functional partitioning	
UNIT III - BYPASSING AND DECOUPLING	**(9)**
Review of resonance- Series resonance, Parallel resonance, Parallel C-Series RL resonance -Physical Characteristics-Impedance, Capacitor types, Energy storage, Resonance, Capacitors in parallel, Power and ground planes, Selecting a capacitor, Power and ground planes-Calculating power and ground plane capacitance, Combined effects of planar and Discrete capacitors	
UNIT IV - CLOCK CIRCUITS, TRACE ROUTING, AND TERMINATIONS	**(9)**
Topology configurations, Component placement- Calculating trace lengths (electrically long traces), Trace routing, Routing layers, Crosstalk, Trace separation, Partitioning, Isolation and partitioning (moating), Filtering and grounding, Local Area Network I/O layout, Electrostatic discharge protection, Design techniques for ESD protection, Guard band implementation	

UNIT V - DESIGN TECHNIQUES	(9)
Localized decoupling, Capacitor implementation, 20-H rule, Trace routing for corners, selecting ferrite components, Grounded heatsinks, Lithium battery circuits, BNC connectors, Creepage and clearance distances current, Carrying capacity of copper traces, Film, Footprint Design for High-Speed Boards-Component Footprint Shapes, Pad Shapes for High-Speed PCB Design, Best Routing Practices for High Speed Routing	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. Mark I. Montrose and Edward M. Nakauchi. "Printed Circuit Board Design Techniques for EMC Compliance", 2nd Edition 2004.

REFERENCES:

1. Amit Bahl "High-Speed PCB Design Guide" Sierra Circuits Inc 2020.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3												3	
3		3											3	
4			3											3
5									3	2				3
CO (W.A)	3	3	3	3					2	2			3	3

22ECX07 - SEMICONDUCTOR DEVICE MODELLING AND SIMULATION				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To know the basics of Si based nano electronics and devices • To acquire knowledge in fundamentals of density of states • Apply principles of metal-semiconductor contacts to design • To understand the semi-classical transport theory and various simulation tools.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the concept of nanoscale devices to model nanoscale devices	Ap	20%
CO2	Analyze the characteristics of semiconductor devices and BJT for design for engineering problem	An	30%
CO3	Apply quantum mechanical models relevant to modern semiconductor devices and technologies.	Ap	30%
CO4	Design and use modern tools to provide solutions in semi-classical transport theory	E	20%
CO5	Communicate effectively about modern semiconductor devices as individual and team	U	Internal Assessment

UNIT I - SI BASED NANOELECTRONICS	**(9)**
Si-Based Nanoelectronics and Device Scaling, Beyond Conventional Silicon-Nanoscale and Hetero-structure Devices, Modeling of Nanoscale Devices, Crystal structure - Classification of Crystals-Miller Indices, Doping, Band Structure, Effective Mass - density of states.	
UNIT II - SEMICONDUCTOR THEORY	**(9)**
Diode - Electron Mobility, Semiconductor Statistics- Fermi - Dirac function and carrier concentration calculation, PN junction under equilibrium, I-V Characteristics-derivation of I-V relation, Minority carrier diffusion equation, Zener diode characteristics, Breakdown - Applications of Zener diode.	
UNIT III - BIPOLAR JUNCTION TRANSISTOR	**(9)**
Transistor configuration-Ebers-Moll model, Non-idealities in BJT, Gummel Poon Model, HBT, BJT Transient and small signal behaviour, Metal-Semiconductor contact (Schottky Barrier/Diode, Ohmic Contacts) and capacitance characteristics, Thermionic emission current flow and fermi-level pinning, Field Effect Transistors (JFET, MESFET, HEMT), MOS Band diagram and C-V characteristics, Threshold voltage and Interface charges, MOSFET I-V characteristics.	
UNIT IV - SEMICLASSICAL TRANSPORT THEORY	**(9)**
Distribution Function, Boltzmann Transport Equation (BTE), Relaxation-Time Approximation (RTA), Drift-Diffusion Model Derivation - Normalization and Scaling Linearization of Poisson's Equation- Scharfetter - Gummel Discretization of the Continuity Equation Newton's Method	

UNIT V - QUANTUM TRANSPORT MODELS	(9)
Tunnelling, Stationary states for a free particle, Potential step, Tunnelling through a single barrier. Transfer matrix approach - Basic description of the method - Piecewise constant potential barrier tool-Quantum mechanical corrections to standard approach, simulation tools, Models for DD, Hydrodynamic simulations, Mobility and G-R models.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. D Vasileska, SM. Goodnick, G Klimeck, "Computational Electronics: Semi-classical and Quantum Device Modeling and Simulation," CRC Press 2010.
2. G. Streetman, and S. K. Banerjee, "Solid State Electronic Devices," 7th edition, Pearson, 2014.

REFERENCES:

1. S.Salivahanan, N. Suresh kumar and A. Vallavanraj, —Electronic Devices and Circuitsll, Tata McGraw Hill Third Edition (2013).
2. D Vasileska, SM. Goodnick, G Klimeck, "Computational Electronics: Semi-classical and Quantum Device Modeling and Simulation", CRC Press ,2017
3. https://onlinecourses.nptel.ac.in/noc23_ee35/preview

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3											3	
3	3												3	
4			3		3									3
5									3	2				
CO (W.A)	3	3	3		3				3	2			3	3

22ECX08 - ELECTRONIC SYSTEM PACKAGING				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To know the concepts of IC Packaging system and system level PWP technologies • To gain comprehensive knowledge about failure mechanism and thermal management • To expertise in various types of packaging techniques • To illustrate the methods and techniques of packaging material and processes

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply design principles for reliability, thermal management and electronic cooling methods	Ap	20%
CO2	Apply knowledge to conduct life-cycle assessments to ensure performance of microsystems	Ap	30%
CO3	Analyze the properties and characteristics of packaging systems relevant to microsystems.	An	30%
CO4	Utilize CAD tools for PWB design and understand the limitations and processes involved in standard and micro via board assembly.	E	20%
CO5	Communicate effectively about electronic packaging systems as an individual and team	U	Internal Assessment

UNIT I - INTRODUCTION TO PACKAGING SYSTEM	**(9)**
Introduction to Microsystems - microsystem technologies-microsystem packaging, Importance of micropackaging - System level microsystem technologies - Future trends-Role of packaging in microelectronics - Microelectronic devices - Semiconductor road map-IC packaging challenges	
UNIT II - FAILURE MECHANISM AND THERMAL MANAGEMENT	**(9)**
Microsystems failure and failure mechanism - Fundamentals of design for reliability - Thermo Mechanically-Induced Failures – Electrically Induced Failures – Chemically Induced Failures - Future trends - Thermal Management - Cooling Requirements for Microsystems - Thermal Management Fundamentals - Electronic Cooling Methods	
UNIT III - SINGLE CHIP AND MULTICHIP PACKAGING	**(9)**
Functions of Single Chip Packages, Types of Single Chip Packages, Fundamentals of Single Chip Packaging Materials, Processes, and Properties - Characteristics of Single Chip Packages Multichip Module Functionality - Multichip Module Advantages- Multichip Modules at the System Level - Types of Multichip Module Substrates –Multichip Module Design –Multichip Module Technology Comparisons	
UNIT IV - SYSTEM LEVEL PWB TECHNOLOGIES	**(9)**
System Level Printed Wiring Board - Types of Printed Wiring Boards -Anatomy of a Printed Wiring Board -Fundamentals of Printed Wiring Boards - CAD Tools for Printed Wiring Board Design-Printed Wiring Board Materials - Standard Printed Wiring Board Fabrication - Limitations in Standard Printed Wiring Board - Process – Micro via Boards - Fundamentals of board assembly - Surface Mount Technology-Through - Hole Assembly.	

UNIT V - PACKAGING MATERIALS AND PROCESSES	(9)
The Role of Materials in Microsystems Packaging - Packaging Materials and Properties - Materials Processes - Future Trends-Electrical Testing- Anatomy of System - Level Electrical Testing - Fundamentals of Electrical Tests - Interconnection Tests -Active Circuit Testing - Design for Testability- Life - Cycle Assessment	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. Rao R. Tummala, Fundamentals of Microsystems Packaging, The McGraw-Hill (2001)

REFERENCES:

1. The Electronic Packaging Handbook Ed. Blackwell, G.R.Boca Raton: CRC Press LLC, 2000

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3												3	
3		3											3	
4			3		3									3
5									3	2		1		
CO (W.A)	3	3	3		3				3	2		1	3	3

VERTICAL 2: COMMUNICATION

22ECX11 - MOBILE COMMUNICATION				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**
PREREQUISITE : NIL				
Course Objective:	• To understand the mobile radio communication principles and the recent trends adopted in cellular systems and investigate different radio propagation models. • To explore the concept of Equalizers and Diversity techniques. • To analyze the different multiple access concepts in wireless communication and design the modern wireless networks.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the knowledge of communication techniques to understand the different cellular technology and solve problems.	Ap	30%
CO2	Analyze the given parameters for different propagation models of wireless networks.	An	30%
CO3	Analyze the architecture of software radio and develop architecture according to the needs.	An	20%
CO4	Compare the performance of Equalizers and diversity techniques and design components to adapt modern wireless networks.	An	20%
CO5	Perform in a team to prepare a report and make an effective oral presentation of the study on topics related to Networks protocols, contribution of cellular systems to the society and its effect on environment.	U	Internal Assessment

UNIT I - CELLULAR CONCEPT	**(9)**
Introduction to wireless communication systems - Modern wireless communication systems: 2G/3G/4G cellular networks - Cellular concept: Frequency reuse - channel assignment - hand off -interference & system capacity – trunking & grade of service - Coverage and capacity improvement - Basics of 5G technology: requirements.	
UNIT II - MOBILE RADIO PROPAGATION	**(9)**
Free space propagation model - Three basic propagation mechanisms: Reflection - Two-Ray model - Diffraction - Knife-edge diffraction model - Scattering - Log-normal shadowing - Okumara model - Hata model - Log-distance path loss model - Small-scale multipath propagation - Parameters of mobile multipath channels - Types of small scale fading - Rayleigh and Rician distributions.	
UNIT III - MULTIPLE ACCESS SCHEMES AND DIVERSITY	**(9)**
FDMA, TDMA, CDMA, SDMA and CSMA, OFDMA. Diversity Techniques – Frequency diversity, Time diversity, Code diversity, Antenna diversity –RAKE Receiver - SIMO, MISO, MIMO, MIMO-OFDM Technique.	

UNIT IV - CAPACITY OF WIRELESS CHANNELS	(9)
AWGN channel capacity – capacity of flat fading channels , Frequency- selective fading channels, Multiuser capacity, Downlink channel capacity, Uplink channel capacity, Outage capacity.	
UNIT V - MODERN WIRELESS NETWORKS	**(9)**
IEEE 802.11a/b/g/n/ac wireless local area networks - 60 GHz millimeter wave gigabit wireless networks - Vehicular wireless networks - Wireless protocols for Internet of Things including Bluetooth, BLE, 802.15.4, Zigbee, LoRA and SigFox.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. Rappaport S. Theodore, "Wireless Communications", Pearson Education, 2nd Edition, 2010.
2. Erik Dahlman, Stefan Parkvall and Johan Skold, "4G, LTE-Advanced Pro and The Road to 5G", Elsevier, 3rd Edition, 2016. Rao R. Tummala, Fundamentals of Microsystems Packaging, The McGraw-Hill (2001)

REFERENCES:

1. W.C.Y.Lee, "Mobile Communications Engineering: Theory and applications", McGraw-Hill International, 2nd Edition, 2009.
2. Martin Sauter, "From GSM to LTE–Advanced Pro and 5G: An Introduction to Mobile Networks and Mobile Broadband", Wiley-Blackwell, 2016".
3. Erik Dahlman, Stefan Parkvall and Johan Skold, "5G NR: The Next Generation Wireless Access Technology", Elsevier, 1st Edition, 2018

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													2
2		3											3	
3		3											3	3
4		3											2	3
5							2	2	2					1
CO (W.A)	**3**	**3**					**2**	**2**	**2**				**2.6**	**2**

22ECX12 - SATELLITE COMMUNICATION				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To understand the orbital mechanics and orbital effects on communication system. • To recognize the satellite power design and earth station systems • To gain knowledge about different multiplexing techniques for satellite communication

Course Outcomes The Student will be able to		Cognitive Level	Weightage of COs in End Semester Examination
CO1	Apply the fundamental concepts of satellite orbits to determine the orbital parameters of different satellite types	Ap	20%
CO2	Analyze the subsystems of uplink & downlink satellite communication systems and earth station systems	An	30%
CO3	Analyze the link design for signal to noise ratio calculations	An	20%
CO4	Design a satellite system that utilizes various multiplexing techniques	E	20%
CO5	Evaluate the contributions of satellite communication to sustainability for various applications	E	10%

UNIT I - SATELLITE ORBITS AND TRAJECTORIES	**(9)**
Orbital Mechanics: Orbit Equations, Kepler's Laws, Orbital Period, Orbit types - Look angle determination - Orbital effects on communication system performance - Satellite Launch.	
UNIT II - SATELLITE AND EARTH STATION SUBSYSTEMS	**(9)**
Satellite Subsystems: Power, Transponders, Antennas - AOCS, TTC&M - Control - Effects of earth - Perturbation, sun transit, moon transit - Satellite power design, Reliability - MTBF Basic Equations - System Noise and G/T ratio - Earth Station subsystems Uplink, Downlink and Design for a specified C/N ratio with GEO and LEO examples	
UNIT III - LINK DESIGN, MODULATION AND ERROR CONTROL	**(9)**
Single link design - Double link design aspects - PAM, Baseband processing - Digital Modulation for satellite links: BPSK, QPSK and QAM - TDM standards for satellite systems - Error control for satellite link: Requirements, ARQ, Concatenated Codes, Interleaving, Turbo codes.	
UNIT IV - MULTIPLE ACCESS FOR SATELLITE COMMUNICATIONS	**(9)**
FDM - FM-FDMA - TDMA - Structure and system design, Onboard Processing systems - DAMA and PAMA - CDMA system design and capacity	

UNIT V- APPLICATIONS	(9)
Remote sensing - Navigation - Scientific and military application - VSAT: Network architecture, Access Control protocols and techniques, VSAT Earth stations - Satellite Mobile Telephony - Global star - DBS/DTH Television - GPS - Weather satellites	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. T.Pratt, C. Bostian and J.Allnutt, "Satellite Communications", John Wiley and Sons, 3rd Edition, 2021.
2. Dennis Roddy, "Satellite Communications", Mc Graw Hill, 4th Edition, 2017

REFERENCES:

1. W.L.Pritchard, H G Suyderhoud and R A Nelson, "Satellite Communication System Engineering", 2nd Edition, PrenticeHall, 2013.
2. Tri. T. Ha, "Digital Satellite Communications", McGraw Hill, 2nd Edition, 2017.
3. Manojit Mithra, "Satellite Communication", Prentice Hall, 2005.
4. M. Richharia, "Satellite systems for Personal Applications", John Wiley, 2010

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3		3											2	
4			3										2	
5			3		2		3							
CO (W.A)	3	3	3		2		3						2	

22ECX13 - OPTICAL COMMUNICATION				
	L	T	P	C
	3	0	0	3

PREREQUISITE : 22ECC10

Course Objective:	• To learn and understand the basic concepts in optical fiber cable. • To gain knowledge on different losses in fiber optic cable. • To know about optical sources, coupling mechanisms, optical networks and optical measurement standards.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply field theory concepts in optical signal, optical sources and detectors.	Ap	30%
CO2	Apply the modal concepts in different mode fibers and determine the losses encountered in optical cable	Ap	30%
CO3	Use the optical equipments to measure the parameters in optical networks.	Ap	20%
CO4	Analyze analog and digital links using link design and rise time budget analysis for a given Optical Fiber communication link.	An	20%
CO5	Give an oral presentation of developments in Optical Fiber Communication with respect to standards, applications, challenges and impacts.	U	Internal Assessment

UNIT I - OPTICAL FIBERS - STRUCTURE	**(9)**
Evolution of Fiber Optic Systems , Elements of an Optical fiber Transmission link , Basic laws and definitions, Optical fiber modes and configurations , Mode theory of circular waveguides - Overview of modes, Key modal concepts , Linearly Polarized waves , Single Mode and Multi Mode Fibers, Graded Index Fiber Structure.	
UNIT II - ATTENUATION AND DISPERSION	**(9)**
Attenuation, Signal dispersion in fibers – Modal Delay, Group delay , Material dispersion , Wave Guide dispersion , Dispersion in single mode fibers, Polarization mode dispersion , RI profile and cut off wavelength, Dispersion Management, Dispersion Shifted Fibers.	
UNIT III - OPTICAL SOURCES	**(9)**
LED's - Surface and Edge emitters, Modulation of LED, LASER Diodes - Fabry-Perot Lasers , Distributed Feedback (DFB) Lasers , Modulation of LASER diodes , Power Launching and Coupling - Source to fiber power launching , Lensing Schemes for Coupling improvement , LED coupling to single mode fibers, Fiber connectors, Fiber splicers.	
UNIT IV - PHOTODETECTOR AND OPTICAL RECEIVER OPERATION	**(9)**
PIN Photo detector, Avalanche Photodiodes, Photodetector noise - Detector response time, Avalanche multiplication of Noise, Fundamental Receiver operation-Error sources, Front End Amplifiers, Digital Receiver Performance- Probability of error, Quantum limit, Point to point link systems considerations - Link Power budget, Rise time budget.	

UNIT V- OPTICAL NETWORKS AND PERFORMANCE MEASUREMENTS	(9)
Operational principles of WDM, EDFA, Solitons, Basic concepts of SONET/SDH, Performance Measurement- Measurement standards, Test Equipments, Power Measurements, Attenuation Measurements, Dispersion Measurements, OTDR.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. Gerd Keiser, "Optical Fiber Communications", McGraw-Hill Education, 5th Edition, 2017.

REFERENCES:

1. John M. Senior, "Optical Fiber Communications", Pearson Education, 3rd Edition, 2014.
2. Govind P.Agrawal, "Fiber-optic Communication Systems", A John Wiley & Sons, 3rd Edition, 2015.
3. R.P.Khare, "Fiber Optics and Optoelectronics", Oxford University, 2004.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3	2												
3	3	2											3	
4		3											2	
5								3	2	3		2		
CO (W.A)	3	2.6						3	2	3		2	2.5	

22ECX14 - INFORMATION THEORY AND CODING				
	L	T	P	C
	3	0	0	3
PREREQUISITE : 22ECC12				

Course Objective:	• To enable the student to investigate different channel coding techniques in text, audio and video. • To make the students to analyze the different block coding techniques. • To make the students to investigate different convolutional codes.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	The Students will be able to apply the coding techniques and design a channel.	Ap	20%
CO2	The Students will be able to analyze and implement the different source coding techniques.	An	20%
CO3	The Students will be able to analyze and solve the different channel coding techniques.	An	20%
CO4	The students will be able to apply different block coding techniques and design.	Ap	20%
CO5	The students will be able to design the convolutional codes.	C	20%

UNIT I - INFORMATION THEORY	**(9)**
Information – Information rate - Entropy – Classification of codes – K-raft McMillan inequality –source coding theorem Shannon Fano coding - Huffman coding - Extended Huffman coding – joint and conditional entropies- Mutual Information Discrete memory less channels: BSC, BEC and channel capacity - Shannon limit.	
UNIT II - SOURCE CODING	**(9)**
Text: Adaptive Huffman coding, arithmetic coding and latex format - Audio: Perceptual coding, masking techniques, psychoacoustic model, MPEG audio layers - I,II & III - Dolby AC3 – Image and video formats: GIF, TI F, BMP, PNG , SIF, CIF & QCIF – Image compression: JPEG – Video compression: Principles-I,B,P frame s and motion estimation.	
UNIT III - CHANNEL CODING	**(9)**
Characteristics of speech signals - Quantization techniques – Channel vocoder - Linear predictive coding – Information capacity theorem – Implication of the information capacity theorem- Information capacity of colored noise channel – Rate distortion theory - Data compression.	
UNIT IV - BLOCK CODES	**(9)**
Hamming codes: Hamming weight, hamming distance, minimum distance decoding – Single parity Codes Repetition codes: Linear block codes, cyclic codes – Syndrome calculation, encoder and decoder - CRC	

UNIT V- CONVOLUTIONAL CODES	(9)
Convolutional codes – Code tree, trellis, state diagram - Encoding - Decoding: Sequential search and Viterbi algorithm - Principle of turbo coding – Other codes: RS code, Gola y code and Burst error correcting code.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. R. Bose, Information Theory, Coding and Cryptography, Tata McGraw Hill, New Delhi, Third Edition, 2016
1. Fred Halsall, Multimedia Communications: Applications, Networks, Protocols and Standards, Pearson Education Asia, Fourth Edition, 2009.

REFERENCES:

1. K.Sayood, Introduction to Data Compression, Elsevier, Netherlands, Fifth Edition, 2017.
2. S.Gravano, Introduction to Error Control Codes, Oxford University Press, England, First Edition, 2007.
3. Amitabha Bhattacharya, Digital Communications, Tata McGraw Hill, New Delhi, First Edition, 2013.
4. Theodore Rappaport, Wireless Communications - Principles and Practice, Pearson Education, Bengaluru, Second Edition, 2012.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3		3											2	
4			3										3	
5									2	2	2	2	2	
CO (W.A)	3	3	3						2	2	2	2	2.2	

22ECX15 - RADAR COMMUNICATION				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To enable the student to explore the concept RADAR transmitters and detectors. • To make the students to analyze the different antennas used for RADAR applications. • To make the students to learn and understand the different types of RADAR and Doppler concepts.

Course Outcomes The Student will be able to		Cognitive Level	Weightage of COs in End Semester Examination
CO1	The Students will be able to discuss and summarize the RADAR communication principles.	Ap	20%
CO2	The Students will be able to analyze the different probabilities and calculate the amount of noise and RADAR signals.	An	20%
CO3	The Students will be able to design RADAR receivers and transmitters for specified application.	An	20%
CO4	The students will be able to construct different antenna for various RADAR applications.	C	20%
CO5	The students will be able to design RADAR by applying the Doppler concepts.	C	20%

UNIT I - INTRODUCTION TO RADAR	**(9)**
Basics of RADAR, EM Waves & properties- applications of RADAR, RADAR frequencies- RADAR block diagram, RADAR Coordinates, Radar equation for hard targets and the SNR- RADAR cross section of targets, RADAR Resolution Elements, Pulse, CW and FMCW RADARS –configurations, transmitter power- pulse repetition frequency, Duty Ratio, Pulse Compression, Coding	
UNIT II - DETECTION OF SIGNALS IN NOISE AND RADAR WAVEFORMS	**(9)**
Probability density functions – probabilities of detection and false alarm-matched filter receiver-detection criteria – integration of radar pulses - constant-false alarm rate receivers - RADAR Waveforms, Pulse Compression, Ambiguity Diagram.	
UNIT III - RADAR TRANSMITTER AND RECEIVER	**(9)**
Introduction- Types of Transmitters - linear-beam power tubes- solid-state RF power sources- magnetron-Klystron, crossed-filed amplifier- RADAR receiver- receiver noise figure- super heterodyne receiver, Digital Receivers, duplexers and receiver protectors- RADAR displays-Human Machine Interface(HMI)	
UNIT IV - RADAR ANTENNA	**(9)**
Functions of RADAR antenna- antenna parameters- antenna radiation pattern and aperture illumination - reflector antennas- electronically steered phased array antennas- phase shifters – frequency - scan arrays-- architectures for phased arrays, radiators for phased arrays- mechanically steered planar array antennas- radiation pattern synthesis -effect of errors on radiation patterns - low side lobes antennas.	

UNIT V- MTI AND PULSE DOPPLER RADAR	(9)
Introduction to Doppler and MTI RADAR - delay –line cancellers- staggered pulse repetition frequencies- doppler filter banks- digital MTI processing - Moving target detector- limitations to MTI performance pulse Doppler radar- MTD, Tracking radar- mono pulse tracking- conical scan and sequential lobing- comparison of trackers. Tracking accuracy- low-angle tracking- Atmospheric & Weather RADARS: Precipitation RADAR, Doppler Weather RADAR, Polarimetric RADAR, Clear Air RADARS.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Merril I Skolnik , "Introduction to Radar Systems", Mc Graw-Hill, 2017.
2. Peebles P Z , "Radar Principles", Wiley, 2016.

REFERENCES:

1. Richard J Doviak , Dusan S Zrnic , "Doppler Radar and Weather Observations", Academic Press, 2014.
2. Bringi V N, Chandrasekar V , "Polarimetric Doppler Weather Radar", Cambridge University Press, 2012.
3. Richards M A, Scheer J A and Holm W A , "Principles of Modern Radar", Scitech Publishing, 2014.
4. Levanon N , "Radar Signals", Wiley-IEEE Press, 2012.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3		3											2	
4			3										3	
5									2	2	2	2	2	
CO (W.A)	3	3	3						2	2	2	2	2.2	

22ECX16 - DIGITAL COMMUNICATION RECEIVERS				
	L	T	P	C
	3	0	0	3

PREREQUISITE : 22ECC12

Course Objective:	• To provide knowledge on complete analysis of synchronization techniques. • To deliberate the performance of Pass band, base band and spread spectrum communication. • To learn and design the fading channels.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	The students will be able to describe Baseband data transmission and reception.	Ap	20%
CO2	The students will be able to analyze the performance of various Pass band data transmission, reception techniques.	An	20%
CO3	The Students will be able to compare the performance of synchronization algorithms.	An	40%
CO4	The students will be able to analyze the performance of synchronizers.	An	20%
CO5	The students will be able to design the receivers of fading channels.	U	Internal Assessment

UNIT I - BASEBAND COMMUNICATION	**(9)**
Baseband PAM, Clock Synchronizers - Error tracking and spectral line generating synchronizers, Squaring synchronizers, Mueller and Muller synchronizers.	
UNIT II - PASSBAND COMMUNICATION	**(9)**
Pass band Transmission, Receivers for PAM, Sufficient Statistics for Reception in Gaussian Noise, Optimum ML receivers - Synchronized detection, Digital matched filter.	
UNIT III - SYNCHRONIZATION ALGORITHMS	**(9)**
ML synchronization algorithms – Estimator Structures for Slowly Varying Synchronization Parameters, Non-Data Aided and Data Aided algorithms. Timing parameter and carrier phase estimation, Phasor Locked Loop.	
UNIT IV - PERFORMANCE ANALYSIS OF SYNCHRONIZERS	**(9)**
Tracking Performance of Carrier and Symbol Synchronizers, Feedback and feed forward synchronizers. Cycle slipping, Acquisition of carrier phase and symbol timing.	
UNIT V- RECEIVERS FOR FADING CHANNELS	**(9)**
Characterization of Fading channels, Detection and parameter synchronization on Fading channels, Receiver structures for fading channels – Outer and Inner receivers, parameter synchronization for flat fading and selective fading channels.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. H.Meyer, M. Moeneclaey, S. A. Fechtel , "Digital Communication Receivers", Wiley, 2015.
2. U.Mengali, A.N.D.Andrea , "Synchronization Techniques for Digital Receivers", Kluwer, 2014.

REFERENCES:

1. Proakis J G, Salehi M , "Digital communications", Tata McGraw Hill, New York, 2018.
2. Rohde U L, Whitaker J C, Zahnd H , "Communications Receivers", McGraw-Hill, 2017.
3. Bernard Sklar , "Digital Communications- Fundamentals and applications", Prentice Hall, 2017.
4. Lathi B P, "Modern Digital and Analog communication Systems", Oxford University Press, 2017.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3		3											2	
4			3										3	
5									2	2	2	2	2	
CO (W.A)	3	3	3						2	2	2	2	2.2	

22ECX17 - SOFTWARE DEFINED RADIO				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To learn and understand the concept of the evolution and technology needs and investigate the essential functional components and architecture of Software Defined Radio. • To comprehend the concepts, architecture, components, radio procedure knowledge and design considerations of Cognitive Radio. • To explore the concepts of next generation wireless networks.

Course Outcomes The Student will be able to		Cognitive Level	Weightage of COs in End Semester Examination
CO1	Apply the concepts of analog and digital technologies to the systems required by a software-defined radio to function and the trade-offs and limitations encountered in the design of a software-defined radio system.	Ap	30%
CO2	Apply the cognitive radio design concepts to develop a cognitive radio environment.	Ap	30%
CO3	Analyze the architecture of software radio and develop architecture according to the needs.	An	20%
CO4	Design next generation wireless network with the application of spectrum management techniques	E	20%
CO5	Conduct experiments using simulation tools to demonstrate the implementation of Cognitive Radio.	U	Internal Assessment

UNIT I - SDR EVOLUTION	**(9)**
Definitions and potential benefits - software radio architecture evolution - foundations - technology tradeoffs and architecture implications - Antenna for Cognitive Radio.	
UNIT II - SDR ARCHITECTURE	**(9)**
Essential functions of the software radio - architecture goals - quantifying degrees of programmability - top level component topology - computational properties of functional components - interface topologies among plug and play modules – architecture partitions.	
UNIT III - INTRODUCTION TO COGNITIVE RADIOS	**(9)**
Marking radio self-aware - cognition cycle - organization of cognition tasks - structuring knowledge for cognition tasks – Enabling location and environment awareness in cognitive radios - concepts - architecture - design considerations.	
UNIT IV - COGNITIVE RADIO ARCHITECTURE	**(9)**
Primary Cognitive Radio functions - Behaviors - Components - A-Priori Knowledge taxonomy - observe - phase data structures - Radio procedure knowledge encapsulation - components of orient - plan - decide phases - act phase knowledge representation - design rules	

UNIT V - NEXT GENERATION (XG) WIRELESS NETWORKS	(9)
The XG Network architecture - spectrum sensing - spectrum management - spectrum mobility - spectrum sharing - upper- layer issues - cross-layer design.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. Alexander M. Wyglinski, Maziar Nekovee, and Y. Thomas Hou, "Cognitive Radio Communications and Networks – Principles and Practice", Elsevier Inc., 2010.
2. Huseyin Arslan , "Cognitive Radio, Software Defined Radio and Adaptive wireless system, Springer, 1st Edition, 2007.

REFERENCES:

1. Bruce A Fette, "Cognitive Radio Technology", Academic Press, 2009.
2. E. Biglieri, A.J. Goldsmith., L.J. Greenstein, N.B. Mandayam, H.V. Poor, "Principles of Cognitive Radio", Cambridge University Press, 2013.
3. Kwang- Cheng Chen and Ramjee Prasad, "Cognitive Radio Networks", John Wiley & Sons, Ltd, 2009.
4. Khattab, Ahmed, Perkins, Dmitri, Bayoumi, Magdy, "Cognitive Radio Networks - From Theory to Practice", Springer Series: Analog Circuits and Signal Processing, 2009.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3												3	
3		3											3	
4			3										2	
5					3			2	2				2	
CO (W.A)	3	3	3		3			2	2				2.5	

22ECX18 - 4G / 5G COMMUNICATION NETWORKS				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				

Course Objective:	• To familiar with evolution of wireless networks and fundamentals of 5G networks. • To acquire knowledge on spectrum sharing, spectrum trading and the processes associated with 5G architecture. • To understand the security features in 5G networks.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the knowledge of communication in finding the protocols and spectrum management.	Ap	30%
CO2	Apply the concepts of wireless networks in 5G through its architecture.	Ap	20%
CO3	Determine the specifications of 5G components.	Ap	30%
CO4	Analyze different network architecture, security features and threats in 5G networks.	An	20%
CO5	Perform in a team to prepare an effective oral presentation on topics related to 5G concepts, spectrum sharing and trading.	U	Internal Assessment

UNIT I - EVOLUTION OF WIRELESS NETWORKS	**(9)**
Networks evolution: 2G, 3G, 4G, evolution of radio access networks, need for 5G. 4G versus 5G, Next Generation core (NG-core), visualized Evolved Packet Core (vEPC).	
UNIT II - 5G CONCEPTS AND CHALLENGES	**(9)**
Fundamentals of 5G technologies, overview of 5G core network architecture, 5G new radio and cloud technologies, Radio Access Technologies (RATs), EPC for 5G.	
UNIT III - NETWORK ARCHITECTURE AND THE PROCESSES	**(9)**
5G architecture and core, network slicing, Multi Access Edge Computing (MEC), visualization of 5G components, end-to-end system architecture, service continuity, relation to EPC, edge computing. 5G protocols: 5G NAS, NGAP, GTP-U, IPSec and GRE.	
UNIT IV - DYNAMIC SPECTRUM MANAGEMENT AND MM-WAVES	**(9)**
Mobility management, Command and control, spectrum sharing and spectrum trading, cognitive radio based on 5G, millimeter waves.	
UNIT V- SECURITY IN 5G NETWORKS	**(9)**
Security features in 5G networks, network domain security, user domain security, flow based QoS framework, mitigating the threats in 5G.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:
1. Stephen Rommer, "5G Core networks: Powering Digitalization", Academic Press,2019 2. Saro Velrajan, "An Introduction to 5G Wireless Networks: Technology, Concepts and Use cases", First Edition, 2020.
REFERENCES:
1. Jyrki. T.J.Penttinen, "5G Simplified: ABCs of Advanced Mobile Communications", Copyrighted Material. 2. Wan Lee Anthony, "5G system Design: An end to end Perspective", Springer Publications, 2019.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3													
2	3													2
3	3													2
4	2	3												3
5								2	3	3		2		
CO (W.A)	**2.3**	**3**						**2**	**3**	**3**		**2**		**2.6**

VERTICAL 3: NETWORKS

22ECX21 - COMPUTER SYSTEMS AND HARDWARE				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**
PREREQUISITE : NIL				

Course Objective:	• To understand the concepts of computer hardware and motherboards. • To provide an adequate knowledge of processors and memory. • To accord basic knowledge in obtaining the features, working and installation concepts of various storage devices. • To learn the concepts of the type, features, specification, working of various input and output devices

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the knowledge of effective troubleshooting and maintenance of hardware components.	Ap	20%
CO2	Analyzethe developmental stages and architectural details of CPUs and memory to solve related hardware issues.	An	30%
CO3	Apply the knowledge ofelaborate features, installation, and maintenance of input and output devices.	Ap	30%
CO4	Design assemble, and configure complete computer systems, ensuring proper installation of components, operating systems, and device drivers.	E	20%
CO5	Give a presentation on self-learning, collaborate in teamwork, and ethically assemble hardware systems to address complex technical challenges.	U	Internal Assessment

UNIT I - HARDWARE AND MOTHERBOARDS	**(9)**
Basic computer hardware structure - Hardware and software - Different type of computers- Features of computer systems: Features of desktop system, Features of server computer, Features of laptops, Features of tablets - Motherboards: Features, components, processor support, controller, BIOS -Trouble shooting and maintenance of motherboards.	
UNIT II - PROCESSING UNIT AND MEMORY	**(9)**
Processor features - Developmental stages of CPU - Towards multiple core processors - Processor architectural details -Processor specifications – Installing and uninstalling CPU – CPU overheating issues – Memory: Features, types, working, memory map, installing and uninstalling memory modules, troubleshooting and maintenance of memory.	
UNIT III -STORAGE DEVICES	**(9)**
Storage Devices, Hard Disks: Details, working, feature, installation, selection, specifications, partitioning and formatting, maintenance and troubleshooting – optical storage devices features, working of optical storage drives, installing optical drives, troubleshooting and maintenance.	

UNIT IV - INPUT AND OUTPUT DEVICES	(9)
LCD monitors: Installing, specification, maintenance and troubleshooting of LCD monitors – LED monitors and touchscreens – Keyboard: Types and features, interfaces, installing, maintenance and troubleshooting – Mouse: types, working, features, interfaces, maintenance and troubleshooting	
UNIT V - ASSEMBLING AND CONFIGURING COMPUTERS	**(9)**
Assembling and configuring: Caution and safety, Setting up the cabinet - Installing heat sink and cooling fan – Installing memory module - Mounting motherboard – Installing hard disk – Connecting motherboard - Connecting to front panel – Connecting mouse, keyboard and monitor – Switching on the computer – Configuring – BIOS Installing operating system – Installing device drivers –Installing add-on cards.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. K. L. James, "Computer Hardware: Installation, Interfacing, Troubleshooting and Maintenance", PHI Learning, Delhi, 1st edition, 2013.
2. B. Govindarajalu, "IBM PC and Clones – Hardware, Troubleshooting and Maintenance", Tata McGraw-Hill, NewDelhi, 1st edition, 2002..

REFERENCES:

1. Jean Andrews, "Guide to Hardware Managing, Maintaining and Troubleshooting", Cengage Learning (Course Technology), Noida, 9th Edition, 2016.
2. Craig Zacker and John Rourke, "PC Hardware: The Complete Reference, McGraw-Hill, New Delhi, 1st edition2017.
3. Michael W. Graves, "A+ Guide to PC Hardware Maintenance and Repair", Cengage Learning, Noida, 1st edition, 2004.
4. Scott M. Mueller, "Upgrading and Repairing PCs", Que Publishing, Ahmedabad, 22nd Edition, 2015..

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3	3												2	
4			3										2	
5							2	1	2				2	
CO (W.A)	3	3	3				2	1	2				2	

22ECX22 - NETWORK INFORMATION SECURITY				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• To understand the different security model. • To study about risk management			

Course Outcomes The Student will be able to		Cognitive Level	Weightage of COs in End Semester Examination
CO1	Apply the knowledge of network security to protect data.	Ap	20%
CO2	Analyze the threat factors in the network system	An	20%
CO3	Analyze the security technology in information theory	An	40%
CO4	Develop skills in securing communication protocols.	An	20%
CO5	Oral presentation on the application of network security	U	Internal Assessment

UNIT I - INTRODUCTION TO INFORMATION SECURITY	**(9)**
The History of Information Security- Critical Characteristics of Information - CNSS Security Model - Components of an Information System - Balancing Information Security and Access - The Systems Development Life Cycle - The Security Systems Development Life Cycle.	
UNIT II - RISK MANAGEMENT	**(9)**
Introduction - An Overview of Risk Management - Risk Identification -Risk Assessment - Risk Control Strategies - Selecting a Risk Control Strategy - Risk Management Discussion Points- Recommended Practices in Controlling Risk.	
UNIT III - PLANNING FOR SECURITY	**(9)**
Introduction - Information Security Policy, Standards and Practices - The Information Security Blueprint: The ISO 27000 Series, NIST Security Models, Design of Security Architecture - Security Education, Training and Awareness Program - Continuity Strategies.	
UNIT IV - SECURITY TECHNOLOGY	**(9)**
Introduction - Intrusion Detection and Prevention Systems: IDPS Terminology, Use of IDPS, Strengths and Limitations of IDPS - Honey Pots, Honey Nets, and Padded Cell Systems - Scanning and Analysis Tools - Biometric Access Controls.	
UNIT V - IMPLEMENTING INFORMATION SECURITY	**(9)**
Introduction - Information Security Project Management - Technical Aspects of Implementation - Nontechnical Aspects of Implementation - Information Systems Security Certification and Accreditation.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Michael E Whitman and Herbert J Mattord, "Principles of Information Security", Course Technology, New Delhi, Seventh Edition, 2021 Reprint.

REFERENCES:

1. Nina Godbole, "Information Systems Security-Security Management, Metrics, Frameworks and Best Practices", Wiley India Pvt. Ltd., New Delhi, First Edition, 2009.(Biometric Controls, Security of Wireless Networks, Laws and Legal Framework)
2. Thomas R.Peltier, "Information Security Fundamentals", Auerbach Publications, Second Edition, 2013.
3. Micki Krause and Harold F.Tipton, "Information Security Management Handbook", Auerbach Publications, Sixth Edition,2008.
4. Mark Merkow and Jim Breithaupt," Information Security - Principles & Practices", Second Edition, Pearson Education,2014.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3											2	
3		2											2	
4			3											
5										2				
CO (W.A)	3	3	3							2			2	

22ECX23 - CRYPTOGRAPHY AND NETWORK SECURITY				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				

Course Objective:	• To learn and understand the concepts and mechanism of security services and attacks in computing and various Network and System Security methods. • To investigate Symmetric Cryptography, its types and Public Key Cryptography Algorithms. • To analyze the Message Authentication algorithms like HASH function and HMAC.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the knowledge of mathematics to cryptography, examine the various system security schemes and apply in the design of communication networks.	Ap	30%
CO2	Analyze algorithms and techniques of Block and Stream ciphers to solve problems in simple substitution ciphers.	An	30%
CO3	Analyze the concepts of message integrity, digital signature and key management schemes to improve the security mechanism.	An	20%
CO4	Examine the various system security schemes and apply in the design of communication networks.	E	20%
CO5	Give oral presentation in teams on a case study of a real time security applied in network platforms.	U	Internal Assessment

UNIT I – SECURITY IN COMPUTING	**(9)**
Security services- Attacks- Mechanism- Points of Security Vulnerability - Methods of Defense- Controls, Effectiveness of Control- Introduction to Cryptography and Steganography- Plan of attack - Attack on Encryption – Standards: Standard Setting Organizations - IEC 62443, ISO 27001.	
UNIT II – SYMMETRIC CRYPTOGRAPHY	**(9)**
Encryption and Decryption- Substitution- Transposition- Traditional Block Cipher Structure- Data Encryption Standard- Advance Encryption Standard- Triple DES, Stream Ciphers, RC4 Ciphers.	
UNIT III – PUBLIC KEY CRYPTOGRAPHY	**(9)**
Introduction to Number Theory-Requirements of Public Key Cryptography - Rivest-Shamir-Adleman(RSA) algorithm - Key Management – Diffie - Hellman Key Exchange - Elliptic Curve Cryptography.	
UNIT IV – MESSAGE AUTHENTICATION	**(9)**
Hash functions –Secure Hash algorithm- Message Authentication Requirements, Functions - HMAC- Digital signatures.	

UNIT V – NETWORK AND SYSTEM SECURITY	(9)
Authentication applications - E-mail Security - IP security - Web security – Malicious Software - Intruders - Firewalls- Art cyber security- Defense in depth.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. William Stallings, "Cryptography & Network Security: Principles & Practices", 7th Edition, Pearson Education, New Delhi, 2017.

REFERENCES:

1. Behrouz A Forouson, "Cryptography & Network Security", Tata McGraw Hill, New Delhi, 2010.
2. Charles P Pleeger, "Security in Computing", Prentice Hall, New Delhi, 2011.
3. Paul C Van Oorschot and Scott A Vanstone, "Handbook of Applied Cryptography", CRC Press.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3											3	
3		3											3	
4			3										2	
5							2	2	2				2	
CO (W.A)	3	3	3				2	2	2				2.5	

22ECX24 - HIGH PERFORMANCE COMMUNICATION NETWORKS				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To understand the concept of networks and functionalities of high speed networks. • To study about different types protocols for real time operations, queuing disciplines and differentiated services • To explore connection-oriented services with reference to MPLS & VPN

	Course Outcomes The Student will be able to	Cognitive Level	Weightage of COs in End Semester Examination
CO1	Apply networking concepts to configure, troubleshoot, and optimize network systems and protocols.	Ap	20%
CO2	Apply the principles and concepts of high speed networks in performance computing.	An	30%
CO3	Analyze various networking technologies, protocols, and services for their effectiveness in meeting specific network requirements.	An	30%
CO4	Ability to analyze the different levels of quality of service (QoS) to different applications.	E	20%
CO5	Perform as an individual or in team, prepare a report on connection-oriented services and give oral presentation.	U	Internal Assessment

UNIT I - NETWORK CONCEPTS	**(9)**
Introduction - Principles - Applications - Services: Network Types- Network architectures - Layered architecture: layered network - Limitations	
UNIT II - HIGH SPEED NETWORKS	**(9)**
Frame Relay Networks – Asynchronous transfer mode – ATM Protocol Architecture,– ATM Service Categories – AAL, High Speed LANs: Fast Ethernet, Gigabit Ethernet, Fiber Channel – Wireless LANs: applications, requirements – Architecture of 802.11	
UNIT III - PROTOCOLS FOR QOS SUPPORT	**(9)**
RSVP – Goals & Characteristics, Data Flow, RSVP operations, Protocol Mechanisms – Multiprotocol Label Switching – Operations, Label Stacking, Protocol details – RTP – Protocol Architecture.	
UNIT IV - INTEGRATED AND DIFFERENTIATED SERVICES	**(9)**
services - Queuing discipline: Fair queuing, processor sharing, bit round fair queuing, generalized processor sharing, weighted fair queuing - Random early detection - Differentiated services.	
UNIT V- ADVANCED NETWORK CONCEPTS	**(9)**
VPN: Remote access, site-to-site, tunneling and point to point protocol - Security in VPN - MPLS: Operation, routing, tunneling and use of FEC, traffic engineering and MPLS based VPNs - Peer to peer connection.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:
1. Jean Warland, Pravin Varaiya, "High Performance Communication Networks", Morgan Kaufmann Publishers, San Francisco ,2nd edition, 2011.
REFERENCES:
1. Lenon Garcia Widjaja, "Communication Networks", Tata McGraw-Hill, New Delhi, 2nd edition, 2007. 2. Ranier Handel Manfred N Huber, Stefan Schroder, "ATM Networks - Concepts, Protocols Applications", Addison Wesley, New York, 3rd edition, 2006. 3. Irvan Pepelnjk, Jim Guichard & Jeff Apcar, "MPLS and VPN Architecture", Volume 1 and 2, Cisco Press, 2007.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3												
3		3												3
4			3											2
5						2			2		2			
CO (W.A)	**3**	**3**	**3**			**2**			**2**		**2**			**2.5**

22ECX25 - WIRELESS ADHOC AND SENSOR NETWORKS				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• To understand the concept of networks • To study about different types sensor networks. • To study about sensor network security and tools.			

	Course Outcomes The Student will be able to	Cognitive Level	Weightage of COs in End Semester Examination
CO1	Apply the challenges and considerations of various routing protocols to design routing protocols for ad hoc networks.	Ap	20%
CO2	Apply layer-wise attack concepts to propose solutions to counteract threats such as jamming and tampering.	Ap	30%
CO3	Analyze the energy consumption factors of sensor nodes and discuss strategies for energy optimization.	An	30%
CO4	Evaluate various routing and MAC protocols, security measures, and platform tools to make informed decisions based on network requirements.	E	20%
CO5	Develop solutions for real-world problems related to energy efficiency, security, and performance optimization in ad hoc and sensor networks and give oral presentation as an individual or in groups.	C	Internal Assessment

UNIT I - AD HOC NETWORKS – INTRODUCTION AND ROUTING PROTOCOLS	**(9)**
Elements of Ad hoc Wireless Networks, Ad hoc wireless Internet, Issues in Designing a Routing Protocol for Ad Hoc Wireless Networks, Classifications of Routing Protocols, Table Driven Routing Protocols – Destination Sequenced Distance Vector (DSDV)–Ad hoc On–Demand Distance Vector Routing (AODV).	
UNIT II - SENSOR NETWORKS – INTRODUCTION & ARCHITECTURES	**(9)**
Challenges for Wireless Sensor Networks, Enabling Technologies for Wireless Sensor Networks, Single-Node Architecture – Hardware Components, Energy Consumption of Sensor Nodes, Network Architecture –,Transceiver Design Considerations, Optimization Goals and Figures of Merit.	
UNIT III - WSN NETWORKING CONCEPTS AND PROTOCOLS	**(9)**
MAC Protocols for Wireless Sensor Networks– S-MAC, The Mediation Device Protocol, PAMAS, Schedule based protocols –IEEE 802.15.4 MAC protocol, Routing Protocols- Energy Efficient Routing, Challenges and Issues in Transport layer protocol.	
UNIT IV - SENSOR NETWORK SECURITY	**(9)**
Network Security Requirements,-Network Security Attacks, Layer wise attacks in wireless sensor networks, possible solutions for jamming, tampering, -Key Distribution and Management, Secure Routing – SPINS, reliability requirements in sensor networks	

UNIT V- SENSOR NETWORK PLATFORMS AND TOOLS	(9)
Sensor Node Hardware – Berkeley Motes, Programming Challenges, Node-level software platforms – TinyOS, CONTIKIOS, Node-level Simulators -TOSSIM, Programming beyond individual nodes – State centric programming.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. C. Siva Ram Murthy and B. S. Manoj, "Ad Hoc Wireless Networks Architectures and Protocols", Prentice Hall, PTR, 2004.
2. Holger Karl , Andreas willig, "Protocol and Architecture for Wireless Sensor Networks", John Wiley publication, Jan 2006.

REFERENCES:

1. Feng Zhao, Leonidas Guibas, "Wireless Sensor Networks: an information processing approach", Elsevier publication, 2004
2. Charles E. Perkins, "Ad Hoc Networking", Addison Wesley, 2000.
3. I.F. Akyildiz, W. Su, Sankarasubramaniam, E. Cayirci, "Wireless sensor networks: a survey", Computer Networks, Elsevier, 2002

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3												3	
3		3											3	
4					3									3
5									3	2		1		
CO (W.A)	3	3			3				3	2		1	3	3

22ECX26 - AUTOMOTIVE ELECTRONICS AND NETWORKING				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To apply fundamentals and innovative concept to optimize the automotive industry • To analyze the ignition system and enhance them with new techniques • To develop the electronic control for vehicle system • To evaluate the physical parameters of automobile system using advanced sensors • To design a advanced automotive communication network

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply fundamentals and innovative concept to optimize the automotive industry	Ap	20%
CO2	Analyze the ignition system and enhance them with new techniques	An	20%
CO3	Develop the electronic control for vehicle system	C	20%
CO4	Evaluate the physical parameters of automobile system using advanced sensors	E	20%
CO5	Design a advanced automotive communication network	C	20%

UNIT I - FUNDAMENTALS OF AUTOMOTIVE ELECTRONICS	**(9)**
Automobile systems: Engine and its control - Ignition systems - Steering systems - Control systems: proportion controller, Proportional Integral controller and Proportional Integral differential controller.	
UNIT II - AUTOMOTIVE SENSORS	**(9)**
Sensor basics & its Functions - Air mass flow sensor- Crankshaft angular position sensor - Throttle valve sensor - Eddy	
UNIT III - AUTOMOTIVE ACTUATORS	**(9)**
Fuel Injectors - Exhaust gas recirculation Actuator - Electronic Ignition sub-systems - Digital Engine control systems: Speed density method - Idle speed control method- EGR control - Distributor-less Ignition control	
UNIT IV - VEHICULAR ELECTRONICS ARCHITECTURE	**(9)**
Intelligent Power distribution module - Supplemental restraint systems - Body control module – Engine control modules - Automatic drive positioned control unit - Driver seat control module - Front air control unit and transmission control unit	
UNIT V- AUTOMOTIVE NETWORKING	**(9)**
Networking basics topologies - Addressing - Control mechanisms: Event control & Timer control - Network topologies for new generation vehicles - Bus systems: CAN Bus, High speed CAN, LIN bus, MOST bus, Bluetooth: Piconet and scatternet.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:
1. Konrad Reif, "Automotive Mechatronics Automotive Networking, Driving Stability Systems, Electronics",Vieweg-Teubner Verlag (2015). 2. Najamuz Zaman (auth.), "Automotive Electronics Design Fundamentals", Springer International Publishing (2015)
REFERENCES:
1. Robert Bosch GmbH, Bosch, "Automotive Electrics and Automotive Electronics Systems and Components, Networking and Hybrid Drive", Springer Vieweg (2014) 2. William Ribbens, "Understanding Automotive Electronics, Fifth Edition-Newnes (1998). 3. W.H.Crouse, "Automobile Electrical Equipment", McGraw-Hill, 1996. 4. P.L.Kholi, "Automotive Electrical Equipment", Tata McGraw-Hill, 1995.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3												3	
2		3											3	
3			2										3	2
4				3									3	2
5				3									3	
CO (W.A)	**3**	**3**	**2**	**3**									**3**	**2**

22ECX27 - NEURAL NETWORKS					
		L	T	P	C
		3	0	0	3
PREREQUISITE : NIL					
Course Objective:	• To understand artificial neural model and architecture of neural networks • To study about to develop learning algorithms of neural networks. • To learn about the application areas of neural networks.				

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply neural network concepts through analysis and implementation of neural network models	Ap	20%
CO2	Apply the steps needed to improve performance of the selected neural network.	Ap	20%
CO3	Analyze vector quantization and self organization feature maps.	An	20%
CO4	Design appropriate neural networks to specific application.	E	20%
CO5	Develop neural network models for complex real-world problems, considering societal impacts and ethics.	E	20%

UNIT I – ARCHITECTURE	**(9)**
Biological Neuron- Artificial Neural Model- Types of activation functions- Feedforward and Feedback- Convex Sets- Convex Hull and Linear Separability- Non-Linear Separable Problem- XOR Problem- Multilayer Networks- Convolutional Neural Networks- Backpropagation Neural Network	
UNIT II - SUPERVISED LEARNING	**(9)**
Perceptron learning and Non Separable sets- Least Mean Square Learning- MSE Error surface- Steepest Descent Search- JL-LMS approximate to gradient descent- Application of LMS to Noise Cancelling- Multi-layered Network Architecture	
UNIT III - SUPPORT VECTOR MACHINES	**(9)**
Statistical Learning Theory- Support Vector Machines- SVM application to Image Classification- Radial Basis Function Regularization theory- Generalized RBF Networks- Learning in RBFNs- RBF application to face recognition.	
UNIT IV - ATTRACTOR NEURAL NETWORKS	**(9)**
Associative Learning- Attractor Associative Memory- Linear Associative memory- Hopfield Network- application of Hopfield Network- Brain State in a Box neural Network- Simulated Annealing- Boltzmann Machine- Bidirectional Associative Memory.	
UNIT V- VECTOR QUANTIZATION	**(9)**
Maximal Eigenvector Filtering- Extracting Principal Components- Generalized Learning Laws- Vector Quantization- Self organization Feature Maps- Application of SOM- Growing Neural Gas	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:
1. Satish Kumar, "Neural Networks A Classroom Approach", McGraw Hill Education Pvt. Ltd, 2nd Edition, 2017
REFERENCES:
1. J.M. Zurada," Introduction to Artificial Neural Systems", Jaico Publications, 1994. 2. B. Yegnanarayana, "Artificial Neural Networks", 2nd Edition, Pearson Education / PHI, 2004. 3. S. Sivanandam," Introduction to Artificial Neural Networks", 1st Edition, Sangam Ltd, 2003.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	**2**	**3**	**4**	**5**	**6**	**7**	**8**	**9**	**10**	**11**	**12**	**1**	**2**
1	3												2	
2	3												2	
3		3												
4			3										3	
5			3			2		2						
CO (W.A)	**3**	**3**	**3**			**2**		**2**					**2.3**	

22ECX28 - ARTIFICIAL INTELLIGENCE				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• To understand the core concepts and historical evolution of AI, principles of logical reasoning, and methods for quantifying uncertainty using probability and Bayes' Rule. • To master informed and uninformed search techniques, applying them to solve various AI problems effectively. • To gain proficiency in classical planning methods, including state space search and planning graphs, for effective problem-solving in AI.			

Course Outcomes The Student will be able to		Cognitive Level	Weightage of COs in End Semester Examination
CO1	Apply AI fundamentals to real-world scenarios, demonstrating an understanding of its history, definitions, and key components.	Ap	20%
CO2	Analyze un-informed and informed search strategies to solve AI and constraint satisfaction problems, avoiding repeated states and searching with partial information.	An	30%
CO3	Design logical reasoning systems using knowledge-based agents and first-order logic to solve problems with incomplete or uncertain information.	An	30%
CO4	Formulate and solve planning problems using classical planning algorithms and graph-based methods.	Ap	20%
CO5	Engage in independent learning to stay updated with AI advancements and continuously improve problem-solving skills.	E	Internal Assessment

UNIT I - FUNDAMENTALS OF ARTIFICIAL INTELLIGENCE	(9)
Introduction–Definition – History of AI - Intelligence, Knowledge, and Human artifice -Future of Artificial Intelligence – Characteristics of Intelligent Agents–Typical Intelligent Agents – Problem Solving Approach to Typical AI problems- Searching for solutions -Un-informed search strategies –Avoiding repeated states -Searching with partial information.	
UNIT II - INFORMED SEARCHING TECHNIQUES	**(9)**
Informed search and search strategies -Heuristic function -Local search algorithms and optimistic problems –Constraint Satisfaction Problems (CSP) -Backtracking search -Structure of problems.	
UNIT III - LOGICAL REASONING	**(9)**
Logical agents: Knowledge-based agents – The Wumpus world. Logic – Propositional logic: A very simple logic Propositional theorem proving. First order logic: Representation – Syntax and semantics of first order logic –Inference in first order logic: Propositional versus first order inference– Unification and lifting – Forward chaining – Backward chaining – Resolution.	

UNIT IV - PLANNING AND DECISION MAKING	(9)
Classical Planning: Definition – Algorithms for planning as state space search- Planning graphs –classical planning approaches. Making simple Decisions-Combining beliefs and desires under Uncertainty-Utility theory, Utility functions-Multi attribute utility functions-Decision networks- The value of information-Decision theoretic expert systems.	
UNIT V- LEARNING	**(9)**
Quantifying uncertainty: Acting under uncertainty - Probability basics – Bayes' Rule. Probabilistic reasoning: Representing knowledge in uncertain domain- The semantics of Bayesian networks. Forms of learning - Supervised learning - Learning decision trees.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Stuart Russell and Peter Norvig, 'Artificial Intelligence –A Modern Approach', 3rd Edition, Pearson Education, 2016.
2. Deepak Khemani, 'Artificial Intelligence', Tata McGraw Hill Education, 2013

REFERENCES:

1. Kevin Night and Elaine Rich, Nair B., 'Artificial Intelligence (SIE)', 3rd Edition, McGraw Hill,2008.
2. Dan W. Patterson, 'Introduction to AI and ES', 3rd Edition, Pearson Education, 2007.
3. Peter Jackson, 'Introduction to Expert Systems', 3rd Edition, Pearson Education, 2007.
4. Nils J. Nilsson, 'Artificial Intelligence: A new Synthesis', Harcourt Asia Pvt. Ltd., 2000.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3	2	1										2	
2	3			3	2									2
3		3	3		2								2	2
4	3		3								1		1	
5									2		3		1	
CO (W.A)	3	2.5	2.3	3	2				2		2		1.5	2

VERTICAL 4: SIGNAL AND IMAGE PROCESSING

22ECX31 - DIGITAL IMAGE PROCESSING					
		L	**T**	**P**	**C**
		3	**0**	**0**	**3**
PREREQUISITE : NIL					
Course Objective:	• To gain knowledge about different image processing techniques. • To understand use of various transforms for different types of images.				

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply transform-domain representation of images using different transformation techniques.	Ap	20%
CO2	Analyze various techniques in image enhancement in spatial and frequency domain.	An	20%
CO3	Implement the compression techniques for images and videos.	Ap	40%
CO4	Design various segmentation algorithms and representation techniques.	C	20%
CO5	Apply the concepts of image processing in gray and color data	U	Internal Assessment

UNIT I - DIGITAL IMAGE FUNDAMENTALS	**(9)**
Elements of digital image processing systems - Elements of visual perception - Brightness-Contrast-Hue-Saturation-Mach band effect - Image sampling-Quantization-Basic relationship between pixels - Zooming and Shrinking of Digital Images - Color image fundamentals- RGB-HSI models.	
UNIT II - IMAGE TRANSFORMS	**(9)**
2D transforms-DFT-DCT-Discrete Sine, Walsh-Hadamard, Slant-Haar, KL transforms and SVD -properties of all transforms.	
UNIT III - IMAGE ENHANCEMENT AND RESTORATION	**(9)**
Spatial Domain enhancement: gray level transformations–histogram equalization-Image averaging-Spatial filtering: Smoothing, Sharpening filters- Frequency domain filters: Smoothing-Sharpening filters-Homomorphic filtering. Image Restoration: Degradation model-Unconstrained and Constrained restoration-Inverse filtering-Wiener filtering.	
UNIT IV - IMAGE COMPRESSION	**(9)**
Need for data compression-Error free compression-Variable length coding-Bit-Plane coding-Lossless and Lossy Predictive coding, JPEG and MPEG Compression Standards.	
UNIT V - IMAGE SEGMENTATION AND REPRESENTATION	**(9)**
Point- Line and edge detection- Thresholding – Region based segmentation: Region splitting and merging. Image representation: chain codes-polygonal approximations-signatures-boundary segments-skeletons	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:
1. Rafael C. Gonzales, Richard E. Woods, "Digital Image Processing", Pearson Education, 4th Edition, 2018.
REFERENCES:
1. Rafael C. Gonzalez, Richard E. Woods, Steven L. Eddins, "Digital Image Processing Using MATLAB", Tata McGraw Hill Pvt. Ltd., 3rd Edition, 2011. 2. Anil Jain K. "Fundamentals of Digital Image Processing", PHI Learning Pvt. Ltd., Second Edition, 2004. 3. William K Pratt, "Digital Image Processing", Willey India Pvt Ltd., Fourth Edition, 2010.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1		3												
2			2										2	
3			2										2	
4					3									
5							2							
CO (W.A)		3	2		3		2						2	

22ECX32 - SPEECH SIGNAL PROCESSING				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**
PREREQUISITE : NIL				

Course Objective:	• To understand the speech production mechanism and the various speech analysis techniques and speech models. • To acquire concepts of the speech compression techniques and linear predictive coding. • To study the speaker recognition and text to speech synthesis techniques.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply knowledge of speech production mechanisms to optimize speech processing.	Ap	20%
CO2	Apply speech compression techniques using various modulation methods.	Ap	20%
CO3	Analyze Hidden Markov Model using speech recognition techniques	An	20%
CO4	Analyze speaker recognition and text to speech synthesis systems.	An	30%
CO5	Design speech signal processing systems with consideration for environmental sustainability	E	10%

UNIT I - SPEECH SIGNAL CHARACTERISTICS & ANALYSIS	**(9)**
Speech production process - Speech sounds and features- - Phonetic representation of speech - Representing- speech in time and frequency domains - Short-Time Analysis of Speech - Short-Time Energy and Zero-Crossing Rate - Short-Time Fourier Transform(STFT) - Speech Spectrum- Cepstrum - Mel-Frequency Cepstrum Coefficients -Hearing and Auditory Perception	
UNIT II - SPEECH COMPRESSION	**(9)**
Sampling and Quantization of Speech (PCM) - Adaptive differential PCM - Delta Modulation -Vector Quantization- Linear predictive coding (LPC) - Code excited Linear predictive Coding(CELP)	
UNIT III - SPEECH RECOGNITION	**(9)**
LPC for speech recognition- Hidden Markov Model (HMM)- training procedure for HMM- subword unit model based on HMM- language models for large vocabulary speech recognition – Overall recognition system based on subword units - Context dependent subword units	
UNIT IV - SPEAKER RECOGNITION	**(9)**
Acoustic parameters for speaker verification- Feature space for speaker recognition-similarity measures-Text dependent speaker verification-Text independent speaker verification techniques	

UNIT V- TEXT TO SPEECH SYNTHESIS	(9)
Text to speech synthesis(TTS)- Concatenative and waveform synthesis methods, sub-word units for TTS, intelligibility and naturalness-role of prosody- Natural Language Processing	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. L. R. Rabiner and R. W. Schafer, "Introduction to Digital Speech Processing", Vol.1, Now publishers inc, 2007.
2. Ben Gold and Nelson Morgan "Speech and Audio signal processing : processing and perception of speech and music", John Wiley and sons 2011

REFERENCES:

1. Lawrence Rabiner, Biiing and– Hwang Juang and B.Yegnanarayana, "Fundamentals of Speech Recognition", Pearson Education, 2009.
2. Claudio Becchetti and Lucio Prina Ricotti, "Speech Recognition", John Wiley and Sons, 1999.
3. Donglos O shanhnessy, "Speech Communication: Human and Machine ", 2nd Ed. University press 2001.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3												2	
3		3											2	
4		3			2								2	
5			2				3	2						
CO (W.A)	3	3	2		2		3	2					2	

22ECX33 - MULTIMEDIA COMPRESSION TECHNIQUES				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To gain deep knowledge about various compression techniques. • To learn the representations, perceptions and applications of multimedia.

	Course Outcomes The Student will be able to	**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply different compression techniques for text files.	Ap	20%
CO2	Analyze the different audio compression coding and speech compression techniques.	An	20%
CO3	Implement the different compression approaches, coding and JPEG standards.	C	40%
CO4	Analyze the techniques used for video compressions.	An	20%
CO5	Apply the concepts of information theory, models and coding.	Ap	Internal Assessment

UNIT I - INTRODUCTION	**(9)**
Overview of Information theory-models and coding- rate distortion theory-scalar quantization-vector quantization structured vector quantizes.	
UNIT II - TEXT COMPRESSION	**(9)**
Compaction techniques - Static Huffman coding - Dynamic Huffman coding - Arithmetic coding - Lempel-Ziv coding - Lempel-Ziv Welsh coding.	
UNIT III-AUDIO AND SPEECH COMPRESSION	**(9)**
Audio compression techniques – frequency domain and filtering - Basic sub band coding - Application to speech coding - G.722 - Application of audio coding: MPEG audio - Silence compression – Speech compression techniques.	
UNIT IV -IMAGE COMPRESSION	**(9)**
Approaches to image compression - Predictive techniques - PCM, DPCM, JPEG, Quad tree DCT coding-EZW coding- SPIHT coding- JPEG 2000 standards.	
UNIT V- VIDEO COMPRESSION	**(9)**
Video signal representation - Video compression techniques - MPEG1, 2, 4 - Motion estimation - H.261, H.263, and H.264 - Overview of wavelet based compression- Real time compression.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:
1. I Sayood Khaleed, "Introduction to Data Compression", Morgan Kauffman, 4th Edition, Morgan Kaufmann publishers2014. 2. Fred Halsall, James F. Kurose, "Multimedia communication - Applications, Networks, Protocols and standards", Pearson Education Limited, 2004
REFERENCES:
1. I David Solomon, "Data Compression the complete reference", Springer, 4th Edition, 2007. 2. Jerry D. Gibson, "Multimedia Communications: Directions and Innovations", Morgan Kaufmann, 2nd Edition, 2001.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1			3										1	3
2					2									
3					2								1	
4				2										
5												2		
CO (W.A)			3	2	2							2	1	3

22ECX34 - DEEP LEARNING				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• To equip students with a comprehensive understanding of fundamental deep learning concepts, including backpropagation and optimization algorithms for training neural networks. • To enable students to apply regularization techniques and diverse hyper parameter tuning strategies to improve model performance. • To empower students to practically implement convolutional neural networks (CNN) and recurrent neural networks (RNN) in real-world applications related to speech and computer vision.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply machine learning concepts such as overfitting, under fitting, and hyper parameter tuning to improve learning algorithms.	Ap	20%
CO2	Analyze gradient-based learning techniques and deep learning fundamentals, including back propagation, regularization, and optimization algorithms.	An	30%
CO3	Design optimization strategies using advanced techniques like momentum-based gradient descent, stochastic gradient descent, and learning rate schedulers.	An	30%
CO4	Implement regularization methods to address challenges like vanishing and exploding gradients, and optimize neural network performance using techniques such as dropout and batch normalization.	Ap	20%
CO5	Explore advanced architectures like CNNs, RNNs, and Transformers, and apply them to vision and speech tasks.	E	Internal Assessment

UNIT I –INTRODUCTION TO MACHINE LEARNING	**(9)**
Machine learning Basics: Learning algorithms - Overfitting – Under fitting -digital camera and lightning, Hyper parameters Estimators - Validation - Maximum Likelihood estimation - Bayesian Statistics - Challenges in Machine□Learning	
UNIT II - DEEP LEARNING FUNDAMENTALS	**(9)**
Gradient based learning - Hidden Units - Architectural design - Back - propagation for MLP - Regularization - Parameter Regularization - Data Augmentation - Dropout - Optimization algorithms - Adaptive learning rates.	
UNIT III - OPTIMIZATION	**(9)**
Introduction to Optimization – Convex Optimization - Drawback of Gradient Descent – Momentum based GD - Nesterov Accelerated GD – Stochastic GD- mini batch GD-learning rate schedulers.	

UNIT IV - REGULARIZATION	(9)
Vanishing and exploding gradients-Activation functions (tanh, relu , leaky relu)-bias-variance tradeoff – L2-Regularization- Batch Normalization –Dropout- Initialization Strategies.	
UNIT V- ADVANCED ARCHITECTURES	**(9)**
CNN-Basic of Convolution – Cross entropy loss – Architectures: LeNet - AlexNet .Resnet, RNN: BPTT – LSTM - GRU–Transformers. Applications to vision and speech.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Ian Goodfellow, YoshuaBengio, Aaron Courville , "Deep Learning", MIT Press, USA, 2016.
2. Adam Gibson, Josh Patterson , "Deep Learning A practitioner's approach", O'Reilly, USA, 2016

REFERENCES:

1. Yusuke Sugomori , "Deep Learning: Practical Neural Networks with Java", Packt Publisher, New York, 2016.
2. Jeff Heaton , "Artificial Intelligence for Humans: Deep Learning and Neural Networks", Lightning Source Inc, Tennessee, 2015

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3				2						1		2	
2		3		3	2									2
3			3		2				2					2
4	3		3								1		2	
5											2	3		1
CO (W.A)	3	3	3	3	2				2		1.3	3	2	1.6

22ECC35 – COMPUTER VISION				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				

Course Objective:	• To equip students with fundamental concepts related to image formation and processing, as well as feature detection, matching, and detection. • To gain a comprehensive understanding of feature-based alignment, motion estimation, and 3D reconstruction principles, including various techniques and model-based reconstruction. • To become familiar with image-based rendering and recognition.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply image processing techniques like geometric transformations, photometric image formation, and digital camera operations to solve computer vision problems.	Ap	20%
CO2	Analyze feature detection, matching, and segmentation methods to identify significant image features.	An	30%
CO3	Design alignment and motion estimation systems using 2D/3D alignment, pose estimation, and optical flow to track motion in visual data.	An	30%
CO4	Implement 3D reconstruction techniques to recover 3D models from visual data.	Ap	20%
CO5	Engage in independent learning to stay updated with advancements in image-based rendering and recognition, improving computer vision systems.	E	Internal Assessment

UNIT I - IMAGE PROCESSING FOUNDATIONS	**(9)**
Computer Vision - Geometric primitives and transformations - Photometric image formation – The digital camera - Point operators - Linear filtering - Neighborhood operators - Pyramids and wavelets - Geometric transformations - Global optimization.	
UNIT II - FEATURE DETECTION, MATCHING AND SEGMENTATION	**(9)**
Points and patches - Edges - Lines - Segmentation - Active contours - Split and merge - Mean shift and mode finding - Normalized cuts - Graph cuts and energy-based methods.	
UNIT III - FEATURE-BASED ALIGNMENT & MOTION ESTIMATION	**(9)**
2D and 3D feature-based alignment - Pose estimation - Geometric intrinsic calibration - Triangulation- Two-frame structure from motion - Factorization - Bundle adjustment - Constrained structure and123motion - Translational alignment - Parametric motion - Spline-based motion - Optical flow – Layered motion.	
UNIT IV - 3D RECONSTRUCTION	**(9)**
Shape from X - Active range finding - Surface representations - Point-based representations Volumetric representations - Model-based reconstruction - Recovering texture maps	

UNIT V- IMAGE-BASED RENDERING AND RECOGNITION	(9)
Interpolation Layered depth images - Light fields and Lumi graphs - Environment mattes -Video-based rendering-Object detection - Face recognition - Instance recognition – Category recognition - Context and scene understanding- Recognition databases and test sets.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. Richard Szeliski, Computer Vision: Algorithms and Applications, Springer- Texts in Computer Science, Second Edition, 2022
2. Computer Vision: A Modern Approach, D. A. Forsyth, J. Ponce, Pearson Education, Second Edition, 2015.

REFERENCES:

1. D. L. Baggio et al., Mastering Open CV with Practical Computer Vision Projectsll, Packet Publishing, 2012.
2. Simon J. D. Prince, —Computer Vision: Models, Learning, and Inferencell, Cambridge University Press, 2012

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3				2						1		2	
2		3		3	2									2
3			3		2				2					2
4	3		3								1		2	
5											2	3		1
CO (W.A)	3	3	3	3	2				2		1.3	3	2	1.6

22ECX36 - MACHINE LEARNING				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				

Course Objective:	• To understand the Machine Learning Concepts. • To obtain knowledge about reinforcement learning techniques and its applications • To get awareness Graphical Model and Ensemble methods

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply appropriate techniques for classification and regression.	Ap	20%
CO2	Analyze basic concepts of Machine Learning	An	30%
CO3	Evaluate and analyze various learning algorithms for the graphical model.	An	30%
CO4	Design and implement various unsupervised models.	E	20%
CO5	Implement the developments of various machine learning algorithms in real time applications and prepare a report for the same.	E	Internal Assessment

UNIT I – INTRODUCTION TO MACHINE LEARNING	**(9)**
Introduction-Types of Machine Learning – Supervised and unsupervised Learning– theory of generalization – generalization bound – approximation-generalization tradeoff – bias and variance – learning curve.	
UNIT II – SUPERVISED LEARNING	**(9)**
Linear regression- Bayesian regression- Regression with Basis functions- Logistic regression- Perceptrons- Large margin classification- Kernel methods- Support Vector Machines-hard SVM, soft SVM- Classification and Regression Trees, Radial Basis Functions.	
UNIT III - UNSUPERVISED LEARNING AND DIMENSIONALITY REDUCTION	**(9)**
Nearest neighbour models - K means - hierarchical clustering - Dimensionality reduction - principle component analysis - linear discriminant analysis- factor Analysis – Independent Component Analysis.	
UNIT IV - GRAPHICAL MODEL AND ENSEMBLE METHODS	**(9)**
Markov Chain Monte Carlo Methods – Sampling – Proposal Distribution-Bayesian Belief Networks-Markov Random Fields- Hidden Markov Models -Boosting - Adaboost, Gradient Boosting; Bagging - Simple methods, Random Forest.	
UNIT V- REINFORCEMENT LEARNING	**(9)**
Passive reinforcement learning – direct utility estimation – adaptive dynamic programming – temporal difference learning – active reinforcement learning – exploration – learning an action-utility function – Generalization in reinforcement learning – policy search – applications in Health care – applications in robot control.	
TOTAL (L:45)= 45 PERIODS	

TEXT BOOKS:
1. Ethem Alpaydin, 'Introduction to Machine Learning', 4th Edition, MIT Press, 2020.
REFERENCES:
1. Tom M Mitchell, 'Machine Learning', 1st Edition, McGraw Hill Education, 2017. 2. Peter Flach, 'Machine Learning: The art and science of algorithms that make sense of data', Cambridge University Press, 2012. 3. K. P. Murphy, 'Machine Learning: A probabilistic perspective', MIT Press, 2012. 4. Christopher M. Bishop, Pattern Recognition and Machine Learning, Springer, 2014. 5. Stephen Marsland, Machine Learning: An Algorithmic Perspective, 2nd Edition, 2014

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3											3	
3		3											3	
4			3										3	
5			3					2		2				
CO (W.A)	3	3	3					2		2			3	

22ECX37 - SOFT COMPUTING				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To understand Artificial Neural Network & Fuzzy Logic models. • To obtain knowledge about Hybrid Soft Computing techniques and its applications. • To get awareness genetic algorithms.

Course Outcomes The Student will be able to		Cognitive Level	Weightage of COs in End Semester Examination
CO1	Apply various soft computing frame works.	Ap	20%
CO2	Analyze various Neural Networks training algorithms.	An	30%
CO3	Develop systems using fuzzy logic.	E	30%
CO4	Evaluate and analyze various genetic algorithm and Hybrid Soft Computing techniques	E	20%
CO5	Give oral presentation as an individual or in groups in implementing the developments of various Computing algorithms.	U	Internal Assessment

UNIT I - ARTIFICIAL NEURAL NETWORK & FUZZY LOGIC	**(9)**
Artificial neural network: Introduction, characteristics- learning methods - taxonomy - Evolution of neural networks- basic models- important technologies - applications. Fuzzy logic: Introduction - crisp sets- fuzzy sets - crisp relations and fuzy relations: cartesian product of relation - classical relation, fuzzy relations, tolerance and equivalence relations, non-iterative fuzzy sets.	
UNIT II - NEURAL NETWORKS	**(9)**
McCulloch-Pitts neuron - linear separability - hebb network - supervised learning network: perceptron networks - adaptive linear neuron, multiple adaptive linear neuron, BPN, RBF, TDNN- associative memory network: auto-associative memory network, hetero-associative memory network, BAM, hopfield networks, iterative autoassociative memory network & iterative associative memory network - unsupervised learning networks: Kohonen self organizing feature maps, LVQ - CP networks, ART network.	
UNIT III - FUZZY SYSTEMS	**(9)**
Membership functions: features, fuzzification, methods of membership value assignments-Defuzzification: lambda cuts - methods- fuzzy arithmetic and fuzzy measures: fuzzy arithmetic - extension principle - fuzzy measures - measures of fuzziness -fuzzy integrals - fuzzy rule base and approximate reasoning : truth values and tables, fuzzy propositions, formation of rules-decomposition of rules, aggregation of fuzzy rules, fuzzy reasoning-fuzzy inference systems-overview of fuzzy expert system-fuzzy decision making.	
UNIT IV - GENETIC ALGORITHM	**(9)**
Genetic algorithm and search space - general genetic algorithm - operators - Generational cycle - stopping condition - constraints- classification - genetic programming - multilevel optimization - real life problem- advances in GA	

UNIT V- HYBRID SOFT COMPUTING TECHNIQUES & APPLICATIONS	(9)
Neuro-fuzzy hybrid systems - genetic neuro hybrid systems - genetic fuzzy hybrid and fuzzy genetic hybrid systems - simplified fuzzy ARTMAP - Applications: A fusion approach of multispectral images with SAR, optimization of traveling salesman problem using genetic algorithm approach, soft computing based hybrid fuzzy controllers	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. S.N.Sivanandam and S.N.Deepa, "Principles of Soft Computing", Wiley India Pvt Ltd, 2011.

REFERENCES:

1. J.S.R.Jang, C.T. Sun and E.Mizutani, "Neuro-Fuzzy and Soft Computing", PHI / Pearson Education 2004.
2. S.Rajasekaran and G.A.Vijayalakshmi Pai, "Neural Networks, Fuzzy Logic and Genetic Algorithm: Synthesis & Applications", Prentice-Hall of India Pvt. Ltd., 2006.
3. George J. Klir, Ute St. Clair, Bo Yuan, "Fuzzy Set Theory: Foundations and Applications" , Prentice Hall, 1997.
4. David E. Goldberg, "Genetic Algorithm in Search Optimization and Machine Learning" Pearson Education India, 2013.
5. Simon Haykin, "Neural Networks Comprehensive Foundation" Second Edition, Pearson Education, 2005.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3												
3			3										3	
4			3										3	
5						2			2			2	2	
CO (W.A)	3	3	3			2			2			2	2.6	

22ECX38 - PATTERN RECOGNITION				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To gain knowledge about pattern classification. • To understand use of supervised and unsupervised algorithm.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the clustering concepts in unsupervised learning and classification.	Ap	20%
CO2	Apply appropriate algorithms and techniques for analyzing structural patterns.	Ap	20%
CO3	Implement the concepts of pattern recognition and analyze the type of pattern given.	An	40%
CO4	Implement various feature extraction algorithms for different types of data.	C	20%
CO5	Explore advanced tools in pattern recognition through research projects, or case studies,	U	Internal Assessment

UNIT I – PATTERN CLASSIFIER	**(9)**
Overview of pattern recognition - Discriminant functions - Supervised learning - Parametric estimation - Maximum likelihood estimation - Bayesian parameter estimation - Perceptron algorithm - LMSE algorithm -Problems with Bayes approach - Pattern classification by distance functions - Minimum distance pattern classifier.	
UNIT II - UNSUPERVISED CLASSIFICATION	**(9)**
Clustering for unsupervised learning and classification - Clustering concept - C-means algorithm - Hierarchical clustering procedures - Graph theoretic approach to pattern clustering - Validity of clustering solutions.	
UNIT III-STRUCTURAL PATTERN RECOGNITION	**(9)**
Elements of formal grammars - String generation as pattern description - Recognition of syntactic description -Parsing - Stochastic grammars and applications	
UNIT IV - FEATURE EXTRACTION AND SELECTION	**(9)**
Entropy minimization - Karhunen - Loeve transformation - Feature selection through functions approximation -Binary feature selection.	

UNIT V- NON-METRIC METHODS FOR PATTERN CLASSIFICATION AND APPLICATIONS	(9)
Non-numeric data or nominal data. Decision trees: Classification and Regression Trees (CART), Applications: Face recognition - preprocessing, face detection algorithms, selection of representative patterns, classification algorithms.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. O.Duda, P.E.Hart and D.G.Stork, "Pattern Classification", John Wiley, 2009.
2. S.Theodoridis and K.Koutroumbas, "Pattern Recognition", 4th Edition, Academic Press, 2009.

REFERENCES:

1. C.M.Bishop, "Pattern Recognition and Machine Learning", Springer, 2006.
2. P.A Devijver and J. Kittler, "Pattern Recognition: A Statistical Approach", Prentice-Hall International, EnglewoodCliffs, NJ, 1980
3. K. Fukunaga, "Introduction to Statistical Pattern Recognition", 2nd Edition, Academic Press, New York, 1990.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	2													
2		2											1	
3			3										1	
4			3		3									
5									2					
CO (W.A)	2	2	3		3				2				1	

22ECX41 - CONTROL SYSTEMS				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To understand the concepts of mathematical models, transfer functions, block diagram reduction techniques, and signal flow graphs. • To provide adequate knowledge of systems in the time domain. • To accord basic knowledge in obtaining the open loop and closed loop frequency responses of systems. • To learn the concepts of stability analysis in the time domain.

Course Outcomes The Student will be able to		Cognitive Level	Weightage of COs in End Semester Examination
CO1	Apply the knowledge of the elements of control systems and their impact on system performance.	Ap	30%
CO2	Apply reduction techniques and, root locus method to simplify and analyze system stability	Ap	20%
CO3	Analyze the state equations, and interpret plot techniques for controllability and observability.	An	20%
CO4	Design controllers using various methods such as PID, lead-lag compensation, and state feedback.	E	20%
CO5	Give a presentation on a comprehensive understanding of control systems, incorporating recent technological advancements and practical applications	U	Internal Assessment

UNIT I - CONTROL SYSTEM MODELLING	**(9)**
Basic elements in control systems – Open and closed loop systems -Mathematical modelling of physical systems: Transfer function model of Mechanical and Electrical systems- Block diagram reduction techniques – Signal flow graphs.	
UNIT II - TIME RESPONSE ANALYSIS	**(9)**
Standard test signals - Type and order of systems -Time domain study of first and second order feedback control systems – Time domain specifications - Steady state errors - Error constants- Introduction to P, PI and PID Controllers.	
UNIT III - FREQUENCY RESPONSE ANALYSIS	**(9)**
Frequency response - Frequency domain specifications - Bode plot- Polar plot - Gain Margin - Phase Margin - Introduction to Compensators - Lead, Lag, and Lag- Lead Compensators.	
UNIT IV - STABILITY ANALYSIS	**(9)**
Concepts of stability - Location of roots on S-plane for stability - Necessary conditions for stability- Routh Hurwitz criterion-Root locus concept-Guidelines for sketching root locus-Nyquist stability criterion.	

UNIT V- CONTROL SYSTEM ANALYSIS USING STATE VARIABLE METHODS	(9)
State variable representation-Conversion of state variable models to transfer functions-Conversion of transfer functions to canonical state variable models-Solution of state equations-state transition matrix - Kalman test for Controllability and Observability.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. J. Nagrath& M. Gopal, "Control Systems Engineering", 6th Edition, New Age International Publishers, 2018.
2. M.Gopal, "Control Systems, Principles & Design", 4th Edition, Tata McGraw Hill, 2012.

REFERENCES:

1. 1. Norman S.Nise, "Control Systems Engineering", 8th Edition, Wiley, 2019.
2. K.Ogata, "Modern Control Engineering", 5th Edition, Pearson Education India,2015
3. Benjamin.C. Kuo, FaridGolnaraghi, "Automatic Control Systems", 10th Edition, McGraw-Hill Education, 2017.
4. S.K.Bhattacharya, "Control System Engineering", Pearson, 3rd Edition, 2013.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													2
2	3													2
3		3											2	2
4			3										2	2
5							2	1	2					1
CO (W.A)	3	3	3				2	1	2				2	2

22ECX42 - VIRTUAL INSTRUMENTATION				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To make students to gain knowledge on various traditional instrumentation and software for instrumentation. • To make the students to understand basic data acquisition systems. • To enable the student to acquire knowledge on IMAQ Vision. • To make the students to gain knowledge on real time control systems. • To motivate the students to acquire knowledge on Hardware & Operating systems.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply virtual instrumentation concepts using modular programming	Ap	20%
CO2	Apply A/D, D/A Converters with timers and counters for data acquisition system	Ap	20%
CO3	Apply PC hardware and operating system for virtual instrumentation	Ap	20%
CO4	Analyze the given images using different image processing tools	An	20%
CO5	Analyze the implementation methods for virtual instrumentation	An	20%

UNIT I - INTRODUCTION	**(9)**
Virtual Instrumentation- Comparison with Traditional Instrumentation - Definition and Flexibility - Architecture - software for Virtual Instrumentation - Modular Programming, Loop and Charts, Arrays, Clusters and Graphs, Case and Sequence Structures, Formula nodes, String and File Input / Output.	
UNIT II - DATA ACQUISITION	**(9)**
A/D and D/A converters, Plug-in Analog Input / Output cards – Digital Input and Output Cards, Organization – Performing analog input and analog output – Scanning multiple analog channels – Issues involved in selection of Data acquisition cards – Data acquisition modules with serial communication – Design of digital voltmeter with transducer input –Timers and Counters	
UNIT III - IMAQ VISION	**(9)**
Vision basics- Image processing and analysis, particle analysis – Machine vision, Hardware modules, Building machine vision system - Image processing tools, Acquisition and implementation using NI- Driver software- Applications.	
UNIT IV - REAL TIME CONTROL	**(9)**
Designs using VI Software – ON/OFF controller – Proportional controller – Modeling and basic control of level and reactor processes – Case studies on development of HMI, SCADA in VI.	

UNIT V- HARWARE & OPERATING SYSTEM OVERVIEW	(9)
PC architecture, operating system requirements, PC based instrumentation, analog and digital interfaces-PXI and SCXI main frame - modular instruments-Real time I/O and compact RIO-Introduction to NI-ELVIS – Transducers – power, speed and timing considerations.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Jovitha Jerome, "Virtual Instrumentation using LABVIEW", PHI Learning, New Delhi, 2010.
2. Gary W. Johnson and Richard Jennings, "LabVIEW Graphical Programming", 4th edition, McGraw-Hill Professional Publishing, 2011.

REFERENCES:

1. Barry Paton, "Sensor, transducers and Lab view", Prentice Hall of India 2000.
2. Buchanan, W. "Computer buses", CRC Press 2000.
3. Lisa K Wells, "Lab view for Everyone", Prentice Hall of India,1996.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3	3													2
4		3												
5		3	1										2	
CO (W.A)	3	3	1										2	2

22ECX43 - WEARABLE DEVICES				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To make students to gain knowledge on wearable systems and sensors. • To make students to signal processing and energy harvesting for wearable devices. • To enable the student to wireless health systems. • To make the students to Smart Textile. • To motivate the students to applications of wearable systems.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Develop skills in signal acquisition, processing, and analysis specific to wearable devices	Ap	20%
CO2	Apply the concept of reactive sensors employed for real life applications	Ap	20%
CO3	Design and implement wearable devices for health monitoring	Ap	20%
CO4	Analyze taxonomy of the wearable devices and its design constraints for measuring physical and biological signals.	An	20%
CO5	Analyze special purpose sensors and the need for developing smart sensors	An	20%

UNIT I - INTRODUCTION TO WEARABLE SYSTEMS AND SENSORS	**(9)**
Wearable Systems- Introduction, Need for Wearable Systems, Drawbacks of Conventional Systems for Wearable Monitoring, Applications of Wearable Systems, Types of Wearable Systems, Components of wearable Systems. Sensors for wearable systems-Inertia movement sensors, Respiration activity sensor, Impedance plethysmography, Wearable ground reaction force sensor.	
UNIT II - SIGNAL PROCESSING AND ENERGY HARVESTING FOR WEARABLE DEVICES	**(9)**
Wearability issues -physical shape and placement of sensor, Technical challenges - sensor design, signal acquisition, sampling frequency for reduced energy consumption, Rejection of irrelevant information. Power Requirements- Solar cell, Vibration based, Thermal based, Human body as a heat source for power generation, Hybrid thermoelectric photovoltaic energy harvests, Thermopiles.	
UNIT III - WIRELESS HEALTH SYSTEMS	**(9)**
Need for wireless monitoring, Definition of Body area network, BAN and Healthcare, Technical Challenges- System security and reliability, BAN Architecture – Introduction, Wireless communication Techniques.	
UNIT IV - SMART TEXTILE	**(9)**
Introduction to smart textile- Passive smart textile, active smart textile. Fabrication Techniques- Conductive Fibres, Treated Conductive Fibres, Conductive Fabrics, And Conductive Inks. Case study- smart fabric for monitoring biological parameters - ECG, respiration.	

UNIT V- APPLICATIONS OF WEARABLE SYSTEMS	(9)
Medical Diagnostics, Medical Monitoring-Patients with chronic disease, Hospital patients, Elderly patients, neural recording, Gait analysis, Sports Medicine.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Annalisa Bonfiglo and Danilo De Rossi, Wearable Monitoring Systems, Springer, 2011
2. Zhang and Yuan-Ting, Wearable Medical Sensors and Systems, Springer, 2013
3. Edward Sazonov and Micheal R Neuman, Wearable Sensors: Fundamentals, Implementation and Applications, Elsevier, 2014
4. Mehmet R. Yuce and JamilY.Khan, Wireless Body Area Networks Technology, Implementation applications, Pan Stanford Publishing Pvt. Ltd, Singapore, 2012

REFERENCES:

1. Sandeep K.S, Gupta, Tridib Mukherjee and Krishna Kumar Venkatasubramanian, Body Area Networks Safety, Security, and Sustainability, Cambridge University Press, 2013.
2. Guang-Zhong Yang, Body Sensor Networks, Springer, 2006
3. NPTEL Course "https://onlinecourses.nptel.ac.in/noc23_ee95/preview"

Mapping of COs with POs / PSOs

	POs												PSOs	
COs	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3	3		1											2
4		3												
5		3											3	
CO (W.A)	3	3	1										3	2

22ECX44 - REAL TIME EMBEDDED SYSTEMS				
	L	T	P	C
	3	0	0	3

PREREQUISITE : 22ECC13

Course Objective:	• To learn the architecture and programming of ARM processor. • To familiar with the embedded computing platform design and analysis. • To exposed to the basic concepts of real time Operating system. • To learn the system design techniques and networks for embedded systems. • To make the students to develop the real time solutions

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply knowledge of functional blocks in embedded system architecture.	Ap	20%
CO2	Apply instruction set and Assembly Language Programming in ARM Processors.	Ap	20%
CO3	Apply the concepts of embedded systems and explain concepts of real time Operating system design.	Ap	30%
CO4	Analyze architecture of different ARM processor cores.	An	20%
CO5	Develop and debug applications on an RTOS platform	E	10%

UNIT I - ARCHITECTURE OF EMBEDDED SYSTEMS	**(9)**
Categories of Embedded Systems- Characteristics of Embedded system -Recent trends in Embedded Systems Hardware Architecture - Software Architecture - Communication software - Process of generation of executable image development / testing tools	
UNIT II - THE ARM RISC ARCHITECTURE	**(9)**
The Reduced Instruction Set Computer –Embedded System Design Process - The ARM programmers model - ARM Development Tools.-ARM organization and implementation: 3 stage and 5 stage pipeline ARM organization-ARM instruction execution- ARM processor cores: ARM7 TDMI- Comparison of ARM8 TDMI-ARM9 TDMI.	
UNIT III - ARM INSTRUCTION AND ASSEMBLY LANGUAGE PROGRAMMING	**(9)**
Exceptions-Conditional execution-Branch and branch with link and exchange - Software interrupt-Data processing Instructions - Single word and unsigned byte data transfer and half word and signed byte data transfer instructions Multiple Register transfer instructions – Swap instructions – The thumb instruction set - Thumb applications.	
UNIT IV - RTOS CONCEPTS	**(9)**
Architecture of the Kernel-task and task scheduler-Interrupt Service Routines-Semaphores-Mutex-Mailboxes- Message Queues - Event Registers – Pipes -Signals-Timers- Memory Management – Priority Inversion Problem.	

UNIT V- RTOS IMPLEMENTATION	(9)
Off the shelf operating system - embedded operating system - Real time operating system: VX works- Micro C/OS-11 hand held operating system : Palm OS- Symbian OS - Case study of coding for an Automatic Chocolate Vending Machine using MUCOS -RTOS- Case study of an Embedded system for an Adaptive Cruise Control Systems in a Car- Case study of an Embedded Systems for a Smart Card.	
TOTAL (L:45) = 45 PERIODS	

TEXT BOOKS:

1. Dr.K.V.K.K Prasad "Embedded Real-Time systems: concept, design & programming", Dream tech Reprint, 2010.
2. Steve furber "ARM system on Chip Architecture", Pearson 16th Edition 2013.

REFERENCES:

1. Rajkamal, "Embedded Systems Architecture Programming and Design", 2nd edition TMH, 2010.
2. Wayne Wolf, "Computers as Components – Principles of Embedded Computer System Design", Morgon Kaufmann Publisher, 2nd Edition 2006.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3	3													3
4		3												
5									2			3	3	
CO (W.A)	3	3							2			3	3	3

22ECX45 - INTERNET OF THINGS & ITS APPLICATIONS				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To study the fundamentals of IoT, M2M and IoT Design Methodology • To learn about different IoT components and network management protocols, interfacing of IoT using Arduino/ Raspberry Pi • To study about various IoT case studies and industrial applications

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply IoT components and networks based on fundamental principles, incorporating various IoT protocols and communication models to facilitate IoT technologies.	Ap	20%
CO2	Analyze the necessity of software-defined networking (SDN) and network function virtualization (NFV) in the design methodology of IoT.	An	30%
CO3	Analyze the network operator requirements, communication modules for IoT network management, and the integration of NETCONF sensors and actuators.	An	30%
CO4	Design an IoT system using Arduino or Raspberry Pi platforms, employing Python for programming.	E	20%
CO5	Collaborate in team-based learning environments, effectively communicate concepts, and adopt continuous learning to develop foundational IoT applications.	U	Internal Assessment

UNIT I - FUNDAMENTALS OF IoT	**(9)**
Introduction-Definition and Characteristics of IoT- Physical design- IoT Protocols-Logical design - IoT communication models, IoT Communication APIs- Enabling technologies - Wireless Sensor Networks, Cloud Computing, Big data analytics, Communication protocols, Embedded Systems, IoT Levels and Templates	
UNIT II - M2M AND IoT DESIGN METHODOLOGY	**(9)**
IoT and M2M- difference between IoT and M2M - Software defined networks, network function virtualization– Needs IoT design methodology – SDN-NFV for IOT- software defined networking – Network function virtualization.	
UNIT III - IoT COMPONENTS AND NETWORKS	**(9)**
IoT System Management- Simple Network Management Protocol – Network operator requirement – NETCONF Sensors and actuators - Communication modules – Zigbee- Architecture – Zigbee and 802.15.4 – protocol layers – Introduction to RFIDs- Wi-Fi- Power sources.	

UNIT IV - BUILDING IoT WITH HARDWARE PLATFORMS	(9)
Logical Design using Python – Data types & structures – control flow – functions- modules - Platform - Arduino/Raspberry Pi- Physical devices - Interfaces - Programming – Serial- SPI – I2C	
UNIT V- CASE STUDIES	**(9)**
Various Real time applications of IoT- Home automation-Automatic lighting-Home intrusion detection-Cities-Smart parking-Environment-Weather monitoring system- Agriculture- Smart irrigation	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. David Hanes, Gonzalo Salgueiro, Patrick Grossetete, Rob Barton and Jerome Henry, "IoT Fundamentals: Networking Technologies, Protocols and Use Cases for Internet of Things", Cisco Press, 2017
2. Arshdeep Bahga, Vijay Madisetti, "Internet of Things – A hands-on approach", Universities Press, 2015
3. Rajkamal, "Internet of Things: Architecture, Design Principles And Applications", McGraw Hill Higher Education

REFERENCES:

1. Olivier Hersent, David Boswarthick, Omar Elloumi, "The Internet of Things – Key applications and Protocols", Wiley Publications 2012.
2. Olivier Hersent, David Boswarthick, Omar Elloumi, "The Internet of Things: Key applications and Protocols",Wiley Publications 2nd edition, 2013.
3. Manoel Carlos Ramon, "Intel Galileo and Intel Galileo Gen 2: API Features and Arduino Projects for LinuxProgrammers", Apress, 2014.
4. Marco Schwartz, "Internet of Things with the Arduino Yun", Packet Publishing, 2014.
5. Adrian McEwen, Hakim Cassimally, "Designing the Internet of Things", Wiley Publications, 2012.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3		3											2	2
4			3											
5									1	1		1	2	
CO (W.A)	3	3	3						1	1		1	2	2

22ECX46 - IOT WITH SINGLE BOARD COMPUTERS				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To describe the concepts of IoT along with its applications and various sensors • To Identify different technologies used in IoT and communication Protocols, Build a prototype using Arduino Uno and Raspberry Pi • To Design an IoT application to interact with Django.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply IoT fundamentals by deploying various microcontrollers in conjunction with sensors and actuators	Ap	20%
CO2	Analyze different IoT protocols and technologies suitable for implementing diverse applications.	An	30%
CO3	Analyze the various Arduino prototypes that integrate with interfacing devices.	An	30%
CO4	Design IoT physical devices and endpoints using Linux on Raspberry Pi, incorporating interfacing devices.	E	20%
CO5	Participate in team learning, effectively communicate, and commit to lifelong learning to develop basic Embedded Applications with Raspberry Pi and Arduino.	U	Internal Assessment

UNIT I - INTRODUCTION TO IOT	**(9)**
Microprocessor, Microcontroller, Embedded System, Definition of IoT, Characteristics of IoT, Physical design of IoT, Logical design of IoT, IoT Enabling Technologies, IoT levels & Deployment Templates, IoT Applications. Sensors and Actuators- Introduction, Sensor, Types of Sensors, Actuators, classification of Actuators	
UNIT II – IOT TECHNOLOGIES	**(9)**
Bluetooth, Bluetooth Low Energy (BLE), WiFi, LiFi, Cellular Networks, Z-Wave, ZigBee, LoRa WAN, 6LowPAN, LPWAN, RFID and NFC, WSN. COMMUNICATION PROTOCOLS: CoAP, MQTT, XMPP, HTTP	
UNIT III - IOT WITH ARDUINO	**(9)**
Introduction to the Arduino-Types of Arduino, Creating an Arduino program Using the Arduino IDE, Using Libraries, Working with Digital Interfaces, Interfacing with Analog devices, Adding Interrupts, Communicating with devices- sensors, DC Motor, Servo motor, LCD	
UNIT IV - IOT WITH RASPBERRY PI	**(9)**
IoT physical devices & endpoints: Architecture of Raspberry Pi, Linux on Raspberry Pi, Raspberry Pi Interfaces, Programming Raspberry Pi with Python, Controlling LED with Raspberry Pi, Interfacing an LED and Switch with Raspberry Pi, Interfacing a Light Sensor (LDR) with Raspberry Pi	

UNIT V- IOT PHYSICAL SERVERS & CLOUD OFFERINGS	(9)
Python Packages for IoT, WAMP - AutoBahn for IoT, Python Web Application Framework – Django, Amazon Web Services for IoT, SkyNet IoT messaging platform	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Vijay Madisetti and Arshdeep Bahga, "Internet of Things (A Hands-on-Approach)", 1st Edition, VPT, 2016.
2. Richard Blum, "Arduino Programming in 24 Hours, Sams Teach Yourself", Pearson Education, 2017.
3. Jain, Prof. Satish, Singh, Shashi," Internet of Things and its Applications", 1st Edition, BPB, 2020.

REFERENCES:

1. I.Donald Norris, "Internet of things do-it-yourself projects with Arduino, Raspberry Pi, and Beagle Bone Black", 1st Edition, McGraw-Hill,2015.
2. Adeal Javed Lake Zurich, Illinois, "Building Arduino Projects for the Internet: Experiments with Real-World Applications", 1st Edition, USA, A press, 2016.
3. Yashavant Kanetkar, Shrirang Korde, "21 IOT Experiments", 1st Edition, BPB Publications, 2018.
4. 4. Dr. Rajesh Singh, Dr. Anita Gehlot, Dr. Lovi Raj Gupta, Navjot Rathour, Mahendra Swain, Bhupendra Singh, "IoT based Projects Realization with Raspberry Pi, Node MCU and Arduino", 1st Edition, BPB Publications, 2020.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2		3											2	
3		3											2	2
4			3											
5									1	1		1	2	
CO (W.A)	3	3	3						1	1		1	2	2

22ECX47 - INDUSTRIAL IOT AND INDUSTRY 4.0				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To impart basic idea in Industry 4.0. and Cyber Physical System • To study about Big Data Analytics and Software Defined Networks, design and development of smart grid • To provide students with good depth of knowledge of designing Industrial 4.0 Systems for various application

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the foundational principles of Industrial Internet of Things (IIoT) and Industry 4.0 across diverse applications.	Ap	20%
CO2	Apply Cyber Physical Systems (CPS) and advanced sensors to strengthen the security of Augmented Reality (AR) and Virtual Reality (VR) environments.	Ap	30%
CO3	Analyze the utilization of machine learning, data science, and fog computing in IoT networks, focusing on R programming and data management with Hadoop	An	30%
CO4	Design and develop industrial IoT applications for smart grids, addressing their associated challenges.	E	20%
CO5	Engage in team-based learning, proficiently communicate ideas, and embrace lifelong learning to cultivate fundamental IoT applications tailored for diverse sectors such as the food industry, healthcare, power plants and quality control.	U	Internal Assessment

UNIT I - INTRODUCTION TO INDUSTRY 4.0	**(9)**
Introduction to Industry 4.0 -Historical Context, General framework- Sensing & actuation- Globalization and Emerging Issues, The Fourth Revolution- LEAN Production Systems,-Smart and Connected Business Perspective- Application areas, Dissemination of Industry 4.0, Artificial intelligence, Additive manufacturing, Robotization and automation, Current situation of Industry 4.0, Industry 5.0 Advances	
UNIT II - INDUSTRY 4.0 AND CYBER PHYSICAL SYSTEM	**(9)**
Introduction to Cyber Physical Systems (CPS) and Next Generation Sensors, Architecture of CPS-Components, Data science and technology for CPS, Emerging applications in CPS in different fields. Collaborative Platform and Product Lifecycle Management- Augmented Reality and Virtual Reality	
UNIT III - BIG DATA ANALYTICS AND SOFTWARE DEFINED NETWORKS	**(9)**
Introduction to Big Data Analytics and Software Defined Networks, Artificial Intelligence, Big Data and Advanced Analysis ,Introduction- Machine Learning and Data Science, R Programming, Data Management with Hadoop. Data Center Networks, Security and Fog Computing: Cloud Computing in IIoT	

UNIT IV - SMART GRID	(9)
Smart grid definition - Smart Grid development, Smart grid solutions, Design challenges of smart grid and Industry 4.0	
UNIT V- Industrial IoT- Smart applications	**(9)**
Understanding smart appliances, Smart operation, Smart monitoring and maintenance, Factories and Assembly Line, Food Industry. Healthcare, Power Plants, Inventory Management & Quality Control, Plant Safety and Security (Including AR and VR safety applications), Case study- Google's Self-Driving Car, Milk Processing and Packaging Industries	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Jean-Claude André, "Industry 4.0", Wiley- ISTE, July 2019, ISBN: 781786304827, 2019.
2. Diego Galar Pascual, Pasquale Daponte, Uday Kumar, "Handbook of Industry 4.0 and SMART Systems", Taylor and Francis, 2020.
3. S. Misra, C. Roy, and A. Mukherjee, "Introduction to Industrial Internet of Things and Industry 4.0", CRC Press.

REFERENCES:

1. S. Misra, A. Mukherjee, and A. Roy, 2020. Introduction to IoT. Cambridge University Press.
2. Pengwei Du and Ning Lu, —Energy storage for smart grids: planning and operation for renewable and variable energy resources VERs ll, Academic Press, 2018, Reprint edition.
3. Hossam A. Gabbar, —Smart Energy Grid Engineeringll, Academic Press, 2017.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3												2	
2	3												2	
3		3											2	2
4			3											
5									1	1		1	2	2
CO (W.A)	3	3	3						1	1		1	2	2

22ECX48 – AUTOMATION FOR ROBOTICS				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				

Course Objective:	• To make the students to understand the concept of robotics. • To facilitate the students to study about technologies applicable for robotics. • To know about different sensing devices of robot. • To study the algorithms applicable for robotics. • To encourage the students to develop 4-axis and 6-axis robot.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the concepts of motion and potential functions to model and control robot movements.	Ap	20%
CO2	Apply mobile robot navigation techniques to real-world scenarios and applications.	Ap	30%
CO3	Implement vision systems for pattern detection and processing, and integrate these systems into robotic applications.	Ap	30%
CO4	Analyze and enhance images by implementing edge detection algorithms and digital filtering techniques.	An	20%
CO5	Develop 4-axis and 6-axis robot for Various Applications.	C	Internal Assessment

UNIT I - INTRODUCTION TO ROBOTICS	**(9)**
Motion - Potential function -Representing Position and Orientation - Cell decomposition sensor and sensor planning - Kinematics - types- Transformation matrix - Inverse kinematics - Geometric methods and algebraic methods - Varying Pose –Applications.	
UNIT II - COMPUTER VISION	**(9)**
Optics, projection on the Image plane and radiometry - Image processing - Connectivity - Images - - Blob filling – Thresholding - Convolution - Digital convolution and filtering and Masking techniques - Edge detection - Mono and stereo vision - Face detection.	
UNIT III - MOBILE ROBOT VEHICLES	**(9)**
Introduction to various Mobile Robot Vehicles- Flying Robots - Navigation – Map-Based Planning - Dead Reckoning - Creating a Map - Rao-Blackwellized SLAM - Pose Graph SLAM - Carlo Localization – Applications.	
UNIT IV –TYPES OF ROBOTICS	**(9)**
Arm -Type Robots - Forward Kinematics -Inverse Kinematics - Jacobian Condition and Manipulability - Resolved-Rate Motion Control - Computing the Manipulator Jacobian Using Twists - Independent Joint Control - RigidBody Dynamics Compensation.	
UNIT V- INTEGRATION TO ROBOT	**(9)**
Building of 4 axis or 6 axis robot - Vision system for pattern detection - Sensors for obstacle detection - Decision making.	
	TOTAL (L:45) = 45 PERIODS

TEXT BOOKS:

1. Stuart Russell and Peter Norvig, "Artificial Intelligence-A Modern Approach", Pearson Education Series in Artificial Intelligence, London, 3rd Edition, 2016.
2. Robert Schilling and Craig, "Fundamentals of Robotics, Analysis and control", PHI, New Delhi, 3rd Edition, 2015.
3. Kevin M. Lynch and Frank C. Park, "Modern Robotics: Mechanics, Planning, and Control", Cambridge University Press, 2017.

REFERENCES:

1. S K Saha, Introduction To Robotics, 2nd Ed., McGraw-Hill, 2014
2. Forsyth and Ponce, "Computer Vision, A modern Approach", Pearson Education, London, 2nd Edition, 2011.
3. Mallot, "Computational Vision Information Processing in Perception and Visual Behavior", MIT Press, Cambridge, 2nd Edition, 2000.
4. Duda. Hart and Stork., "Pattern Recognition", Wiley-Inter science, UK, 2nd Edition, 2000.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3	3													
4		3											3	
5				3	2				1			1	1	
CO (W.A)	**3**	**3**		**3**	**2**				**1**			**1**	**2**	

MANAGEMENT ELECTIVES

22GEA02 - PRINCIPLES OF MANAGEMENT				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**
PREREQUISITE : NIL				

Course Objective:	• To provide with a foundational understanding of management concepts and practices. • To equip students with the knowledge and skills necessary to manage and lead organizations effectively, understanding both theoretical frameworks and practical applications in management. • To learn about various planning tools and decision-making processes crucial for organizational success. • To gain insights into human resource management functions. • To study effective communication strategies and the impact of information technology on communication and how effective control can lead to improved productivity and organizational performance.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply key management theories and practices to real-world business scenarios, demonstrating the ability to implement management functions.	Ap	20%
CO2	Analyze human resource management practices, evaluating how recruitment, training, performance appraisal, and employee relations contribute to organizational success.	An	30%
CO3	Evaluate strategic decisions and their impacts on organizational performance, the effectiveness of communication strategies and the use of information technology in facilitating efficient and effective communication within organizations.	E	30%
CO4	Create comprehensive strategic plans and organizational policies and design control systems to ensure continuous improvement in productivity and organizational performance.	C	20%
CO5	Engage in independent study as a member of a team and develop higher-order thinking skills that are crucial for effective management and leadership in complex organizational settings with assignments or case studies.	U	Internal Assessment

UNIT I -INTRODUCTION TO MANAGEMENT AND ORGANIZATIONS	**(9)**
Definition of Management - Science or Art - Manager Vs Entrepreneur - types of managers -managerial roles and skills - Evolution of Management - Scientific, human relations, system and contingency approaches - Types of Business organization-Organization culture and Environment - Current trends and issues in Management.	
UNIT II -PLANNING	**(9)**
Nature and purpose of planning - planning process - types of planning - objectives - setting objectives - policies - Planning premises - Strategic Management - Planning Tools and Techniques - Decision making steps and process.	

UNIT III -ORGANISING	(9)
Nature and purpose - Formal and informal organization - organization chart - organization structure - types - Line and staff authority - departmentalization -delegation of authority - centralization and decentralization - Job Design - Human Resource Management - HR Planning, Recruitment, selection, Training and Development, Performance Management, Career planning and management	
UNIT IV - DIRECTING	**(9)**
Foundations of individual and group behaviour - motivation -motivation theories - motivational techniques - job satisfaction - job enrichment - leadership - types and theories of leadership -communication - process of communication - barrier in communication - effective communication -communication and IT.	
UNIT V - CONTROLLING	**(9)**
System and process of controlling - budgetary and non-budgetary control techniques - use of computers and IT in Management control - Productivity problems and management - control and performance -direct and preventive control -reporting.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Harold Koontz, Heinz Weihrichand Mark V. Cannice, "Essentials of Management: An International, Innovation, and Leadership Perspective", 11th Edition, Tata McGraw-Hill Education, 2021.
2. J.A.F. Stoner, R.E. Freeman, and Daniel R. Gilbert "Management", 6th Edition, Pearson Education, 2018

REFERENCES:

1. JAF Stoner, Freeman R.E and Daniel R Gilbert "Management", 6th Edition, Pearson Education, 2004.
2. Robert Kreitner & Mamata Mohapatra, "Management", Biztantra, 2008.
3. Stephen A. Robbins & David A. Decenzo & Mary Coulter, "Fundamentals of Management", 7th Edition, Pearson Education, 2011.
4. Tripathy PC & Reddy PN, "Principles of Management", Tata Mcgraw Hill, 1999.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3										3			
2		3									3			
3										3				
4			3							3				
5											3	3		
CO (W.A)	3	3	3							3	3	3		

22GEA03 - TOTAL QUALITY MANAGEMENT				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To recognize the importance of quality councils and strategic planning in TQM. • To explore the elements and historical development of TQM. • To foster employee involvement through motivation, empowerment, teamwork, and recognition. • To implement continuous process improvement methods like Juran's Trilogy, PDSA Cycle, 5S, and Kaizen. • To Conduct quality audits and understand the introduction to other ISO standards like ISO 14000, IATF 16949, TL 9000, IEC 17025, ISO 18000, ISO 20000, ISO 22000, and ISO 21001.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Describe the elements and principles of Total Quality Management (TQM).	Ap	30%
CO2	Apply continuous process improvement methodologies such as Juran's Trilogy, PDSA Cycle, 5S, and Kaizen.	Ap	20%
CO3	Apply various quality tools and techniques in both manufacturing and service industry.	Ap	20%
CO4	Develop strong supplier partnerships and understand supplier selection, rating, and relationship development.	An	20%
CO5	Choose appropriate quality standards and implement them in the respective industry Applications.	E	10%

UNIT I - QUALITY CONCEPTS AND PRINCIPLES	**(9)**
Definition of Quality - Dimensions of Quality - Quality Planning - Quality Assurance and Control - Quality Costs with Case Studies - Elements / Principles of TQM - Historical Review – Leadership – Qualities / Habits - Quality Council - Quality Statements, Strategic Planning – Importance - Case Studies - Deming Philosophy - Barriers to TQM Implementation – Cases with TQM Success and Failures.	
UNIT II -TQM-PRINCIPLES AND STRATEGIES	**(9)**
Customer Satisfaction - Customer Perception of Quality - Customer Complaints - Customer Retention, Employee Involvement – Motivation - Empowerment - Teams - Recognition and Reward - Performance Appraisal, Continuous Process Improvement - Juran's Trilogy - PDSA Cycle - 5S - Kaizen, Supplier Partnership - Partnering - Sourcing - Supplier Selection - Supplier Rating - Relationship Development, Performance Measures – Purpose – Methods - Cases.	
UNIT III - CONTROL CHARTS FOR PROCESS CONTROL	**(9)**
Basic Seven Tools of Quality and its Role in Quality Control, Statistical Fundamentals - Measures of Central Tendency and Dispersion, Population and Sample - Normal Curve - Control Charts for Variables and Attributes - Process Capability - Case Study- Introduction to Six Sigma.	

UNIT IV - TQM-MODERN TOOLS	(9)
New Seven Tools of Quality, Benchmarking - Need - Types and Process, Quality Function Deployment - House of Quality (HOQ) Construction - Case Studies, Introduction to Taguchi's Robust Design - Quality Loss Function - Design of Experiments (DOE), Total Productive Maintenance (TPM) - Uptime Enhancement, Failure Mode and Effect Analysis (FMEA) - Risk Priority Number (RPN) – Process - Case Studies.	
UNIT V - QUALITY SYSTEMS	**(9)**
Need for ISO 9000 and Other Quality Systems - ISO 9000: 2015 Quality System – Elements - Implementation of Quality System - Documentation - Quality Auditing, Introduction to ISO 14000 - IATF 16949 - TL 9000-IEC 17025 - ISO 18000 - ISO20000 - ISO 22000 - ISO21001. Process of Implementing ISO - Barriers in ISO Implementation.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Besterfield Dale H., Besterfield Carol, Besterfield Glen H., Besterfield Mary, UrdhwareshеHemant, UrdhwareshеRashmi "Total Quality Management", 5th Edition, Pearson Education, Noida, 2018.

REFERENCES:

1. Subburaj Ramasamy, "Total Quality Management", McGraw Hill Education, New Delhi, 2017.
2. James R. Evans and William M. Lindsay, "The Management and Control of Quality", 8th Edition, Cengage Learning, 2012.
3. David Goetsch & Stanley Davis, "Quality Management for Organizational Excellence: Introduction to Total Quality", 8thEdition, Pearson, 2017.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3	3													
4		3												
5	3													
CO (W.A)	3	3												

22GEA04 - PROFESSIONAL ETHICS				
	L	**T**	**P**	**C**
	3	**0**	**0**	**3**

PREREQUISITE : NIL

Course Objective:	• To develop students' ability to identify, analyse, and resolve ethical dilemmas in engineering contexts, fostering a commitment to professional responsibility, integrity, and ethical decision-making. • To provide engineering students with a comprehensive understanding of ethical principles and practices in the engineering profession. • To familiarize students with key ethical theories, principles, and frameworks that guide ethical decision-making in professional practice. • To foster the ability to communicate ethical concerns and collaborate effectively with diverse stakeholders, including colleagues, clients, and the public. • To encourage students to uphold integrity, honesty, and accountability in their professional activities, fostering a culture of trust and reliability.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply ethical reasoning to evaluate and resolve these issues.	Ap	30%
CO2	Apply ethical principles and reasoning to analyze real-world case studies in engineering.	Ap	30%
CO3	Analyze the importance of ethics in professional practice.	An	20%
CO4	Develop the ability to make informed and ethical decisions in engineering practice.	An	10%
CO5	Recognize the importance of continuous learning and professional development in maintaining ethical standards.	E	10%

UNIT I - INTRODUCTION TO PROFESSIONAL ETHICS	**(9)**
Definition and Importance of Ethics, Ethical Theories and Principles, Ethics vs. Morals vs. Values, Role of Ethics in Engineering.	
UNIT II - PROFESSIONAL RESPONSIBILITY AND CODES OF CONDUCT	**(9)**
Professional Responsibility and Accountability, Codes of Conduct in Engineering (e.g., IEEE, NSPE), Conflicts of Interest and Whistle blowing, Case Studies.	
UNIT III - ETHICAL DECISION-MAKING AND PROBLEM-SOLVING	**(9)**
Ethical Decision-Making Models, Tools and Frameworks for Ethical Analysis, Resolving Ethical Dilemmas, Case Studies	
UNIT IV - LEGAL AND REGULATORY ASPECTS	**(9)**
Legal Frameworks Governing Engineering Practice, Intellectual Property Rights, Health, Safety, and Environmental Regulations, Case Studies.	

UNIT V: SOCIAL AND ENVIRONMENTAL RESPONSIBILITY	(9)
Social Responsibility of Engineers, Sustainable Engineering Practices, Impact of Engineering on Society and Environment, Case Studies.	
TOTAL(L:45) = 45 PERIODS	

TEXT BOOKS:

1. Charles E. Harris Jr., Michael S. Pritchard, and Michael J. Rabins,"Engineering Ethics: Concepts and Cases", 6th edition, 2018.
2. Mike W. Martin and Roland Schinzinger, "Ethics in Engineering", 5thEdition 2010.
3. M. Govindarajan, S. Natarajan, and V. S. SenthilKumar, "Professional Ethics and Human Values", Ist Edition 2006.

REFERENCES:

1. Stephen H. Unger, "Engineering Ethics: Real-World Case Studies"
2. Online Ethics Center for Engineering and Science - www.onlineethics.org
3. National Society of Professional Engineers (NSPE) - www.nspe.org

Mapping of COs with POs / PSOs

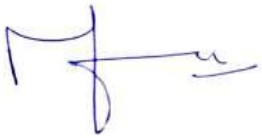
COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		3												
4		3												
5								3						
CO (W.A)	3	3						3						

22GEZ01-ENTREPRENEURSHIP DEVELOPMENT				
	L	T	P	C
	2	0	2	3
PRE REQUISITE : NIL				
Course Objective:	• Learn basic concepts in entrepreneurship, develop mind-set and skills necessary to explore entrepreneurship • Apply process of problem –opportunity identification and validation through human centred approach to design thinking in building solutions as part of engineering projects. • Analyze market types, conduct market estimation, identify customers, create customer persona, develop the skills to create a compelling value proposition and build a Minimum Viable Product. • Explore business models, create business plan, conduct financial analysis and feasibility analysis to assess the financial viability of a venture ideas & solutions built with domain expertise. • Prepare and present an investible pitch deck of their practice venture to attract stakeholders.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Analyze different types of entrepreneurs and their impact on emerging economies through case studies of successful and failed engineering entrepreneurs	An	20%
CO2	Apply concepts related to societal problems, generate and validate ideas, and assess business opportunities by studying emerging markets and their potential	Ap	20%
CO3	Develop prototypes using various methods and tools, understand their importance in the entrepreneurial process, and iterate based on feedback to enhance their designs	C	20%
CO4	Apply the Lean Canvas to develop business models and craft effective pitches that engage investors and customers	Ap	20%
CO5	Analyze the entrepreneurial ecosystem, including its components, financing models, and stakeholder networks through interactive activities such as visits and interactions with startup founders	Ap	20%

MODULE–I: ENTREPRENEURIAL MINDSET	(6+6)
Introduction to Entrepreneurship: Definition – Types of Entrepreneurs – Emerging Economics–Developing and Understanding an Entrepreneurial Mindset– Importance of Technology Entrepreneurship – Benefits to the Society. Case Analysis: Study cases of successful & failed engineering entrepreneurs - Foster Creative Thinking: Engage in a series of Problem-Identification and Problem-Solving tasks.	
MODULE– II: OPPORTUNITIES	(6+6)
ProblemsandOpportunities–IdeasandOpportunities–Identifyingproblemsinsociety– Creation of opportunities – Exploring Market Types – Estimating the Market Size, - Knowing the Customer and Consumer - Customer Segmentation - Identifying niche markets – Customer discovery and validation; Market research techniques, tools for validation of ideas and opportunities. Activity Session: Identify emerging sectors / potential opportunities in existing markets - Customer Interviews: Conduct preliminary interviews with potential customers for Opportunity Validation – Analyse feedback to refine the opportunity.	
MODULE–III: PROTOTYPING &ITERATION	(6+6)
Prototyping – Importance in entrepreneurial process – Types of Prototypes - Different methods – Tools & Techniques.Hands-on sessions on prototyping tools (3D printing, electronics, software), Develop a prototype based on identified opportunities; Receive feedback and iterate on the prototypes.	
MODULE– IV: BUSINESS MODELS &PITCHING	(6+6)
Business Model and Types - Lean Approach - 9 block Lean Canvas Model - Riskiest assumptions to Business Models – Using Business Model Canvas as a Tool – Pitching Techniques:Importanceofpitching-Typesofpitches-craftingacompellingpitch –pitch presentation skills - using storytelling to gain investor/customer attention. ActivitySession:Developabusinessmodelcanvasfortheprototype;presentandreceive feedback from peers and mentors - Prepare and practice pitching the business ideas- Participate in a Pitching Competition and present to a panel of judges - receive & reflect feedback.	
MODULE–V:ENTREPRENEURIAL ECOSYSTEM	(6+6)
Understanding the Entrepreneurial Ecosystem – Components: Angels, Venture Capitalists, Maker Spaces, Incubators, Accelerators, Investors. Financing models–equity, debt, crowd funding, etc, Support from the government and corporate. Navigating Ecosystem Support: Searching & Identifying the Right Ecosystem Partner – Leveraging the Ecosystem - Building the right stakeholder network. Activity Session: Arrangement of Guest Speaker Sessions by successful entrepreneurs and entrepreneurial ecosystem leaders (incubation managers; angels; etc), Visit one or two entrepreneurial ecosystem players (Travel and visit a research park or incubator or maker space or interact with startup founders).	
TOTAL(L:30,P:30) = 60 PERIODS	
TEXT BOOKS:	
1. Robert D. Hisrich, Michael P. Peters, Dean A. Shepherd, SabyasachiSinha (2020). Entrepreneurship, McGraw Hill, 11th Edition. 2. Ries,E.(2011).The Lean Startup: How Today's Entrepreneurs Use Continuous Innovation to Create Radically Successful Businesses. Crown Business.	
REFERENCES:	
1.Blank, S.G.,&Dorf,B.(2012).The Startup Owner's Manual: The Step-by-Step Guide for Building a Great Company. K&S Ranch. 2. Roy, R.(2017).Indian Entrepreneurship: Theory and Practice New Delhi: Oxford University Press. 3. Osterwalder,A.,&Pigneur, Y.(2010).Business Model Generation: A Handbook for Visionaries, Game Changers, and Challengers. John Wiley & Sons.	

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1		3							3	3		3		
2		3	3				2		3	3		3		
3			3		3				3	3		3		
4									3	3	3	3		
5									3	3	3	3		
CO (W.A)		3	3		3		2		3	3	3	3		



OPEN ELECTIVES

22ECZ01 – FUNDAMENTALS OF IoT				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• To Describe the concepts of IoT along with its applications and various sensors • To learn about different IoT components and Technologies used in IoT • To learn about interfacing of IoT using Arduino/ Raspberry Pi • To study about various IoT case studies and industrial applications			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply IoT fundamentals by deploying various microcontrollers in conjunction with sensors and actuators	Ap	20%
CO2	Analyze different IoT protocols and technologies suitable for implementing diverse applications.	An	30%
CO3	Analyze the various Arduino prototypes that integrate with interfacing devices.	An	30%
CO4	Design IoT physical devices and endpoints using Linux on Raspberry Pi, incorporating interfacing devices.	An	20%
CO5	Participate in team learning, effectively communicate, and commit to lifelong learning to develop basic Embedded Applications with Raspberry Pi and Arduino.	U	Internal Assessment

UNIT I - INTRODUCTION TO IoT	**(9)**
Introduction to IoT, Characteristics of IoT, Physical design of IoT, Logical design of IoT, IoT Enabling Technologies, IoT levels & Deployment Templates, IoT Applications. Sensors and Actuators- Introduction, Sensor, Types of Sensors, Actuators, classification of Actuators	
UNIT II - TECHNOLOGIES USED IN IoT	**(9)**
Bluetooth, Bluetooth Low Energy (BLE), WiFi, LiFi, Cellular Networks, Z-Wave, ZigBee, LoRaWAN, 6LowPAN, LPWAN, RFID and NFC, WSN. COMMUNICATION PROTOCOLS: CoAP, MQTT, XMPP, HTTP	
UNIT III - IOT WITH ARDUINO	**(9)**
Introduction to the Arduino-Types of Arduino, Creating an Arduino program Using the Arduino IDE, Using Libraries, Working with Digital Interfaces, Interfacing with Analog devices, Adding Interrupts, Communicating with devices- sensors, DC Motor, Servo motor, LCD	
UNIT IV - IoT WITH RASPBERRY PI	**(9)**
IoT physical devices & endpoints: Architecture of Raspberry Pi, Linux on Raspberry Pi, Raspberry Pi Interfaces, Programming Raspberry Pi with Python, Controlling LED with Raspberry Pi, Interfacing an LED and Switch with Raspberry Pi, Interfacing a Light Sensor (LDR) with Raspberry Pi	
UNIT V- CASE STUDIES	**(9)**

Various Real time applications of IoT- Home automation-Automatic lighting-Home intrusion detection-Cities-Smart parking-Environment-Weather monitoring system- Agriculture- Smart irrigation
TOTAL (L:45) : 45 PERIODS

TEXT BOOKS:

1. IoT Fundamentals: Networking Technologies, Protocols and Use Cases for Internet of Things, David Hanes, Gonzalo Salgueiro, Patrick Grossetete, Rob Barton and Jerome Henry, Cisco Press, 2017
2. Internet of Things – A hands-on approach, Arshdeep Bahga, Vijay Madisetti, Universities Press, 2015

REFERENCES:

1. Donald Norris, Internet of things_ do-it-yourself projects with Arduino, Raspberry Pi, and Beagle Bone Black, 1st Edition, McGraw-Hill,2015.
2. Adeal Javed Lake Zurich, Illinois, Building Arduino Projects for the Internet: Experiments with Real-World Applications, 1st Edition, USA, A press, 2016.
3. Marco Schwartz, Internet of Things with Arduino Cookbook, Packt publisher2016.
4. Dr. Rajesh Singh, Dr. Anita Gehlot, Dr. Lovi Raj Gupta, Navjot Rathour, Mahendra Swain, Bhupendra Singh, IoT based Projects Realization with Raspberry Pi, NodeMCU and Arduino, 1 st Edition, BPB Publications, 2020.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3												
3		3												
4		3			2									
5									1	1	1	1		
CO (W.A)	3	3	2	2	2				1	1	1	1		

22ECZ02 – SENSORS AND TRANSDUCERS				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• To understand the concepts of calibration, characteristics and response of transducers • To impart knowledge in the construction and characteristics of various electrical transducers • To familiarize about different transducers and sensors			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply knowledge of measurement standards, calibration techniques.	Ap	30%
CO2	Apply the change in electrical characteristics of elements with respect to change in physical parameters to be measured to construct the sensors.	Ap	30%
CO3	Apply the piezo electric and photo electric property of elements to construct conversion type transducers	Ap	30%
CO4	Analyze the possible errors in instruments, static and dynamic characteristics of measurement systems	An	10%
CO5	Select the suitable sensors for different application based on knowledge about their characteristics.	U	Internal Assessment

UNIT I - CHARACTERISTICS OF INSTRUMENTS	**(9)**
Units and Standards - Static calibration- Classification of errors -Error analysis -Limiting error -Probable error -Static Characteristics-Accuracy, Precision, Resolution, Sensitivity, Linearity, Hysteresis, Range and Span, Drift, Dead Zone- Dynamic characteristics and order of the systems.	
UNIT II - VARIABLE RESISTANCE TRANSDUCERS	**(9)**
Principles of operation - Construction details -Characteristics of resistance transducers -Resistance potentiometers -Strain gauges -Resistance thermometers - Thermistors- Hot wire anemometer -Piezo resistive sensor.	
UNIT III - VARIABLE INDUCTANCE TRANSDUCERS	**(9)**
Induction potentiometer -Variable reluctance transducers -Linear Variable Differential Transformer- LVDT Pressure transducer- Rotary Variable Differential Transformer-Eddy current transducers.	
UNIT IV - VARIABLE CAPACITIVE TRANSDUCERS	**(9)**
Variable air gap type - Variable area type - Variable permittivity type - Feedback type capacitance proximity pickup - Capacitor microphone.	
UNIT V - OTHER TRANSDUCERS	**(9)**
Piezoelectric transducer- Ultrasonic transducer, magnetostrictive transducer, fiber optic transducers, hall	

effect transducers, photoelectric transducers, and Digital transducer.
TOTAL (L:45) : 45 PERIODS

TEXT BOOKS:
1. A. K. Sawhney, Puneet Sawhney, A course in Electrical and Electronic Measurements and Instrumentation, Nineteenth edition Dhanpat Rai & Co (P) Ltd, 2012. 2. H.S.Kalsi, Electronic Instrumentation, Third Edition, Tata McGraw Hill Education Private Limited,2012. 3. D. Patranabis, Sensors and Transducers, 2nd Edition, Prentice Hall India Pvt. Ltd, 2009.
REFERENCES:
1. E.O.Doeblin, Measurement Systems: Applications and Design , 6th Edition, Tata McGraw-Hill BookCo., 2012. 2. D. V. S. Murthy, Transducers and Instrumentation, 2nd Edition, Prentice Hall of India Pvt. Ltd., NewDelhi, 2013.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3	3													
4		2												
5											2	3		
CO (W.A)	3	2									2	3		

22ECZ03 – PRINCIPLES OF COMMUNICATION					
		L	T	P	C
		3	0	0	3
PREREQUISITE : NIL					
Course Objective:	• To provide knowledge on complete analysis of Amplitude and Angle modulation schemes. • To discuss the performance of Pass band and base band communication.				

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply various concepts of theorems and Transforms for computing parameters of Communication systems.	Ap	30%
CO2	Apply concepts of modulation to design and analyze communication systems.	Ap	30%
CO3	Analyze the signal characteristics, system performance, and trade-offs of Analog and Digital modulation Techniques for a given set of parameters.	An	20%
CO4	Analyze the role of spread spectrum techniques in enhancing signal security, resistance to interference, and improving system performance in wireless communications.	An	20%
CO5	Engage in independent/team learning, communicate effectively and give oral presentation.	U	Internal Assessment

UNIT I - AMPLITUDE MODULATION	**(9)**
Functional block diagram of communication systems- Linear modulation schemes: Generation of AM: DSBFC using balanced modulator- DSBSC, SSBSC, Super heterodyne receivers.	
UNIT II - ANGLE MODULATION	**(9)**
Frequency modulation and Phase modulation, Relation between FM and PM waves, Narrowband FM, Wideband FM-FM demodulation: frequency discriminator-pre emphasis and de-emphasis in FM.	
UNIT III - PULSE MODULATION	**(9)**
Sampling process, Pulse Modulation – Pulse Amplitude Modulation, Pulse Position Modulation, Pulse Width Modulation - Quantization process- Pulse Code Modulation-Differential Pulse Code Modulation-Delta Modulation-Adaptive delta modulation.	
UNIT IV - BASEBAND TRANSMISSION	**(9)**
Matched Filter –Error rate due to noise –Inter-symbol Interference-Eye patterns - Nyquist criterion for distortion less base band Binary Transmission.	
UNIT V - PASSBAND DATA AND SPREAD SPECTRUM MODULATION	**(9)**

Pass band Transmission model-Generation, detection of Binary Modulation schemes (ASK, FSK, PSK). Spread Spectrum: PN sequence and its properties- Direct sequence spread spectrum-Frequency Hopping spread spectrum.
TOTAL (L:45) : 45 PERIODS

TEXT BOOKS:
1. 1. Simon Haykin, "Communications Systems", Wiley Education, 5th Edition, 2010. 2. T L Singal, "Analog & Digital Communications", Tata McGraw-Hill Education, 4th Edition, 2017.
REFERENCES:
1. Taub H and Schilling D L, "Principles of Communication Systems", McGraw Hill, 4th Edition, 2017. 2. Praokis J.G., "Digital Communications" 5th Edition, McGraw Hill, 2014. 3. Bernard Sklar, Pabitra Kumar Ray "Digital Communications: Fundamentals & Applications", Pearson Education, 2nd Edition, 2009.

Mapping of COs with POs / PSOs

COs	**POs**												**PSOs**	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2		3												
3		3												
4			3											
5									2	2	2	2		
CO (W.A)	3	3	2	2	2				2	2	2	2		

22ECZ04 – VLSI TECHNOLOGY					
		L	T	P	C
		3	0	0	3
PREREQUISITE : NIL					
Course Objective:	• To provide knowledge silicon wafer, plasma etching and IC fabrication technologies • To discuss the performance of MOS transistors				

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the fundamental fabrication principles of MOS transistors and predict the behavior of MOS devices under various conditions.	Ap	30%
CO2	Apply the fundamental principles and operation of MOS transistors to predict the behavior of MOS devices under various conditions.	Ap	30%
CO3	Analyze the impact of different fabrication technologies on the performance and reliability of MOS transistors.	An	20%
CO4	Analyze the physical design process and evaluate the performance of CMOS circuits.	An	20%
CO5	Engage independently or collaboratively deliver oral presentations on the VLSI system fabrication.	U	Internal Assessment

UNIT I - CRYSTAL GROWTH, WAFER PREPARATION, EPITAXY AND OXIDATION	(9)
Electronic Grade Silicon, Czochralski crystal growing, Silicon Shaping, processing consideration, Vapor phase Epitaxy, Molecular Beam Epitaxy, Silicon on Insulators, Thin Oxides, Oxidation Techniques Oxide properties, Oxidation of Poly Silicon.	
UNIT II – LITHOGRAPHY AND RELATIVE PLASMA ETCHING	(9)
Optical Lithography, Electron Lithography, X-Ray Lithography, Ion Lithography, Plasma properties, Feature Size control and Anisotropic Etch mechanism, relative Plasma Etching techniques .	
UNIT III - DEPOSITION, DIFFUSION, ION IMPLEMENTATION AND METALISATION	(9)
Deposition process, Polysilicon, plasma assisted Deposition, Models of Diffusion in Solids, – Atomic Diffusion Mechanism – Implant equipment. Annealing Shallow junction – High energy implantation – Physical vapor deposition, Patterning.	
UNIT IV – IC FABRICATION TECHNOLOGIES	(9)
P -Well process, N -Well process, Twin -tub process, Silicon on Insulator (Sol) process, Stick Diagram, Layout Diagram, Layout Design Rules.	
UNIT V - MOS TRANSISTOR THEORY	(9)

NMOS and PMOS transistors, CMOS logic, MOS transistor theory –Introduction, Enhancement mode transistor action, Depletion mode transistor action, Ideal I-V characteristics, Threshold voltage, Body effect, Second order effects.
TOTAL (L:45) : 45 PERIODS

TEXT BOOKS:
1. S.M.Sze, "VLSI Technology", Mc.Graw. Hill 2nd Edition. 2017. 2. Pucknell, "Basic VLSI Design", Prentice Hall of India Publication, 1995.
REFERENCES:
1. Neil H.E. "Weste and Kamran Eshraghian, Principles of CMOS VLSI Design", Pearson Education ASIA, 3rd edition, 2009. 2. Jan M. Rabaey, Anantha Chadrakasan, Borivoje Nikolic, "Digital Integrated Circuits: A Design Perspective", PHI, 2nd Edition, 2016.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		3												
4		3												
5									2	2	2	2		
CO (W.A)	3	3							2	2	2	2		

MINOR COURSES

22ECM01 –SEMICONDUCTOR PHYSICS				
	L	T	P	C
	3	0	0	3

PREREQUISITE : NIL

Course Objective:	• To know the basics of electronic states and energy band structure formation. • To know the importance of carrier concentration and doping in semiconductors.

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the theoretical concepts to real-world semiconductor device design and applications.	Ap	30%
CO2	Do band structure calculations and relate them to the electrical properties of real semiconductors	Ap	30%
CO3	Analyze the Carrier Properties in Semiconductors to determine the intrinsic and extrinsic Fermi levels.	An	20%
CO4	Analyze the behavior of p-n junctions, including homo- and hetero-junctions, and understand the functioning of MOS diodes and MOSFETs	An	20%
CO5	Effectively communicate, participate in team learning and commit to life-long learning	U	Internal Assessment

UNIT I – ELECTRONIC STATES	**(9)**
Crystal structures -reciprocal lattice – Brillouin zone and band representation. Dynamics of electrons in periodic potential: Kronig-Penny and nearly free electron models – band structure calculations -band structures of real semiconductors. Band gaps in semiconductors: Holes and effective mass concept – properties of conduction and valence bands.	
UNIT II - CARRIERS AND DOPING	**(9)**
Fermi distribution and energy – Density of states – valence and conduction band density of states – intrinsic concentration – intrinsic Fermi level – n and p type doping – density of carriers in extrinsic semiconductors and their temperature dependence – extrinsic semiconductor Fermi energy level – degenerate and non-degenerate semiconductors – band-gap engineering –electrons and holes in quantum wells and super lattices.	
UNIT III – ELECTRICAL TRANSPORT	**(9)**
Scattering in semiconductors – Velocity-electric field relations: Low field response; mobility and high field transport. Very high field transport: Breakdown phenomena – avalanche breakdown - Zener tunneling. Carrier transport by diffusion – transport by drift and diffusion: Einstein's relation. Charge injection and quasi-Fermi levels.	
UNIT IV – OPTICAL TRANSPORT	**(9)**
Electron – hole pair generation and recombination: band to band and intra band transitions, free – carrier and phonon transitions. Excitons: Origin, electronic levels and properties. Radiative recombination (Shockely – Read- Hall and Auguer) processes. Carrier transport: continuity equations. Optical constants: Kramers – Kronig relations – Electron-phonon interaction – Semiconductor laser.	
UNIT V - DEVICES	**(9)**

Processing of semiconductor devices: crystal growth, doping, deposition of dielectric films, lithography and metallization – p-n semiconductor junctions – homo and hetero junctions. MOS diode and MOSFET. Semiconductor quantum structures, density of states and excitons. Semiconductor photonic structures: 1D, 2D and 3D photonic crystals. Active and passive optoelectronic devices: photo processes.
TOTAL (L:45) : 45 PERIODS

TEXT BOOKS:
1. R.F.Pierret, "Semiconductor Device Fundamentals", Pearson, 2006. 2. D.Neamen and D.Biswa, "Semiconductor physics and devices", McGraw Hill Education, 2017.
REFERENCES:
1. N.Garcia, A. Damask and S.Schwarz "Physics for Computer Science Students", Springer Verlag, 2012. 2. Umesh Mishra and Jasprit Singh, "Semiconductor Device Physics and Design", Springer, 2008. 3. Nandita Dasgupta and Amitava Dasgupta, "Semiconductor Devices: Modelling and Technology", PHI Learning Pvt. Ltd., 2004

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		3												
4		3												
5									2	2	2	2		
CO (W.A)	3	3							2	2	2	2		

22ECM02 - SEMICONDUCTOR DEVICES				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• Introduce the fundamental principles and operational characteristics of semiconductor devices, including diodes, transistors, and special devices. • Develop an in-depth understanding of current conduction mechanisms and the behavior of PN junctions under various conditions.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply knowledge of semiconductor physics to solve real-world electronic circuit problems.	Ap	40%
CO2	Apply the hybrid-π and Ebers-Moll models to predict transistor behavior.	Ap	30%
CO3	Analyze basic circuits using semiconductor devices for various engineering applications.	An	20%
CO4	Evaluate the performance characteristics of different devices to select suitable components for specific tasks	An	10%
CO5	Effectively communicate, participate in team learning and commit to life-long learning	U	Internal Assessment

UNIT I - SEMICONDUCTOR DIODES	(9)
PN junction diode - Current equations - Diffusion and Drift Current Densities - Forward and Reverse bias characteristics - Switching Characteristics.	
UNIT II - BIPOLAR JUNCTION TRANSISTIORS	(9)
NPN and PNP – Junctions - Early effect - Current equations – Input and Output characteristics of CE, CB, CC Configurations - Transistor as an amplifier.	
UNIT III -FIELD EFFECT TRANSISTORS	(9)
JFET – Drain and Transfer Characteristics - Current equations - Pinch off voltage and its significance, MOSFET – Characteristics - Threshold voltage - Channel length modulation - D-MOSFET - E-MOSFET Current equation - FINFET - DUAL GATE MOSFET.	
UNIT IV - SPECIAL SEMICONDUCTOR DEVICES	(9)
Metal-Semiconductor Junction – MESFET – Schottky barrier diode - Zener diode - Varactor diode – Tunnel diode – PIN diode - LASER diode - LDR.	
UNIT V -POWER DEVICES AND DISPLAY DEVICES	(9)
UJT - SCR - Diac - Triac - Power BJT - Power MOSFET - DMOS – VMOS, LED – LCD - Photo transistor - OptoCoupler - Solar cell - CCD.	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:
1. R David A. Bell, —Electronic Devices and Circuits‖, Oxford University Press, Fifth Edition, (2008). 2. Jacob Millman & Christos C. Halkias, —Electronic Devices and Circuits‖, McGraw Hill, 2nd Edition, 2007. 3. D.Neamen and D.Biswa, —Semiconductor physics and devices‖, McGraw Hill Education, 2017.
REFERENCES:
1. S. Salivahanan, N. Suresh kumar and A. Vallavanraj, —Electronic Devices and Circuits‖, Tata McGraw Hill Third Edition (2013). 2. R.L. Boylestad & L. Nashelsky, —Electronic Devices and Circuit Theory‖, PHI Learning Private Limited, Ninth Edition, 2008.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		3												
4		2												
5									2	2	2	2		
CO (W.A)	3	2.5							2	2	2	2		

22ECM03 - SEMICONDUCTOR DEVICE MODELLING AND SIMULATION				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• Provide foundational knowledge of Si-based nano electronics and device scaling principles. • Develop a thorough understanding of semiconductor device physics, including PN junctions and transistors.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply semiconductor physics concepts such as crystal structures, doping mechanisms, and band theory to explain device performance at the nanoscale	Ap	40%
CO2	Apply computational and numerical techniques to model semiconductor behavior.	Ap	30%
CO3	Analyze the physical properties and behavior of semiconductor devices, including PN junctions, BJTs, and FETs, using advanced theoretical models	An	20%
CO4	Assess nanoelectronic devices for advanced applications, considering material properties, operational limits, and performance trade-offs.	An	10%
CO5	Effectively communicate, participate in team learning and commit to life-long learning	U	Internal Assessment

UNIT I – Si-BASED NANOELECTRONICS	(9)
Si-Based Nano electronics and Device Scaling, Nanoscale and Hetero structure Devices, Crystal structure- Unit cell and Miller Indices, Reciprocal Space, Doping, Band Structure, Effective Mass, Simulation of carrier concentration for semiconductors using Simulink.	
UNIT II - PN JUNCTION DIODE	(9)
Density of states, Electron Mobility, Semiconductor Statistics- Fermi-Dirac function and carrier concentration calculation, p-n junction under equilibrium, derivation of I-V relation, Minority carrier diffusion equation, Non-idealities in the p-n junction diode (Breakdown and Generation-Recombination currents) Simulation of MOSFET I-V relation using Simulink.	
UNIT III - BIPOLAR JUNCTION TRANSISTIORS	(9)
Transistor configurations, BJT- I-V relation and gain, Hybrid -π model - Ebers-Moll model, Non-idealities in BJT, Gummel Poon Model, HBT, BJT Transient and small signal behavior, Metal-Semiconductor contact (Schottky Barrier/Diode, Ohmic Contacts) and capacitance characteristics, Thermionic emission current flow and fermi-level pinning	
UNIT IV - FIELD EFFECT TRANSISTORS	(9)
Field Effect Transistors (JFET, MESFET, HEMT), MOS Band diagram and C-V characteristics, Threshold voltage and Interface charges, MOSFET I-V, gradual channel approximation and frequency response, non-idealities and CMOS	
UNIT V -SEMICLASSICAL TRANSPORT THEORY	(9)

Semiclassical Transport Theory -: Distribution Function, Boltzmann Transport Equation (BTE), Relaxation-Time Approximation (RTA), Scattering and Mobility. Drift-Diffusion Model Derivation and dielectric relaxation time, Taylor series expansion and Finite Difference method, Normalization, Scaling and Linearization of Poisson's Equation and Scharfetter–Gummel Discretization of the Continuity Equation
TOTAL (L:45) : 45 PERIODS

TEXT BOOKS:
1. R David A. Bell, —Electronic Devices and Circuits, Oxford University Press, Fifth Edition, (2008). 2. Jacob Millman & Christos C. Halkias, —Electronic Devices and Circuits, McGraw Hill, 2nd Edition, 2007.
REFERENCES:
1. S. Salivahanan, N. Suresh kumar and A. Vallavanraj, —Electronic Devices and Circuits, Tata McGraw Hill Third Edition (2013). 2. R.L. Oylestad & L. Nashelsky, —Electronic Devices and Circuit Theory, PHI Learning Private Limited, Ninth Edition, 2008.

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		2												
4		1												
5									2	2	2	2		
CO (W.A)	3	1.5							2	2	2	2		

22ECM04 – ANALOG AND DIGITAL ELECTRONICS				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• Introduce fundamental concepts of semiconductors and their applications in electronic devices. • Explain the operational principles of bipolar junction transistors (BJTs), field-effect transistors (FETs), and rectifier circuits. • Develop an in-depth understanding of operational amplifiers (op-amps) and their practical applications.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the behavior of intrinsic and extrinsic semiconductors to explain the operation of PN junction diodes, Zener diodes and transistors.	Ap	40%
CO2	Apply theoretical concepts to design and troubleshoot analog and digital electronic systems for real-world applications	Ap	30%
CO3	Analyze the DC and AC performance of operational amplifiers, including their configurations, feedback mechanisms, and applications in analog circuits.	An	20%
CO4	Analyze rectifier circuits and evaluate their performance in terms of efficiency, ripple factor, and voltage characteristics.	An	10%
CO5	Effectively communicate, participate in team learning and commit to life-long learning	U	Internal Assessment

UNIT I – INTRODUCTION	(9)
Semiconductors overview: intrinsic & extrinsic, energy band diagram - Mobility - Electrons and holes - The P-N junction diode - Zener diode - Avalanche effect- Rectifier Circuits Half wave, Full wave circuits, Efficiency, PIV, Ripple factor and AC and DC current and voltage in rectifier. PNP and NPN Bipolar junction Transistors - H parameters equivalent circuit – Common emitter amplifier - DC behavior: the load slope and the Q point - AC behavior - Emitter follower amplifier - Field effect transistors: JFET and MOSFET.	
UNIT II – OPERATIONAL AMPLIFIERS: DC PERFORMANCE	(9)
The operational amplifier - Input resistance, Output resistance, Open loop gain - Bias currents – Offset currents - Offset voltage - Differential mode gain - Common mode gain - Common mode rejection ratio - Negative feedback - Open loop gain and closed loop gain - Inverter amplifier - Non-inverter amplifier - The voltage follower - Differential amplifier. Adders, Subtractors, Comparator, Integrator and Differentiator.	
UNIT III – DIGITAL TECHNIQUES : COMBINATIONAL CIRCUITS	(9)
Numbering systems - Binary, octal and hexadecimal numbers - Boole algebra - Conversion and operations - AND gate- OR gate - Inverter - NAND gate - NOR gate - Exclusive OR gate. Morgans laws. Combinational Circuits: Truth tables, logic expressions, Logic simplification using K- map, half and full adder/subtractor, multiplexers, demultiplexers, Logic families: TTL and CMOS.	
UNIT IV – DIGITAL TECHNIQUES: SEQUENCIAL CIRCUITS	(9)

Gated Latches & Flip Flops- Level triggered and Edge triggered Flip-Flops, Flop (FF) types: RS type. JK FF. JK FF Master slave. D FF. T FF. Flip Flop Conversion. Shift registers, Counters. Memories Structure: address and data bus. ROM, PROM, EPROM and flash RAM. Volatiles Memories: RAM, SRAM, DRAM. Addressing modes.
UNIT V – DIGITAL TO ANALOG CONVERTERS AND ANALOG TO DIGITAL CONVERTERS (9)
DAC: Input latch. Binary Weighted Resistor Network. R-2R Ladder Resistor Network. Pulse Width Modulation. Resolution. Accuracy. Linearity. Zero Offset. Settling Time. Glitches. ADC: Sampling. Real time sampling and equivalent time sampling. Sampling theorem (Nyquist). Sampling and holding. Conversion.
TOTAL (L:45) : 45 PERIODS

TEXT BOOKS:
1. J L Robert Boylestead, Louis Nashelsky, Electronic Devices and Circuit Theory, Pearson Education, 2012. 2. M.Morris Mano, Michael D Ciletti, —Digital Design‖, 4th edition Pearson, 2011.
REFERENCES:
1. D.RoyChoudhry, Shail Jain, —Linear Integrated Circuits‖, New Age International Pvt. Ltd., 2000. 2. Thomas L.Floyd, —Digital Fundamentals‖, Prentice Hall, 11th Edition, 2015.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		2												
4		1												
5									2	2	2	2		
CO (W.A)	3	1.5							2	2	2	2		

22ECM05 – SEMICONDUCTOR OPTOELECTRONICS				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• Introduce fundamental concepts of light sources, wave nature of light, and semiconductor physics. • Explore the working mechanisms and performance characteristics of different optical detectors.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply quantum mechanical and solid-state physics concepts to understand semiconductor junction devices.	Ap	40%
CO2	Apply the analog and digital modulation techniques in optoelectronic devices, including electro-optic and magneto-optic modulators.	Ap	30%
CO3	Analyze the working principles and performance metrics of optical sources such as LEDs, plasma displays, and lasers.	An	20%
CO4	Analyze an integrated optoelectronic circuits, including hybrid and monolithic systems, for various photonic applications	An	10%
CO5	Effectively communicate, participate in team learning and commit to life-long learning	U	Internal Assessment

UNIT I – LIGHT SOURCES AND SEMICONDUCTOR PHYSICS	(9)
Wave nature of light, Polarization, Interference, Diffraction, Light Source, review of Quantum Mechanical concept, Review of Solid State Physics, Review of Semiconductor Physics and Semiconductor Junction Device.	
UNIT II – OPTICAL SOURCES	(9)
Introduction, Photo Luminescence, Cathode Luminescence, Electro Luminescence, Injection Luminescence, Injection Luminescence, LED, Plasma Display, Liquid Crystal Displays, Numeric Displays, Laser Emission, Absorption, Radiation, Population Inversion, Optical Feedback, Threshold condition, Laser Modes, Classes of Lasers, Mode Locking, laser applications.	
UNIT III – OPTICAL DETECTORS	(9)
Photo detector, Thermal detector, Photo Devices, Photo Conductors, Photo diodes, Detector Performance.	
UNIT IV – OPTOELECTRONIC MODULATING DEVICES	(9)
Introduction, Analog and Digital Modulation, Electro-optic modulators, Magneto Optic Devices, Acoustoptic devices, Optical, Switching and Logic Devices.	
UNIT V – INTEGRATED OPTOELECTRONIC CIRCUITS	(9)
Introduction, hybrid and Monolithic Integration, Application of Opto Electronic Integrated Circuits, Integrated transmitters and Receivers, Guided wave devices.	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:
1. J. Wilson and J.Haukes, —Opto Electronics – An Introduction, Prentice Hall of India Pvt. Ltd., New Delhi, 1998 2. Bhattacharya —Semiconductor Opto Electronic Devices, Prentice Hall of India Pvt., Ltd., New Delhi, 2017.
REFERENCES:
1. Jasprit Singh, —Opto Electronics – As Introduction to materials and devices, McGraw-Hill International Edition,1998

Mapping of COs with POs / PSOs														
COs	**POs**												**PSOs**	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		2												
4		1												
5									2	2	2	2		
CO (W.A)	3	1.5							2	2	2	2		

22ECM06 – MICRO ELECTRO MECHANICAL SYSTEMS				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• Introduce the fundamental concepts of micro-scale systems and the emerging trends in engineering and science related to MEMS. • Explain the structure, materials, and working principles of microelectromechanical systems.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the significance of micro-scale systems in modern engineering and science	Ap	30%
CO2	Apply knowledge of MEMS devices to solve real-world engineering problems, enhancing their practical and analytical skills	Ap	30%
CO3	Analyze various MEMS fabrication processes, including photolithography, ion implantation, and thin-film deposition techniques	An	20%
CO4	Analyze micro-actuators using various actuation principles, including thermal, piezoelectric, and electrostatic forces	An	20%
CO5	Effectively communicate, participate in team learning and commit to life-long learning	U	Internal Assessment

UNIT I – INTRODUCTION	(9)
New trends in Engineering and Science: Micro scale systems-Introduction to Design of MEMS, Overview of Micro electro mechanical Systems, Applications of Micro electro mechanical systems.	
UNIT II – BASICS OF MEMS	(9)
Micro electromechanical systems, devices and structures Definitions, Materials for MEMS: Silicon, silicon compounds, polymers, metals.	
UNIT III – MEMS FABRICATION TECHNOLOGIES	(9)
Microsystem fabrication processes: clean room standards, Semiconductor wafer cleaning, Photolithography, Ion Implantation, Diffusion and Oxidation. Thin film depositions: LPCVD, Sputtering, Evaporation, Electroplating; Etching techniques: Dry and wet etching, electrochemical etching; Micromachining: Bulk Micromachining.	
UNIT IV – MICRO SENSORS	(9)
Design of Acoustic wave sensors, resonant sensor, Vibratory gyroscope, Capacitive and Piezo Resistive Pressure sensors- engineering mechanics behind these Microsensors.	
UNIT V – MICRO ACTUATORS	(9)
Design of Actuators: Actuation using thermal forces, Actuation using shape memory Alloys, Actuation using piezoelectric crystals, Actuation using Electrostatic forces (Parallel plate, Torsion bar, Comb drive actuators), Micromechanical Motors and pumps.	

TOTAL (L:45) : 45 PERIODS

TEXT BOOKS:

1. J. Marc Madou, —Fundamentals of Microfabrication‖, CRC press 1997.
2. Stephen D. Senturia, Micro system Design, Kluwer Academic Publishers, 2001

REFERENCES:

1. Ran Hsu, "MEMS and Microsystems Design and Manufacture", Tata Mcraw Hill, 2002.
2. Chang Liu, "Foundations of MEMS", Pearson education India limited, 2006

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		2												
4		2												
5									2	2	2	2		
CO (W.A)	3	2							2	2	2	2		

22ECM07 –ELECTRONIC SYSTEM PACKAGING				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• Explore electromagnetic compatibility (EMC) and strategies to mitigate electromagnetic interference (EMI) in electronic systems. • Provide a comprehensive understanding of thermal design considerations and cooling techniques for electronic devices and systems.			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply principles of thermal management to electronic devices, using techniques such as heat sinks, heat pipes, and Peltier cooling.	Ap	40%
CO2	Apply concurrent engineering approaches and rapid prototyping to develop efficient and user-friendly electronic systems.	Ap	30%
CO3	Analyze the evolution, classification, and challenges of printed circuit board (PCB) design and manufacturing.	An	20%
CO4	Apply the impact of electromagnetic interference (EMI) and design rules to ensure electromagnetic compatibility (EMC)	An	10%
CO5	Effectively communicate, participate in team learning and commit to life-long learning	U	Internal Assessment

UNIT I – PACKAGING OF ELECTRONIC SYSTEMS	(9)
Electronic systems and needs. Physical integration of circuits, packages, boards and full electronic systems, Connectivity in Electronic equipment, Study of Electronic components and its packaging. Package classifications (Through hole and SMDs) and packaging trends. Standards of packaging, Packaging hierarchy of Electronic Products and Systems, Hierarchy of Interconnection Levels.	
UNIT II - MANUFACTURING AND DESIGN OF SECOND LEVEL (PCB) BOARDS AND FABRICATION METHOD	(9)
Evolutions of Printed Circuit Boards, Classification of Printed Circuit Boards(Single Sided PC Boards, Double Sided PC Boards, Multilayer PC Boards) ,Challenges in Modern PCB Design and Manufacture, Major Market Drivers for PCB Industry, PCB for Electronic Systems. PCB design considerations/ design rules for analog, digital and power applications.	
UNIT III – ELECTROMAGNETIC COMPATIBILITY	(9)
Electromagnetic interference in electronic systems and its impact, Analysis of electronic circuit from noise emission point of view (both conducted and radiated emission) cross talk and reflection. Design rules to overcome EMI.	
UNIT IV – THERMAL DESIGN OF CHIPS AND BOARDS	(9)
Thermal management of electronic devices and systems, Overview. Thermal interface material. Heat density in electronic components, Heat transfer through conduction, convection and radiation, Heat sinks, Principle, Construction and materials. Performance, Method of cooling, Heat pipes, Peltier cooling plates. Recent developments, Application in Electronics Systems, Personal Computers, Batteries and Soldering.	

UNIT V - INDUSTRIAL DESIGN OF ELECTRONIC PRODUCTS	(9)
Fundamentals of Industrial Design, Industrial Design Process - Investigation of customer needs, Conceptualization, Preliminary refinement, Further refinement and final concept selection, Ergonomics, Aesthetics-Colour, Form, Type, Concurrent Engineering, Physical Design of Packaging Standards, Materials, Manufacturing, Rapid Prototyping.	
TOTAL (L:45) : 45 PERIODS	

TEXT BOOKS:

1. Rao R. Tummala, "Fundamentals of Microsystems Packaging", McGraw Hill, NY, 2001.

REFERENCES:

1. William D. Brown, "Advanced Electronic Packaging", IEEE Press, 1999.

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		2												
4		1												
5									2	2	2	2		
CO (W.A)	3	1.5							2	2	2	2		

22ECM08 – SYSTEM ON A CHIP DESIGN				
	L	T	P	C
	3	0	0	3
PREREQUISITE : NIL				
Course Objective:	• Introduce the fundamental concepts, driving forces, and applications of System-on-Chip (SoC) technology • Provide practical knowledge of SoC implementation using modern tools and real-time operating systems (RTOS)			

Course Outcomes The Student will be able to		**Cognitive Level**	**Weightage of COs in End Semester Examination**
CO1	Apply the key components, design flow, and trade-offs in SoC development.	Ap	40%
CO2	Apply testing methods and automation tools to ensure the reliability and functionality of SoC designs	Ap	30%
CO3	Analyze processor architectures, including RISC, CISC, VLIW, and vector processors, for various applications.	An	20%
CO4	Evaluate different system-level interconnection topologies and protocols, such as AMBA, Wishbone, and Avalon	An	10%
CO5	Effectively communicate, participate in team learning and commit to life-long learning	U	Internal Assessment

UNIT I – SOC INTRODUCTION	(9)
Driving Forces for SoC- Components - Generic template- Design flow- Hardware/Software nature, Design Trade-OffsMajor Applications-SYSTEM-LEVEL DESIGN: Processor selection-Concepts in Processor Architecture: Instruction set architecture (ISA) -Robust processors: Vector processor, VLIW, Superscalar, CISC, RISC—Processor evolution: Soft and Firm processors, Custom-Designed processors-IP based design-on-chip memory.	
UNIT II - SYSTEM-LEVEL INTERCONNECTION	(9)
On-chip Buses: basic architecture, topologies, arbitration and protocols, Bus standards: AMBA, Core Connect, Wishbone, Avalon-Network-on-chip: Architecture-topologies-switching strategies- routing algorithms-flow control, quality-of-service-Re-configurability in communication architectures.	
UNIT III – CO-DESIGN CONCEPTS	(9)
Nature of hardware & software- quest for energy efficiency- driving factors for hardware- software codesign-Codesign space-Dualism of Hardware design and Software design-Modeling Abstraction Level-Concurrency and Parallelism Hardware Software tradeoffs- Introducing Dataflow modelling.	
UNIT IV – SOC IMPLEMENTATION	(9)
Study of Microblaze RISC processor - Real-time operating system (RTOS), peripheral interface and components, High density FPGAs-Introduction to tools used for SOC design: Xilinx SoC based development kit.	
UNIT V - SOC TESTING	(9)
Manufacturing test of SoC: Core layer, system layer, application layer-P1500 Wrapper Standardization-SoC Test Automation (STAT).	

TOTAL (L:45) : 45 PERIODS

TEXT BOOKS:
1. Michael J.Flynn, Wayne Luk , "Computer system Design: System-on-Chip", Wiley-India, 2012. 2. Sudeep Pasricha, Nikil Dutt , "On Chip Communication Architectures: System on Chip Interconnect", Morghan Kaufmann Publishers, 2008.
REFERENCES:
1. W.H.Wolf , "Computers as Components: Principles of Embedded Computing System Design", Elsevier, 2008. 2. Patrick Schaumont , "A Practical Introduction to Hardware/Software Co-design", 2nd Edition, Springer, 2012

Mapping of COs with POs / PSOs

COs	POs												PSOs	
	1	2	3	4	5	6	7	8	9	10	11	12	1	2
1	3													
2	3													
3		2												
4		1												
5									2	2	2	2		
CO (W.A)	3	1.5							2	2	2	2		